élantec
HIGH PERFORMANCE ANALOG INTEGRATED CIRCUITS

EL2039/EL2040

Very High Slew Rate Wideband Operational Amplifier

ELANTEC INC T-79-07-10

Features

- Low offset voltage—0.5 mV typ., 2 mV max
- Low supply current—13 mA typ., 17 mA max
- High slew rate—
  EL2039—600 V/μs
  EL2040—400 V/μs
- Large open loop gain—15 kV/V (83 dB)
- Wide gain-bandwidth—
  EL2039—600 MHz
  EL2040—400 MHz
- High power bandwidth—
  EL2039—9.5 MHz
  EL2040—6.3 MHz
- Output voltage swing—±11V
- MIL-STD-883 Rev. C compliant
- Improved replacements for HA2539 and HA2540

Applications

- Pulse and video amplifiers
- Wideband amplifiers
- High speed sample-hold circuits
- Local area networks

Ordering Information

Part No.	Temp. Range	Package	Outline #
EL2039CJ	0°C to +75°C	14-Pin CerDIP	MDP0014
EL2039CN	0°C to +75°C	14-Pin P-DIP	MDP0031
EL2039J	−55°C to +125°C	14-Pin CerDIP	MDP0014
EL2039J/883B	−55°C to +125°C	14-Pin CerDIP	MDP0014
EL2039L/883B	−55°C to +125°C	20-Pad LCC	MDP0007
EL2040CJ	0°C to +75°C	14-Pin CerDIP	MDP0014
EL2040CN	0°C to +75°C	14-Pin P-DIP	MDP0031
EL2040J	−55°C to +125°C	14-Pin CerDIP	MDP0014
EL2040J/883B	−55°C to +125°C	14-Pin CerDIP	MDP0014
EL2040L/883B	−55°C to +125°C	20-Pad LCC	MDP0007

5962-8964802 is the SMD version of this device.

General Description

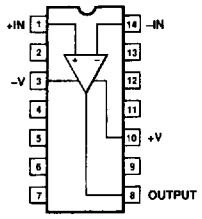
The EL2039 and EL2040 monolithic operational amplifiers are pin compatible with the HA2539 and HA2540, but have patented circuitry for improved dynamic performance and DC accuracy, and a typical power reduction of 35%. Additionally, these Elantec amplifiers are stable when driving capacitive loads and are well behaved when the output is overdriven. Both devices are compensated for closed loop gains ≥ 10. The EL2039 is the fastest of the series with a 600 V/μs slew rate and 600 MHz gain-bandwidth product. The EL2040 has a 400 V/μs slew rate and 400 MHz gain-bandwidth product. The EL2039 and EL2040 are fabricated with Elantec's Complementary Bipolar process and are zener zap trimmed for low offset voltage.

Elantec's high speed amplifiers are widely used in military, video and medical applications. They are especially suited for high speed video amplifiers, pulse detectors, and wide bandwidth filters.

Elantec's EL2039/883B and EL2040/883B comply with MIL-STD-883 Revision C in all aspects, including burn-in at 125°C. Elantec's facilities comply with MIL-I-45208A and other applicable quality specifications. For information on Elantec's military processing, see the Elantec document, QRA-2: *Elantec's Military Processing—Monolithic Products.*

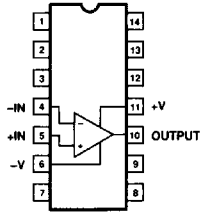
Connection Diagrams

EL2039 DIP



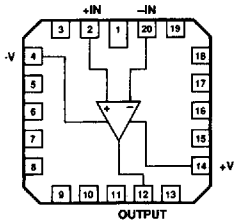
Top View

EL2040 DIP



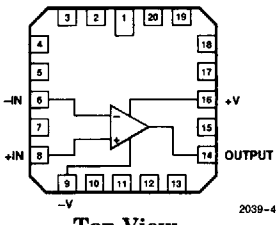
Top View

EL2039 LCC



Top View

EL2040 LCC



Top View

Note: Non-designated pins are no connects and are not electrically connected internally.

Manufactured under U.S. Patent No. 4,837,523

ELANTEC INC

EL2039/EL2040

Very High Slew Rate Wideband Operational Amplifier

Absolute Maximum Ratings (T_A = 25°C)

V_S	Voltage between V+ and V−	35V
V_{DIFF}	Differential Input Voltage	6V
I_{OP}	Output Current, Peak	50 mA
I_{OC}	Output Current, Continuous	25 mA
P_D	Internal Power Dissipation	See Curves
T_A	Operating Temperature Range	
	EL2039, EL2040	−55°C to +125°C
	EL2039C, EL2040C	0°C to +75°C
T_J	Operating Junction Temperature	
	CerDIP, Ceramic LCC	175°C
	Plastic DIP	150°C
T_{ST}	Storage Temperature	−65°C to +150°C
T_{LT}	Lead Temperature	
	(Soldering, 5 seconds)	300°C

Important Note:
All parameters having Min/Max specifications are guaranteed. The Test Level column indicates the specific device testing actually performed during production and Quality inspection. Elantec performs most electrical tests using modern high-speed automatic test equipment, specifically the LTX77 Series system. Unless otherwise noted, all tests are pulsed tests, therefore $T_J = T_C = T_A$.

Test Level	Test Procedure
I	100% production tested and QA sample tested per QA test plan QCX0002.
II	100% production tested at T_A = 25°C and QA sample tested at T_A = 25°C, T_{MAX} and T_{MIN} per QA test plan QCX0002.
III	QA sample tested per QA test plan QCX0002.
IV	Parameter is guaranteed (but not tested) by Design and Characterization Data.
V	Parameter is typical value at T_A = 25°C for information purposes only.

DC Electrical Characteristics V_S = ±15V; R_L = 1 kΩ, unless otherwise specified

Parameter	Description	Temp	Min	Typ	Max	EL2039 EL2040 Test Level	EL2039C EL2040C Test Level	Units
V_{OS}	Input Offset Voltage	25°C		0.5	2	I	I	mV
		Full			6	I	III	mV
TCV_{OS}	Average Offset Voltage Drift	Full		20		V	V	µV/°C
I_B	Bias Current	25°C		5	15	I	I	µA
		Full			20	I	III	µA
I_{OS}	Offset Current	25°C		1	4	I	I	µA
		Full			6	I	III	µA
R_{IN}	Input Resistance	25°C		10		V	V	kΩ
C_{IN}	Input Capacitance	25°C		1		V	V	pF
V_{CM}	Common Mode Range	Full	±11	±12		I	II	V
e_{IN}	Input Noise Voltage (f = 1 kHz, R_G = 0Ω)	25°C		6		V	V	nV/√Hz
A_{VOL}	Large Signal Voltage Gain (Note 1)	25°C	10k	15k		I	I	V/V
		Full	5k			I	III	V/V
CMRR	Common-Mode Rejection Ratio (Note 2)	Full	60	90		I	II	dB
V_O	Output Voltage Swing	Full	±11	±12		I	II	V
I_O	Output Current (Note 11)	Full	±25	±50		I	II	mA
R_O	Output Resistance	25°C		30		V	V	Ω
I_S	Supply Current	Full		13	17	I	II	mA
PSRR	Power-Supply Rejection Ratio (Note 7)	Full	60	85		I	II	dB

EL2039/EL2040

ELANTEC INC

Very High Slew Rate Wideband Operational Amplifier

AC Electrical Characteristics—EL2039

Parameter	Description	Temp	Min	Typ	Max	EL2039 Test Level	EL2039C Test Level	Units
GBW	Gain-Bandwidth Product (Notes 3, 4)	25°C		600		V	V	MHz
FPBW	Full-Power Bandwidth (Notes 1, 5, 8)	25°C	8.7	9.5		I	I	MHz
t_r	Rise Time (Note 6)	25°C		4		V	V	ns
OS	Overshoot (Note 6)	25°C		35		V	V	%
SR	Slew Rate (Note 6)	25°C	550	600		I	I	V/μs
ts	Settling Time (Note 6) 10V Step to 0.1%	25°C		100		V	V	ns

AC Electrical Characteristics—EL2040

Parameter	Description	Temp	Min	Typ	Max	EL2040 Test Level	EL2040C Test Level	Units
GBW	Gain-Bandwidth Product (Notes 3, 4)	25°C		400		V	V	MHz
FPBW	Full-Power Bandwidth (Notes 1, 5, 8)	25°C	5.5	6		I	I	MHz
t_r	Rise Time (Note 6)	25°C		5		V	V	ns
OS	Overshoot (Note 6)	25°C		15		V	V	%
SR	Slew Rate (Note 6)	25°C	350	400		I	I	V/μs
t_s	Settling Time (Notes 9, 10) 10V Step to 0.1%	25°C		70		V	V	ns

Note 1: $V_O = \pm 10V$.

Note 2: Two tests are performed, $V_{CM} = 0V$ to $+10V$ and $V_{CM} = 0V$ to $-10V$.

Note 3: $V_O = 90$ mV.

Note 4: $A_V = 10$.

Note 5: Full Power Bandwidth guaranteed based on slew rate measurement using: $FPBW = \frac{\text{Slew Rate}}{2\pi V_{peak}}$.

Note 6: Refer to Test Circuits section of data sheet.

Note 7: Two tests are performed. $V+ = +15V$, and $V-$ is changed from $-5V$ to $-15V$. $V- = -15V$, and $V+$ is changed from $+5V$ to $+15V$.

Note 8: $R_L = 1\ k\Omega$.

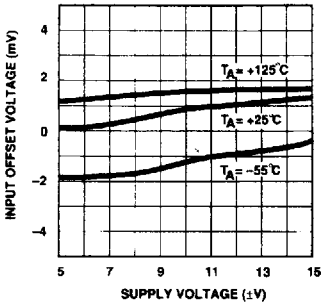
Note 9: Settling time measurements are made with techniques in the following reference: "Take The Guesswork Out of Settling-Time Measurements," EDN, September 19, 1985.

Note 10: $A_V = -10$, $R_L = 1k$.

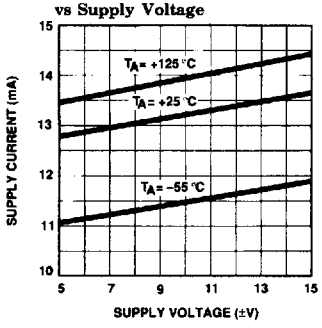
Note 11: $R_L = 200\Omega$.

EL2039/EL2040 Typical DC Performance Curves

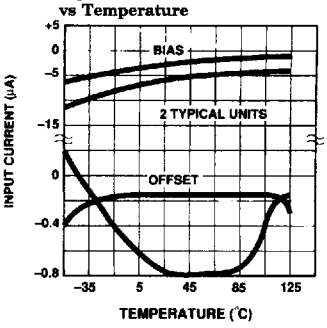
Input Offset Voltage vs Supply Voltage



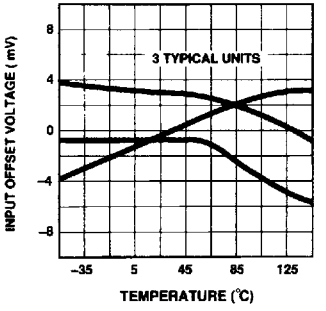
Supply Current vs Supply Voltage



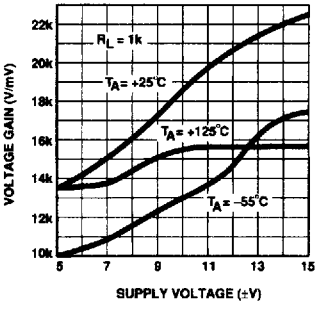
Input Currents vs Temperature



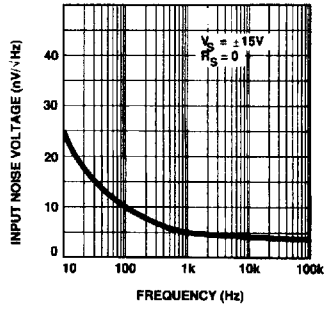
Input Offset Voltage vs Temperature



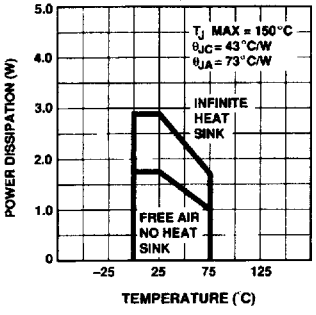
Voltage Gain vs Supply Voltage



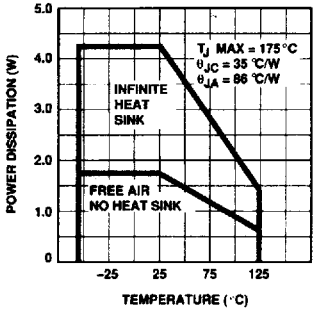
Input Noise Voltage vs Frequency



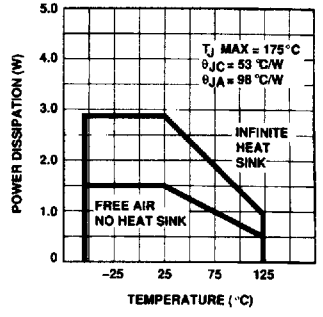
14-Lead Plastic DIP Maximum Power Dissipation vs Ambient Temperature



14-Lead CerDIP Maximum Power Dissipation vs Ambient Temperature



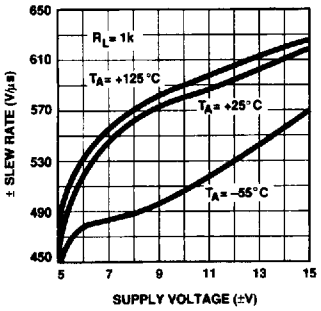
20-Lead LCC Maximum Power Dissipation vs Ambient Temperature



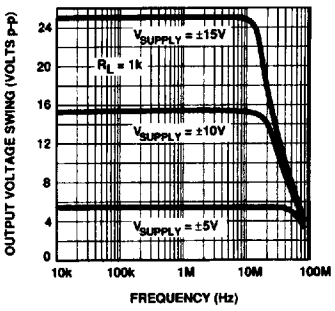
2039-5

EL2039 Typical AC Performance Curves

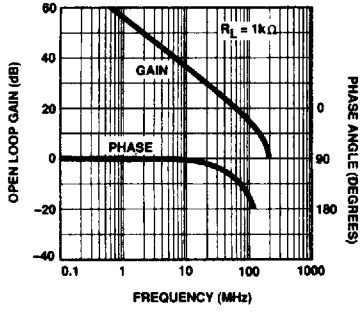
Slew Rate vs Supply Voltage



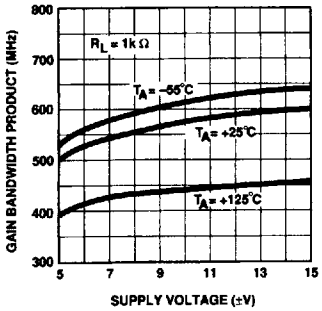
Output Voltage Swing vs Frequency



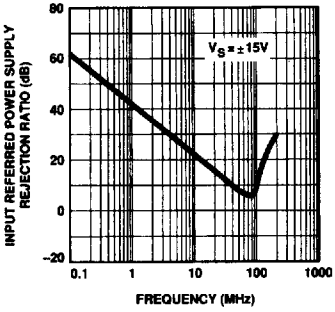
Open Loop Voltage Gain vs Frequency



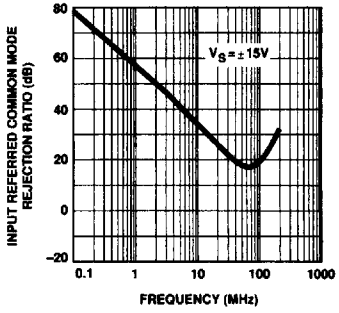
Gain Bandwidth Product at 10 MHz vs Supply Voltage



Input Referred Power Supply Rejection Ratio vs Frequency

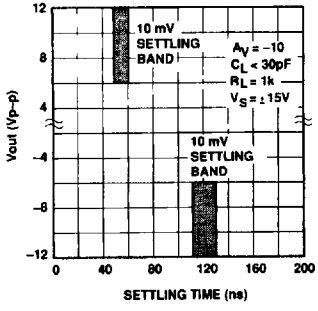


Input Referred Common Mode Rejection Ratio vs Frequency



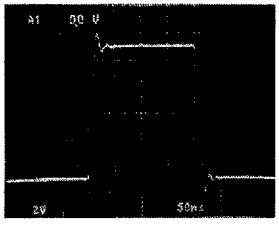
2039-6

Settling Time



2039-7

Large Signal Response



V_{IN} = ±0.5V
V_O = ±5V

2039-8

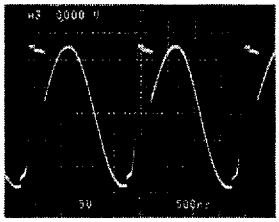
Small Signal Response

V_{IN} = ±10 mV
V_O = ±100 mV

2039-9

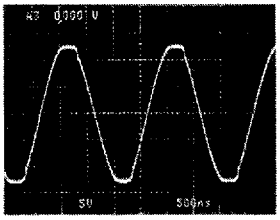
EL2039 Typical AC Performance Curves — Contd.

HA2539 at Onset of Clipping



2039-10

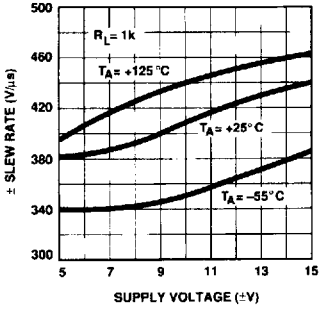
EL2039 at Onset of Clipping



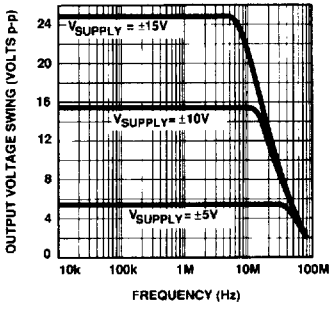
2039-11

EL2040 Typical AC Performance Curves

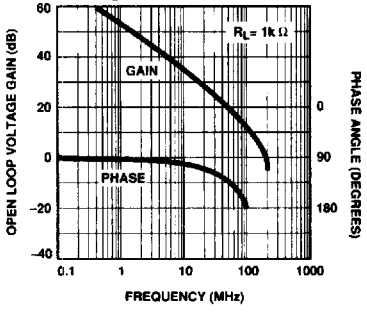
±Slew Rate vs Supply Voltage



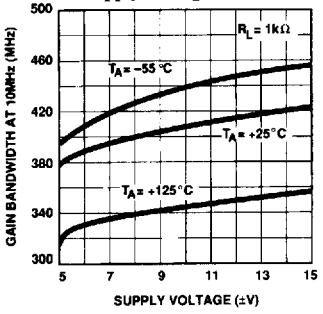
Output Voltage Swing vs Frequency



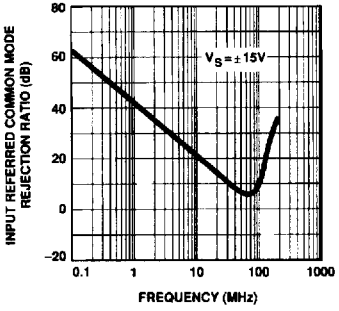
Open Loop Voltage Gain vs Frequency



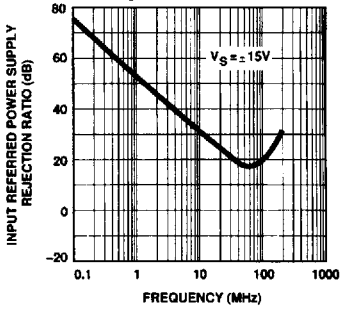
Gain Bandwidth Product at 10 MHz vs Supply Voltage



Input Referred Common Mode Rejection Ratio vs Frequency



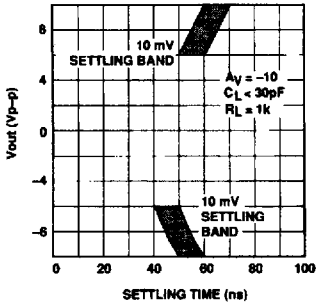
Input Referred Power Supply Rejection Ratio vs Frequency



2039-12

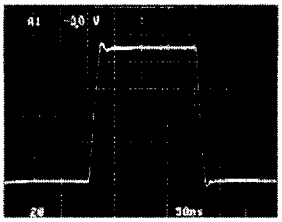
EL2040 Typical AC Performance Curves — Contd.

Settling Time



2039-13

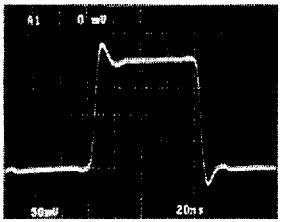
Large Signal Response



V_{IN} = ±0.5V
V_O = +5V

2039-14

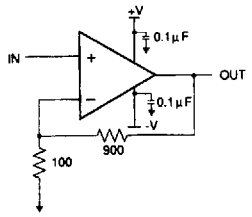
Small Signal Response



V_{IN} = ±10 mV
V_O = ±100 mV

2039-15

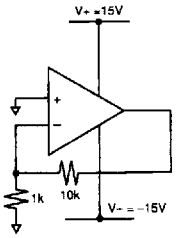
Test Circuit



2039-16

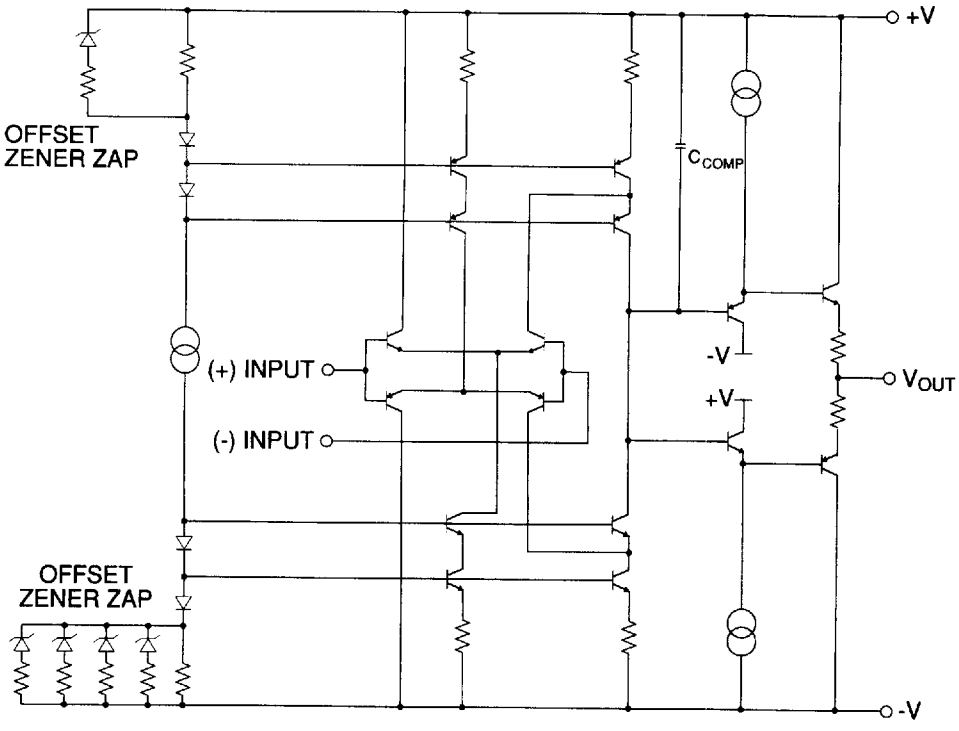
A_V = 10
C_L = 10 pF Scope Probe

Burn-In Circuit



2039-17

Schematic


+V
OFFSET
ZENER ZAP
C_{COMP}
(+) INPUT
(-) INPUT
-V
+V
V_{OUT}
OFFSET
ZENER ZAP
-V
2039-18

EL2039/EL2040

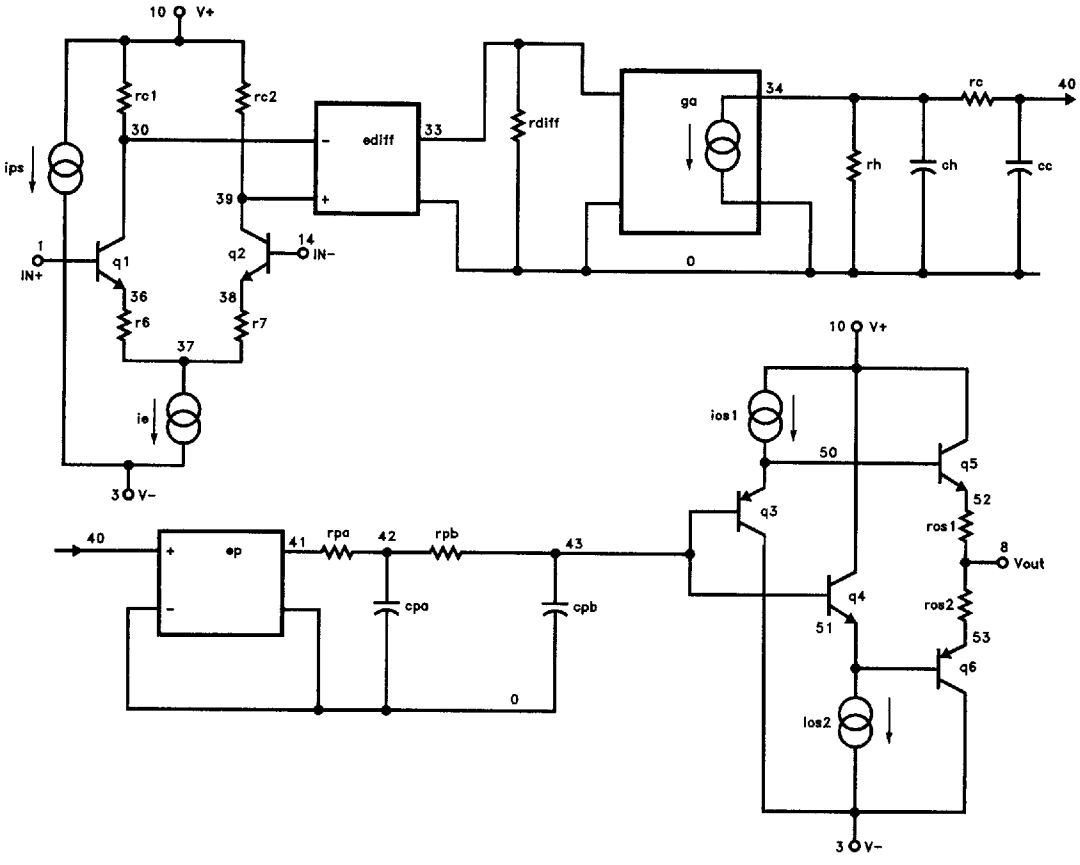
ELANTEC INC

Very High Slew Rate Wideband Operational Amplifier

EL2039 Macromodel

```
* Connections:      +input
*                   |    -input
*                   |    |    +Vsupply
*                   |    |    |    -Vsupply
*                   |    |    |    |    output
*                   |    |    |    |    |
.subckt M2039       1    14   10   3    8
* Input Stage
ie 37 3 1.7mA
r6 36 37 60
r7 38 37 60
rc1 10 30 75
rc2 10 39 75
q1 30 1 36 qn
q2 39 14 38 qna
ediff 33 0 39 30 7.25
rdiff 33 0 1Meg
* Compensation Section
ga 0 34 33 0 5.2m
rh 34 0 .525Meg
ch 34 0 1.5pF
rc 34 40 600
cc 40 0 7pF
* Poles
ep 41 0 40 0 1
rpa 41 42 75
cpa 42 0 7pF
rpb 42 43 50
cpb 43 0 3pF
* Output Stage
ios1 10 50 1.25mA
ios2 51 3 1.25mA
q3 3 43 50 qp
q4 10 43 51 qn
q5 10 50 52 qn
q6 3 51 53 qp
ros1 52 8 25
ros2 8 53 25
* Power Supply Current
ips 10 3 9.5mA
* Models
.model qn npn(is=800.0E-18 bf=170 tf=0.2nS)
.model qna npn(is=864E-18 bf=200 tf=0.2nS)
.model qp pnp(is=800E-18 bf=60 tf=0.2nS)
.ends
```

EL2039 Macromodel — Contd.



EL2040 Macromodel

```
* Connections:      +input
*                   |     -input
*                   |     |     +Vsupply
*                   |     |     |     -Vsupply
*                   |     |     |     |     output
*                   |     |     |     |     |
.subckt M2040       5     4     11    6     10
* Input Stage
ie 37 6 1.3mA
r6 36 37 60
r7 38 37 60
rc1 11 30 75
rc2 11 39 75
q1 30 5 36 qn
q2 39 4 38 qna
ediff 33 0 39 30 7.25
rdiff 33 0 1Meg
* Compensation Section
ga 0 34 33 0 5.2m
rh 34 0 .525Meg
ch 34 0 1.5pF
rc 34 40 600
cc 40 0 7pF
* Poles
ep 41 0 40 0 1
rpa 41 42 75
cpa 42 0 7pF
rpb 42 43 50
cpb 43 0 3pF
* Output Stage
ios1 11 50 1.25mA
ios2 51 6 1.25mA
q3 6 43 50 qp
q4 11 43 51 qn
q5 11 50 52 qn
q6 6 51 53 qp
ros1 52 10 25
ros2 10 53 25
* Power Supply Current
ips 11 6 9.5mA
* Models
.model qn npn(is=800.0E-18 bf=130 tf=0.2nS)
.model qna npn(is=864E-18 bf=150 tf=0.2nS)
.model qp pnp(is=800E-18 bf=60 tf=0.2nS)
.ends
```

EL2040 Macromodel — Contd.