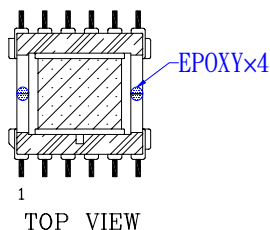
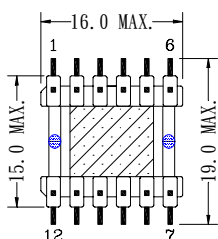
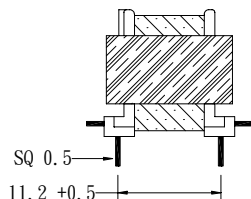
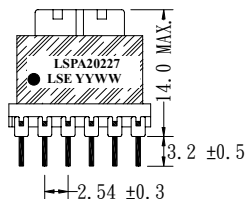


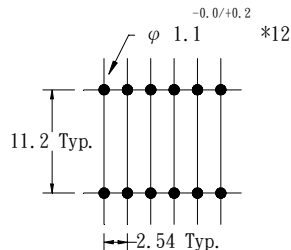
Mechanical



Note :
1. Unit : mm
2. Scale : F
3. 【YYWW】 : Date Code



BOTTOB VIEW

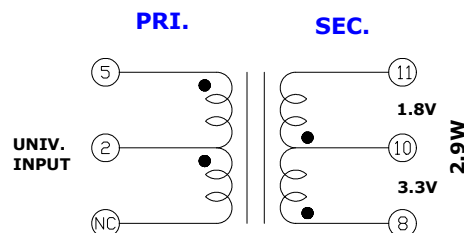


Hole Arrangement
View in soldering direction

Feature

- ◆ Universal Input 85~265 Volts
- ◆ Designed for Power Integrations TNY Switch II Chip Sets

Schematic



Electrical Characteristics

A. D.C. Resistance : (25°C)

R(5-2) : 4.750 Ω max.

R(8-11) : 21.0 m Ω max.

B. Inductance :

L(5-2) : 1.492 mH $\pm 10\%$

@ 132 KHz, 1V, Ls

C. Leakage Inductance :

Lk(5-2) : 113.0 μ H max.

@ 132 KHz, 1V, Ls, shorted P(8+10+11)

D. Turns Ratio :

P (8-10) ==> 0.020 $\pm$ 0.008Vrms

@ Pins(5-2) Input 20 KHz, 1.2V

P (10-11) ==> 0.030 $\pm$ 0.008Vrms

@ Pins(5-2) Input 20 KHz, 1.2V

P (8-11) ==> 0.050 $\pm$ 0.008Vrms

@ Pins(5-2) Input 20 KHz, 1.2V

E. Isolation Voltage (Hi-Pot) :

Pri. To Sec. ==> 3000 Vac, 3mA, 60 Sec

Sec. To Core ==> 500 Vac, 3mA, 60 Sec

Approved	Checked	Prepare	Date	Page	Rev.
許神長 05-MAR-2004	許神長 05-MAR-2004	胡文秀 05-MAR-2004	05-MAR-2004	1/1	1