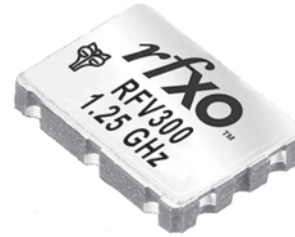


3.3V LV-PECL VCXO

RFV300



FEATURES

- 3.3V Operation
- High Performance
- Complementary Output
- Pb Free

Applications include SONET / SDH / ATM / WAN

PRELIMINARY

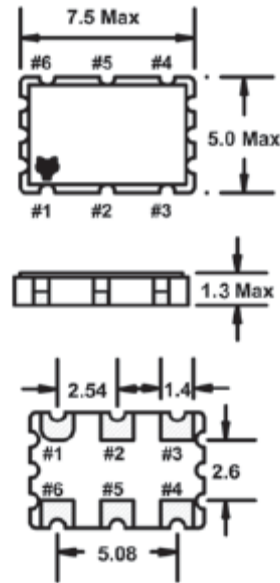
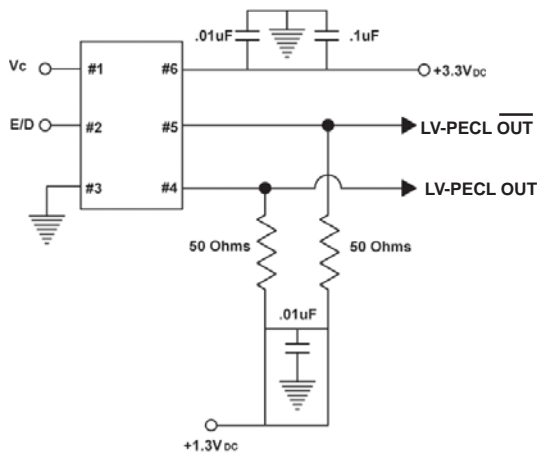
• ELECTRICAL CHARACTERISTICS	
PARAMETERS	MAX (unless otherwise noted)
Frequency Range (Fo)	600.000 ~ 1250MHz
Absolute Pull Range (APR) Vc=1.65V±1.5V	±50 PPM
Temperature Range	
Operating (TOPR)	-40°C ~ +85°C
Storage (TSTG)	-55°C ~ +125°C
Input Voltage (VDD)	3.3V ± 10%
Control Voltage (Vc)	1.65V ± 1.5V
Input Current (IDD)	40mA
Rise Time (20% ~ 80% Vp-p)	0.5nS
Fall Time (80% ~ 20% Vp-p)	0.5nS
Symmetry (50% Vp-p)	45/55 %
Output Voltage (VOL)	1.65 V
(VOH)	2.155 V Min
Linearity	± 10%
Modulation Bandwidth	>10kHz
PECL Skew (50% Vp-p)	125pS
Jitter	
RMS 12kHz to 20MHz	0.3pS Typ.
RMS 50kHz to 80MHz	0.8pS Typ.
RMS Period	2.8pS Typ.
Cycle-to-Cycle	23pS Typ.
Output Disable Time	100nS Max
Output Enable Time	100nS Max

¹ Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration.

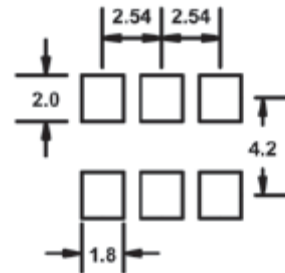
² An internal pullup resistor from pin 2 to pin 6 allows active output if pin 2 is left open.

All specifications subject to change without notice. Rev. 12/5/03

RFV300 Series Recommended Circuit



Recommended Solder Pad Layout



Pin Connections

# 1 Vc	# 4 Output 1
# 2 E/D	# 5 Output 2
# 3 GND	# 6 VDD

All dimensions are in millimeters.

• ENABLE / DISABLE FUNCTION	
(Pin 2)	OUTPUT (Pin 4, pin 5)
OPEN ²	ACTIVE
'1' Level VIH ≥ 2.0V	ACTIVE
'0' Level VIL ≤ 1.0V	High Z