**International**
**IOR Rectifier**

PD - 9.1297

**PRELIMINARY**

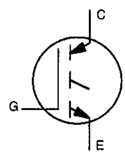
# IRGPC60M

INSULATED GATE BIPOLAR TRANSISTOR

Short Circuit Rated
Fast IGBT

### Features

- Short circuit rated - 10µs @ 125°C, $V_{GE}$ = 15V
- Switching-loss rating includes all "tail" losses
- Optimized for medium operating frequency (1 to 10kHz) See Figure 1 for Current vs. Frequency curve




**n-channel**

$V_{CES}$ = 600V

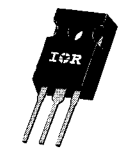
$V_{CE(sat)} \leq$ 2.6V

@$V_{GE}$ = 15V, $I_C$ = 51A

### Description

Insulated Gate Bipolar Transistors (IGBTs) from International Rectifier have higher usable current densities than comparable bipolar transistors, while at the same time having simpler gate-drive requirements of the familiar power MOSFET. They provide substantial benefits to a host of high-voltage, high-current applications.

These new short circuit rated devices are especially suited for motor control and other applications requiring short circuit withstand capability.



**TO-247AC**

### Absolute Maximum Ratings

| | Parameter | Max. | Units |
|---|---|---|---|
| $V_{CES}$ | Collector-to-Emitter Voltage | 600 | V |
| $I_C$ @ $T_C$ = 25°C | Continuous Collector Current | 70* | A |
| $I_C$ @ $T_C$ = 100°C | Continuous Collector Current | 51 | |
| $I_{CM}$ | Pulsed Collector Current ① | 170 | |
| $I_{LM}$ | Clamped Inductive Load Current ② | 170 | |
| $t_{sc}$ | Short Circuit Withstand Time | 10 | µs |
| $V_{GE}$ | Gate-to-Emitter Voltage | ±20 | V |
| $E_{ARV}$ | Reverse Voltage Avalanche Energy ③ | 25 | mJ |
| $P_D$ @ $T_C$ = 25°C | Maximum Power Dissipation | 280 | W |
| $P_D$ @ $T_C$ = 100°C | Maximum Power Dissipation | 110 | |
| $T_J$<br>$T_{STG}$ | Operating Junction and<br>Storage Temperature Range | -55 to +150 | °C |
| | Soldering Temperature, for 10 sec. | 300 (0.063 in. (1.6mm) from case) | |
| | Mounting torque, 6-32 or M3 screw. | 10 lbf•in (1.1N•m) | |

### Thermal Resistance

| | Parameter | Min. | Typ. | Max. | Units |
|---|---|---|---|---|---|
| $R_{\theta JC}$ | Junction-to-Case | ---- | ---- | 0.45 | °C/W |
| $R_{\theta CS}$ | Case-to-Sink, flat, greased surface | ---- | 0.24 | ---- | |
| $R_{\theta JA}$ | Junction-to-Ambient, typical socket mount | ---- | ---- | 40 | |
| Wt | Weight | ---- | 6 (0.21) | ---- | g (oz) |

Revision 0

## Electrical Characteristics @ $T_J$ = 25° C (unless otherwise specified)

| | Parameter | Min. | Typ. | Max. | Units | Conditions | |
|---|---|---|---|---|---|---|---|
| $V_{(BR)CES}$ | Collector-to-Emitter Breakdown Voltage | 600 | — | — | V | $V_{GE}$ = 0V, $I_C$ = 250μA | |
| $V_{(BR)ECS}$ | Emitter-to-Collector Breakdown Voltage ④ | 20 | — | — | V | $V_{GE}$ = 0V, $I_C$ = 1.0A | |
| $\Delta V_{(BR)CES}/\Delta T_J$ | Temperature Coeff. of Breakdown Voltage | — | 0.62 | — | V/°C | $V_{GE}$ = 0V, $I_C$ = 2.0mA | |
| $V_{CE(ON)}$ | Collector-to-Emitter Saturation Voltage | — | 1.7 | 2.6 | V | $I_C$ = 51A | $V_{GE}$ = 15V See Fig. 2, 5 |
| | | — | 2.2 | — | | $I_C$ = 87A | |
| | | — | 2.0 | — | | $I_C$ = 51A, $T_J$ = 150°C | |
| $V_{GE(th)}$ | Gate Threshold Voltage | 3.0 | — | 5.5 | | $V_{CE} = V_{GE}$, $I_C$ = 250μA | |
| $\Delta V_{GE(th)}/\Delta T_J$ | Temperature Coeff. of Threshold Voltage | — | -13 | — | mV/°C | $V_{CE} = V_{GE}$, $I_C$ = 250μA | |
| $g_{fe}$ | Forward Transconductance ⑤ | 22 | 33 | — | S | $V_{CE}$ = 100V, $I_C$ = 51A | |
| $I_{CES}$ | Zero Gate Voltage Collector Current | — | — | 250 | μA | $V_{GE}$ = 0V, $V_{CE}$ = 600V | |
| | | — | — | 2000 | | $V_{GE}$ = 0V, $V_{CE}$ = 600V, $T_J$ = 150°C | |
| $I_{GES}$ | Gate-to-Emitter Leakage Current | — | — | ±100 | nA | $V_{GE}$ = ±20V | |

## Switching Characteristics @ $T_J$ = 25° C (unless otherwise specified)

| | Parameter | Min. | Typ. | Max. | Units | Conditions |
|---|---|---|---|---|---|---|
| $Q_g$ | Total Gate Charge (turn-on) | --- | 110 | 160 | nC | $I_C$ = 51A |
| $Q_{ge}$ | Gate - Emitter Charge (turn-on) | --- | 27 | 42 | | $V_{CC}$ = 400V See Fig. 8 |
| $Q_{gc}$ | Gate - Collector Charge (turn-on) | --- | 43 | 65 | | $V_{GE}$ = 15V |
| $t_{d(on)}$ | Turn-On Delay Time | --- | 40 | --- | ns | |
| $t_r$ | Rise Time | --- | 41 | --- | | |
| $t_{d(off)}$ | Turn-Off Delay Time | --- | 380 | 560 | | $I_C$ = 51A, $V_{CC}$ = 480V |
| $t_f$ | Fall Time | --- | 210 | 310 | | $V_{GE}$ = 15V, $R_G$ = 3.3Ω |
| $E_{on}$ | Turn-On Switching Loss | --- | 1.5 | --- | mJ | Energy losses include "tail" |
| $E_{off}$ | Turn-Off Switching Loss | --- | 4.9 | --- | | See Fig. 9, 10, 11, 14 |
| $E_{ts}$ | Total Switching Loss | --- | 6.4 | 9.5 | | |
| $t_{sc}$ | Short Circuit Withstand Time | 10 | --- | --- | μs | $V_{CC}$ = 360V, $T_J$ = 125°C<br>$V_{GE}$ = 15V, $R_G$ = 3.3Ω, $V_{CPK}$ < 500V |
| $t_{d(on)}$ | Turn-On Delay Time | --- | 38 | --- | ns | $T_J$ = 150°C, |
| $t_r$ | Rise Time | --- | 44 | --- | | $I_C$ = 51A, $V_{CC}$ = 480V |
| $t_{d(off)}$ | Turn-Off Delay Time | --- | 680 | --- | | $V_{GE}$ = 15V, $R_G$ = 3.3Ω |
| $t_f$ | Fall Time | --- | 410 | --- | | Energy losses include "tail" |
| $E_{ts}$ | Total Switching Loss | --- | 12 | --- | mJ | See Fig. 10, 14 |
| $L_E$ | Internal Emitter Inductance | --- | 13 | --- | nH | Measured 5mm from package |
| $C_{ies}$ | Input Capacitance | --- | 4400 | --- | pF | $V_{GE}$ = 0V |
| $C_{oes}$ | Output Capacitance | --- | 340 | --- | | $V_{CC}$ = 30V See Fig. 7 |
| $C_{res}$ | Reverse Transfer Capacitance | --- | 33 | --- | | $f$ = 1.0MHz |

**Notes:**

① Repetitive rating; $V_{GE}$ = 20V, pulse width limited by max. junction temperature. ( See fig. 13b )

② $V_{CC}$ = 80%($V_{CES}$), $V_{GE}$ = 20V, L = 10μH, $R_G$ = 3.3Ω, ( See fig. 13a )

③ Repetitive rating; pulse width limited by maximum junction temperature.

④ Pulse width ≤ 80μs; duty factor ≤ 0.1%.

⑤ Pulse width 5.0μs, single shot.

* Current Limited by the Package, (Die Current = 87A).

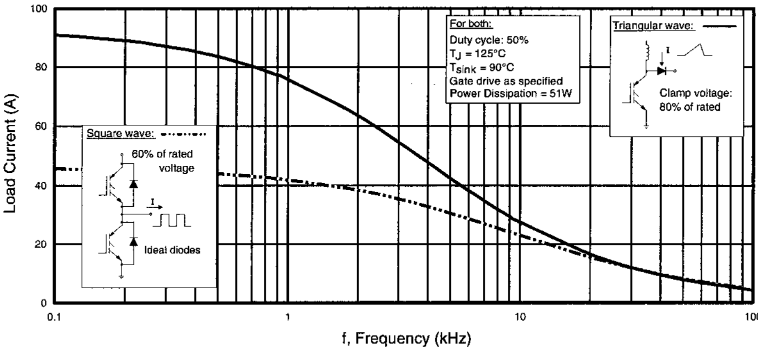



**Fig. 1** - Typical Load Current vs. Frequency
(For square wave, $I=I_{RMS}$ of fundamental; for triangular wave, $I=I_{PK}$)

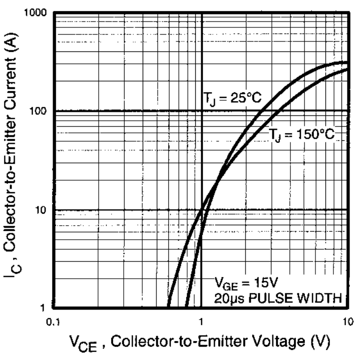



**Fig. 2** - Typical Output Characteristics

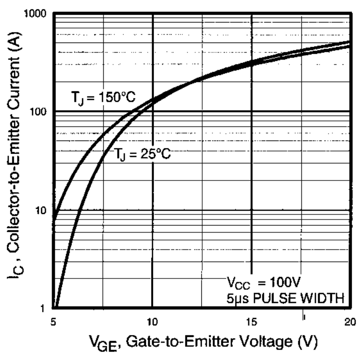



**Fig. 3** - Typical Transfer Characteristics

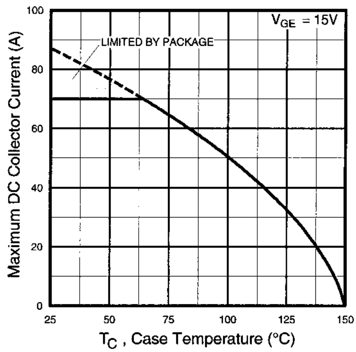



**Fig. 4** - Maximum Collector Current vs. Case Temperature

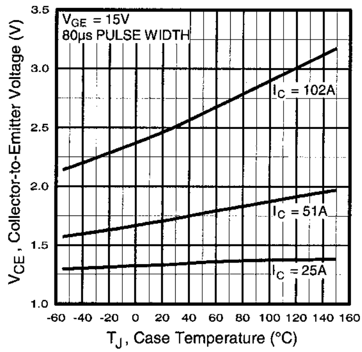



**Fig. 5** - Collector-to-Emitter Voltage vs. Case Temperature

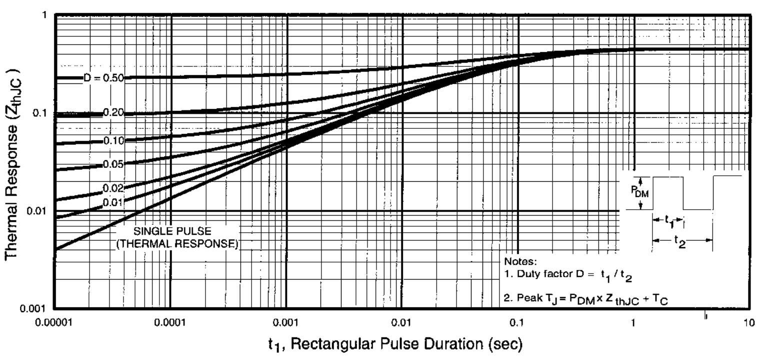



**Fig. 6** - Maximum Effective Transient Thermal Impedance, Junction-to-Case

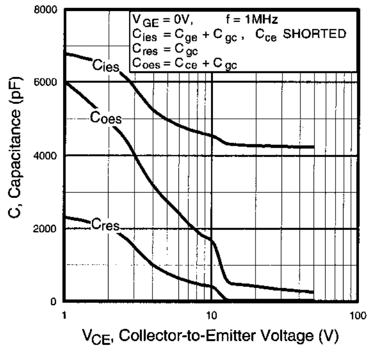



**Fig. 7** - Typical Capacitance vs. Collector-to-Emitter Voltage

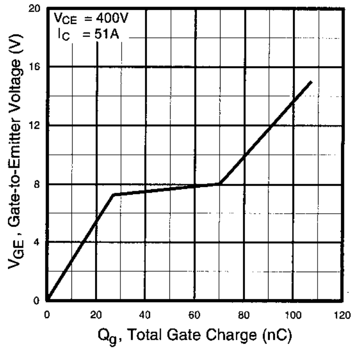



**Fig. 8** - Typical Gate Charge vs. Gate-to-Emitter Voltage

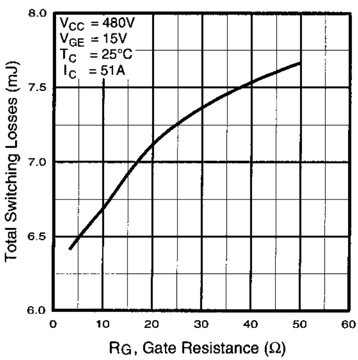



**Fig. 9** - Typical Switching Losses vs. Gate Resistance

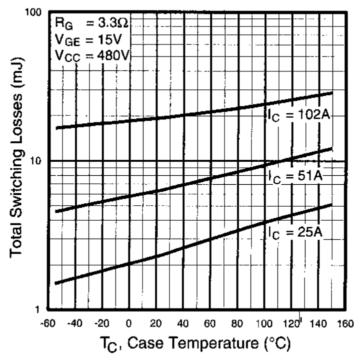



**Fig. 10** - Typical Switching Losses vs. Case Temperature

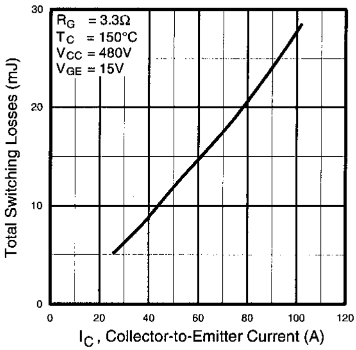



**Fig. 11** - Typical Switching Losses vs. Collector-to-Emitter Current

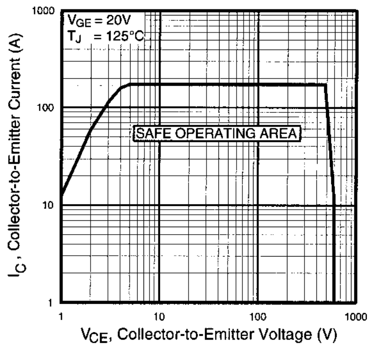
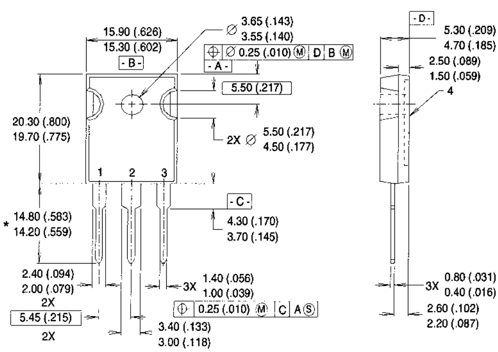



**Fig. 12** - Turn-Off SOA




NOTES:
1. DIMENSIONS & TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION : INCH.
3. DIMENSIONS ARE SHOWN MILLIMETERS (INCHES).
4. CONFORMS TO JEDEC OUTLINE TO-247AC.

LEAD ASSIGNMENTS
1 - GATE
2 - COLLECTOR
3 - EMITTER
4 - COLLECTOR

* LONGER LEADED (20mm) VERSION AVAILABLE (TO-247AD) TO ORDER ADD "-E" SUFFIX TO PART NUMBER

**CONFORMS TO JEDEC OUTLINE TO-247AC (TO-3P)**

Dimensions in Millimeters and (Inches)

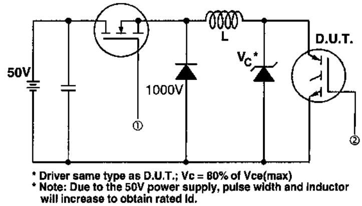



**Fig. 13a** - Clamped Inductive Load Test Circuit

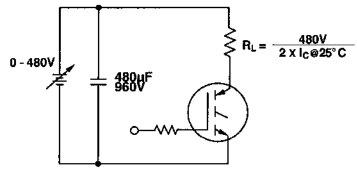



**Fig. 13b** - Pulsed Collector Current Test Circuit

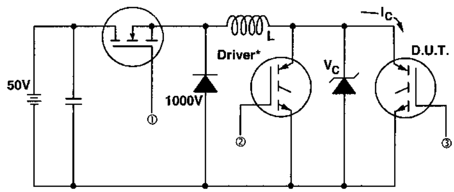



**Fig. 14a** - Switching Loss Test Circuit

*** Driver same type as D.U.T., VC = 480V**

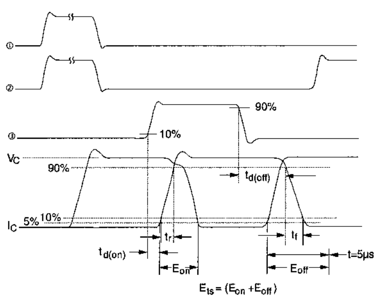



**Fig. 14b** - Switching Loss Waveforms

**International IOR Rectifier**

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**IR CANADA:** 7321 Victoria Park Ave., Suite 201, Markham, Ontario L3R 3L1, Tel: (905) 475 1897 **IR GERMANY:** Saalburgstrasse 157, 61350 Bad Homburg Tel: 6172 37066 **IR ITALY:** Via Liguria 49, 10071 Borgaro, Torino Tel: (39) 1145 10111 **IR FAR EAST:** K&H Bldg., 2F, 3-30-4 Nishi-Ikeburo 3-Chome, Toshima-Ki, Tokyo 171 Tel: (03)3983 0641 **IR SOUTHEAST ASIA:** 315 Outram Road, #10-02 Tan Boon Liat Building, Singapore 0316 Tel: 65 221 8371

*Data and specifications subject to change without notice. 4/95*