

General Description

The MIC38C4x are fixed frequency, high performance, current-mode PWM controllers. Micrel's BiCMOS devices are pin compatible with 384x bipolar devices but feature several improvements.

Undervoltage lockout circuitry allows the '42 and '44 versions to start up at 14.5V and operate down to 9V, and the '43 and '45 versions start at 8.4V with operation down to 7.6V. All versions operate up to 20V.

When compared to bipolar 384x devices operating from a 15V supply, start-up current has been reduced to 50µA typical and operating current has been reduced to 4.0 mA typical. Decreased output rise and fall times drive larger MOSFETs, and rail-to-rail output capability increases efficiency, especially at lower supply voltages. The MIC38C4x also features a trimmed oscillator discharge current and bandgap reference.

The MIC38C4x denotes 8-pin plastic DIP and SOIC packages. MIC384Cx-1 denotes 14-pin plastic DIP and SOIC packages. 8-pin devices feature small size, while 14-pin devices separate the analog and power connections for improved performance and power dissipation.

For fast rise and fall times and higher output drive, refer to the MIC38HC4x.

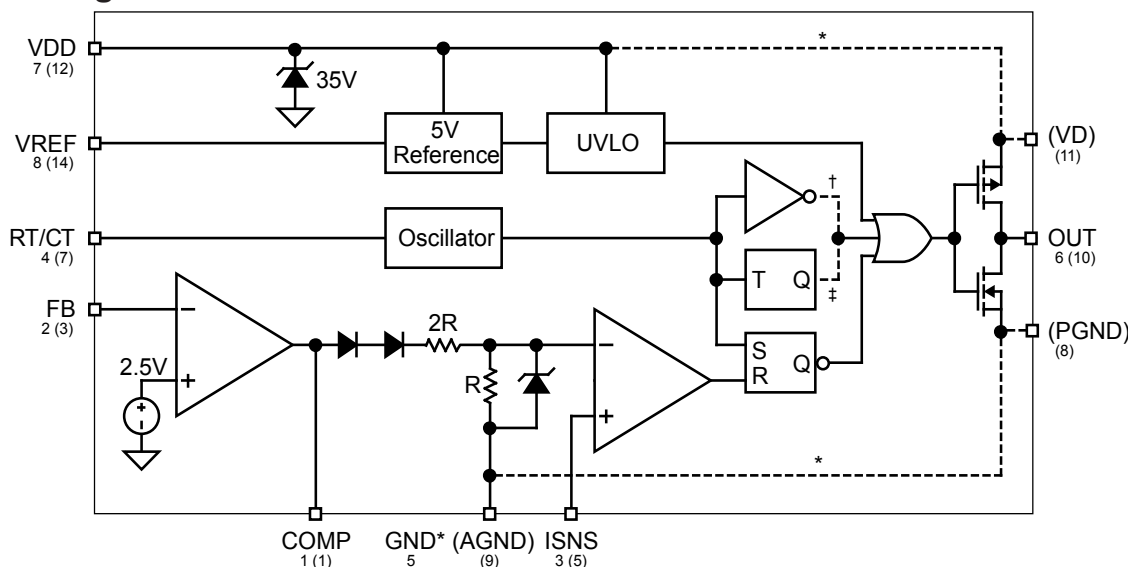
Features

- Fast 40ns output rise and 30ns output fall times
- -40°C to $+85^{\circ}\text{C}$ temperature range meets UC284x specifications
- High-performance, low-power BiCMOS Process
- Ultralow start-up current ($50\mu\text{A}$ typical)
- Low operating current (4mA typical)
- CMOS outputs with rail-to-rail swing
- $\geq 500\text{kHz}$ current-mode operation
- Trimmed 5V bandgap reference
- Pin-for-pin compatible with UC3842/3843/3844/3845(A)
- Trimmed oscillator discharge current
- UVLO with hysteresis
- Low cross-conduction currents

Applications

- Current-mode, off-line, switched-mode power supplies
- Current-mode, dc-to-dc converters.
- Step-down “buck” regulators
- Step-up “boost” regulators
- Flyback, isolated regulators
- Forward converters
- Synchronous FET converters

Functional Diagram



() pins are on MIC38C4x-1 (14-lead) versions only

* MIC38C4x. (8-lead) versions only

† MIC38C42, MIC38C43 (96% max. duty cycle) versions only

‡ MIC38C44, MIC38C45 (50% max. duty cycle) versions only

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Ordering Information

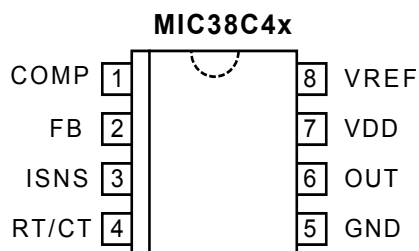
Part Number*		Temperature Range	Package
Standard	Pb-Free		
MIC38C42BN	MIC38C42YN	-40°C to +85°C	8-pin Plastic DIP
MIC38C43BN	MIC38C43YN	-40°C to +85°C	8-pin Plastic DIP
MIC38C44BN	MIC38C44YN	-40°C to +85°C	8-pin Plastic DIP
MIC38C45BN	MIC38C45YN	-40°C to +85°C	8-pin Plastic DIP
MIC38C42-1BN	Contact Factory	-40°C to +85°C	14-pin Plastic DIP
MIC38C43-1BN	Contact Factory	-40°C to +85°C	14-pin Plastic DIP
MIC38C44-1BN	MIC38C44-1YN	-40°C to +85°C	14-pin Plastic DIP
MIC38C45-1BN	Contact Factory	-40°C to +85°C	14-pin Plastic DIP
MIC38C42BM	MIC38C42YM	-40°C to +85°C	8-pin SOIC
MIC38C43BM	MIC38C43YM	-40°C to +85°C	8-pin SOIC
MIC38C44BM	MIC38C44YM	-40°C to +85°C	8-pin SOIC
MIC38C45BM	MIC38C45YM	-40°C to +85°C	8-pin SOIC
MIC38C42-1BM	MIC38C42-1YM	-40°C to +85°C	14-pin SOIC
MIC38C43-1BM	MIC38C43-1YM	-40°C to +85°C	14-pin SOIC
MIC38C44-1BM	MIC38C44-1YM	-40°C to +85°C	14-pin SOIC
MIC38C45-1BM	MIC38C45-1YM	-40°C to +85°C	14-pin SOIC

* Refer to the Part Number Cross Reference for a listing of Micrel devices equivalent to UC284x and UC384x devices.

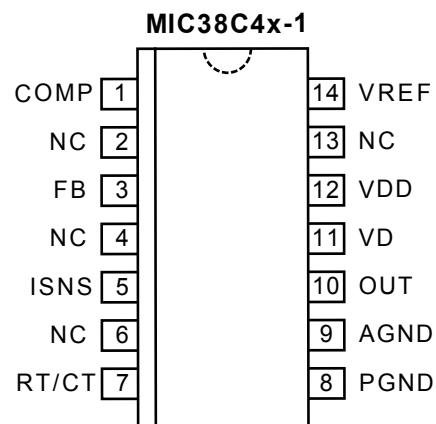
Selection Guide

Duty Cycle	UVLO Thresholds	
	Startup 8.4V Minimum Operating 7.6V	Startup 14.5V Minimum Operating 9V
0% to 96%	MIC38C43	MIC38C42
0% to 50%	MIC38C45	MIC38C44

Pin Configuration



8-Pin DIP (N)
8-Lead SOIC (M)
8-Lead MM8™ (MM)



14-Pin DIP (-1BN)
14-Lead SOIC (-1BM)

Pin Description

Pin Number N, M, MM	Pin Number -1BN, -1BM	Pin Name	Pin Function
1	1	COMP	Compensation: Connect external compensation network to modify the error amplifier output.
	2	NC	Not internally connected.
2	3	FB	Feedback (Input): Error amplifier input. Feedback is 2.5V at desired output voltage.
	4	NC	Not internally connected.
3	5	ISNS	Current Sense (Input): Current sense comparator input. Connect to current sensing resistor or current transformer.
	6	NC	Not internally connected.
4	7	RT/CT	Timing Resistor/Timing Capacitor: Connect external RC network to select switching frequency.
5		GND	Ground: Combined analog and power ground.
	8	PGND	Power Ground: N-channel driver transistor ground.
	9	AGND	Analog Ground: Controller circuitry ground.
6	10	OUT	Power Output: Totem-pole output.
	11	VD	Power Supply (Input): P-channel driver transistor supply input. Return to power ground (PGND).
7	12	VDD	Analog Supply (Input): Controller circuitry supply input. Return to analog ground (AGND).
	13	NC	Not internally connected.
8	14	VREF	5V Reference (Output): Connect external RC network.

Absolute Maximum Ratings

Zener Current (V_{DD}) 30mA

Operation at $\geq 18V$ may require special precautions (Note 6).

Supply Voltage (V_{DD}), **Note 6** 20V

Switch Supply Voltage (V_D) 20V

Current Sense Voltage (V_{ISNS}) -0.3V to 5.5V

Feedback Voltage (V_{FB}) -0.3V to 5.5V

Output Current, 38C42/3/4/5 (I_{OUT}) 0.5A

Storage Temperature (T_A) -65°C to +150°C

Operating Ratings

Junction Temperature (T_J) 150°C

Package Thermal Resistance

8-Pin Plastic DIP (θ_{JA}) 125°C/W

8-Pin MM8™ (θ_{JA}) 250°C/W

8-Pin SOIC (θ_{JA}) 170°C/W

14-Pin Plastic DIP (θ_{JA}) 90°C/W

14-Pin SOIC (θ_{JA}) 145°C/W

Electrical Characteristics

$V_{DD} = 15V$, **Note 4**; $R_T = 11.0k$; $C_T = 3.3nF$; -40°C $\leq T_A \leq 85^\circ\text{C}$; unless noted

Parameter	Test Conditions	Min	Typ	Max	Units
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Reference Section

Output Voltage	$T_A = 25^\circ\text{C}$, $I_O = 1\text{mA}$	4.90	5.00	5.10	V
Line Regulation	$12V \leq V_{DD} \leq 18V$, $I_O = 5\mu\text{A}$, Note 6		2	20	mV
Load Regulation	$1 \leq I_O \leq 20\text{mA}$		1	25	mV
Temp. Stability	Note 1		0.2		mV/°C
Total Output Variation	Line, Load, Temp., Note 1	4.82		5.18	V
Output Noise Voltage	$10\text{Hz} \leq f \leq 10\text{kHz}$, $T_A = 25^\circ\text{C}$, Note 1		50		μV
Long Term Stability	$T_A = 125^\circ\text{C}$, 1000 hrs., Note 1		5	25	mV
Output Short Circuit		-30	-80	-180	mA

Oscillator Section

Initial Accuracy	$T_A = 25^\circ\text{C}$, Note 5	49	52	55	kHz
Voltage Stability	$12 \leq V_{DD} \leq 18V$, Note 6		0.2	1.0	%
Temp. Stability	$T_{MIN} \leq T_A \leq T_{MAX}$, Note 1		0.04		%/°C
Clock Ramp	$T_A = 25^\circ\text{C}$, $V_{RT/CT} = 2V$	7.7	8.4	9.0	mA
Reset Current	$T_A = T_{MIN}$ to T_{MAX}	7.2	8.4	9.5	mA
Amplitude	$V_{RT/CT}$ peak to peak		1.9		Vp-p

Error Amp Section

Input Voltage	$V_{COMP} = 2.5V$	2.42	2.50	2.58	V
Input Bias Current	$V_{FB} = 5.0V$		-0.1	-2	μA
A_{VOL}	$2 \leq V_O \leq 4V$	65	90		dB
Unity Gain Bandwidth	Note 1	0.7	1.0		MHz
PSRR	$12 \leq V_{DD} \leq 18V$	60			dB
Output Sink Current	$V_{FB} = 2.7V$, $V_{COMP} = 1.1V$	2	14		mA
Output Source Current	$V_{FB} = 2.3V$, $V_{COMP} = 5V$	-0.5	-1		mA
V_{OUT} High	$V_{FB} = 2.3V$, $R_L = 15k$ to ground	5	6.8		V
V_{OUT} Low	$V_{FB} = 2.7V$, $R_L = 15k$ to V_{REF}		0.1	1.1	V

Parameter	Test Conditions	Min	Typ	Max	Units
Current Sense					
Gain	Notes 2, 3	2.85	3.0	3.15	V/V
Maximum Threshold	$V_{COMP} = 5V$, Note 2	0.9	1	1.1	V
PSRR	$12 \leq V_{DD} \leq 18V$, Note 2		70		dB
Input Bias Current			-0.1	-2	μA
Delay to Output			120	250	ns
Output					
$R_{DS(ON)}$ High	$I_{SOURCE} = 200mA$		20		Ω
$R_{DS(ON)}$ Low	$I_{SINK} = 200mA$		11		Ω
Rise Time	$T_A = 25^\circ C$, $C_L = 1nF$		40	80	ns
Fall Time	$T_A = 25^\circ C$, $C_L = 1nF$		30	60	ns
Undervoltage Lockout					
Start Threshold	MIC38C42/4	13.5	14.5	15.5	V
	MIC38C43/5	7.8	8.4	9.0	V
Minimum Operating Voltage	MIC38C42/4	8	9	10	V
	MIC38C43/5	7.0	7.6	8.2	V
Pulse Width Modulator					
Maximum Duty Cycle	MIC38C42/3	94	96		%
	MIC38C44/5	46	50		%
Minimum Duty Cycle				0	%
Total Standby Current					
Start-Up Current	$V_{DD} = 13V$ for MIC38C42/44 $V_{DD} = 7.5V$ for MIC38C43/45		50	200	μA
Operating Supply Current	$V_{FB} = V_{ISNS} = 0V$		4.0	6.0	mA
Zener Voltage (V_{DD})	$I_{DD} = 25mA$, Note 6	30	37		V

Note 1: These parameters, although guaranteed, are not 100% tested in production.

Note 2: Parameter measured at trip point of latch with $V_{EA} = 0$.

Note 3: Gain defined as:

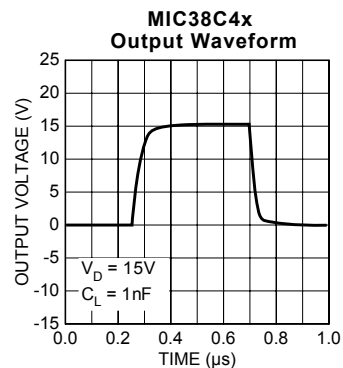
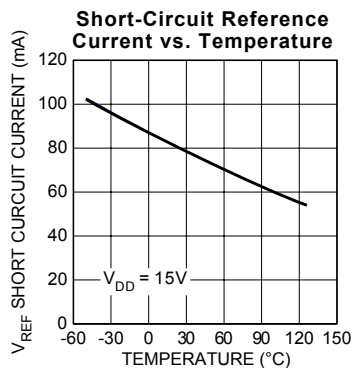
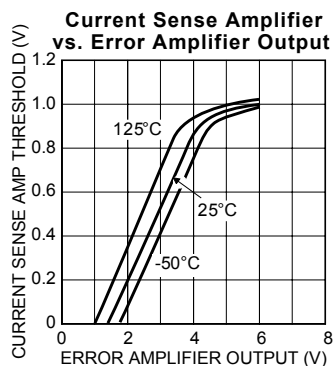
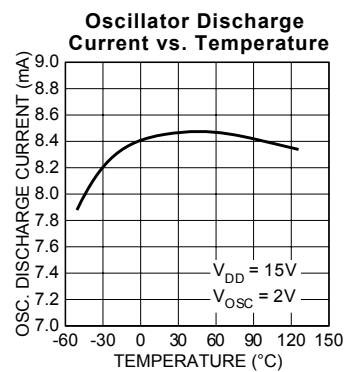
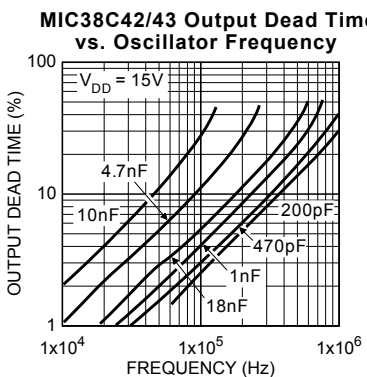
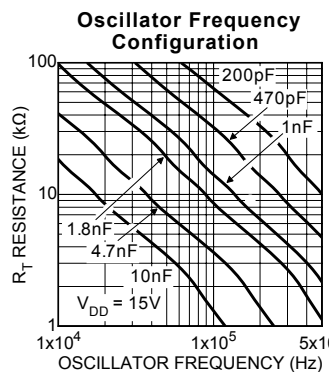
$$A = \frac{\Delta V_{PIN1}}{V_{TH}(I_{SNS})}; 0 \leq V_{TH}(I_{SNS}) \leq 0.8V$$

Note 4: Adjust V_{DD} above the start threshold before setting at 15V.

Note 5: Output frequency equals oscillator frequency for the MIC38C42 and MIC38C43. Output frequency for the MIC38C44, and MIC38C45 equals one half the oscillator frequency.

Note 6: On 8-pin version, 20V is maximum input on pin 7, as this is also the supply pin for the output stage. On 14-pin version, 40V is maximum for pin 12 and 20V maximum for pin 11.

Typical Characteristics



M9999-072805

Test	Conditions	Results
Line Regulation	$V_{IN} = 26V$ to $80V$, $I_O = 2A$	0.5%
Load Regulation	$V_{IN} = 48V$, $I_O = 0.2A$ to $2A$	0.6%
Efficiency	$V_{IN} = 48V$, $I_O = 2A$	90%
Output Ripple	$V_{IN} = 48V$, $I_O = 2A$ (20MHz BW)	100mV

Symbol	Custom Coil ¹	ETS ²
CT1	4923	ETS 92420
T1	4924	ETS 92419
L1	4925	ETS 92421

1. Custom Coils, Alcester, SD tel: (605) 934-2460

2. Energy Transformation Systems, Inc. tel: (415) 324-4949.

Synchronous Buck Converter

Refer to figure 2. This MIC38C43 synchronous buck converter uses an MIC5022 half-bridge driver to alternately drive the PWM switch MOSFET (driven by GATEH, or high-side output) and a MOSFET which functions as a synchronous rectifier (driven by the GATEL, or low-side output).

The low-side MOSFET turns on when the high-side MOSFET is off, allowing current to return from ground. Current flows through the low-side MOSFET in the source to drain direction.

The on-state voltage drop of the low-side MOSFET is lower than the forward voltage drop of an equivalent Schottky rectifier. This lower voltage drop results in higher efficiency.

A sense resistor (5mΩ) is connected to the driver's high-side current sense inputs to provide overcurrent protection. Refer to the MIC5020, MIC5021, and MIC5022 data sheets for more information.

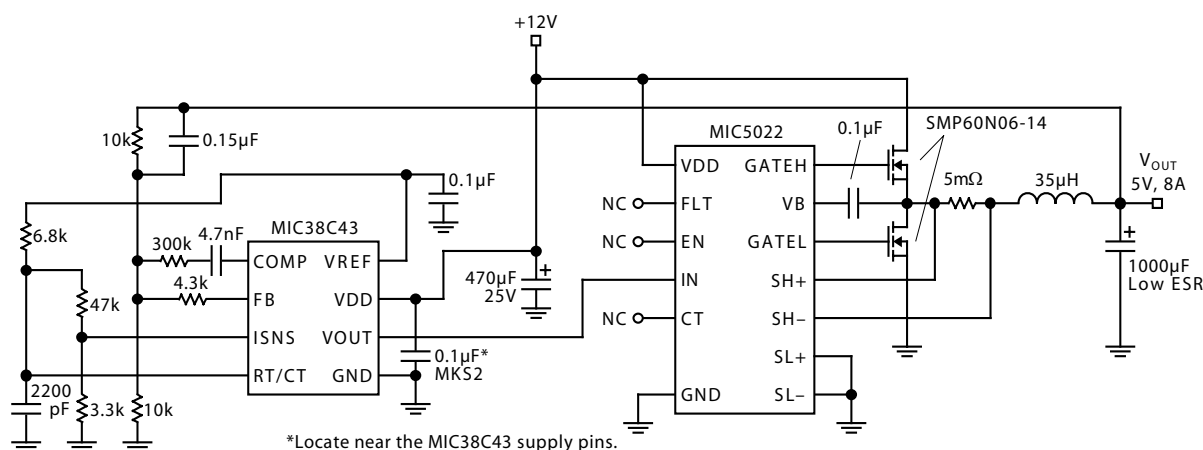
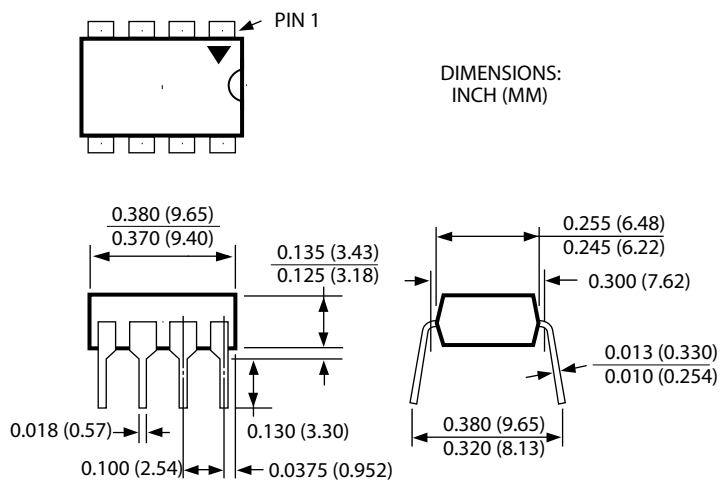
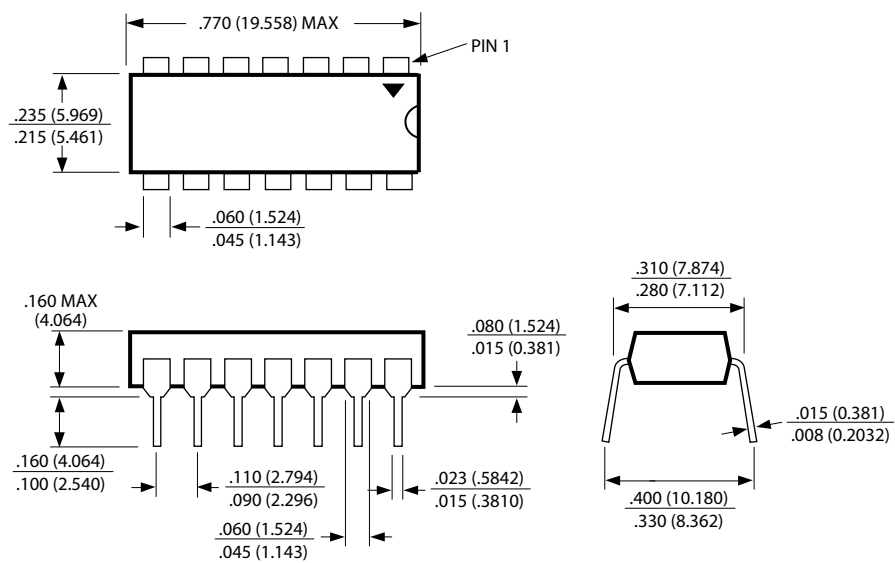


Figure 2. 100kHz, Synchronous Buck Converter

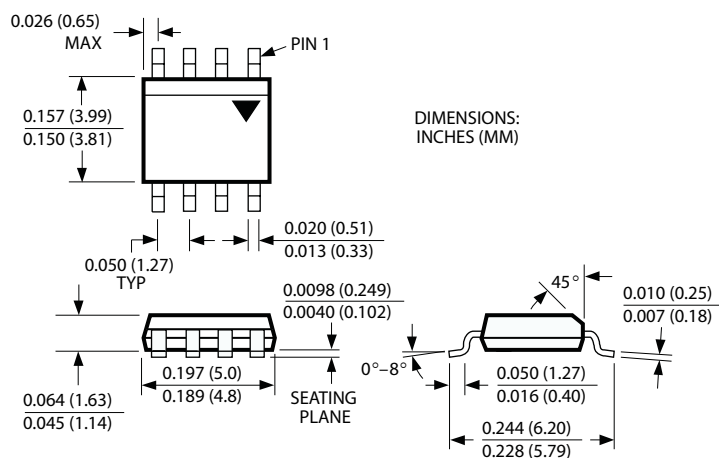
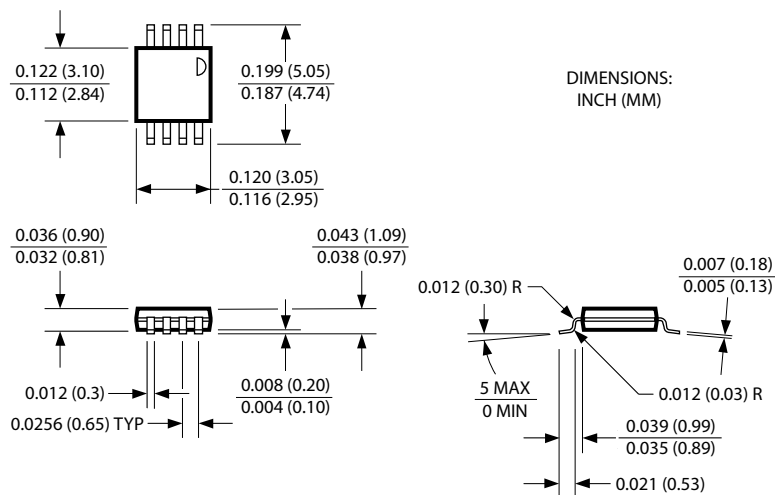
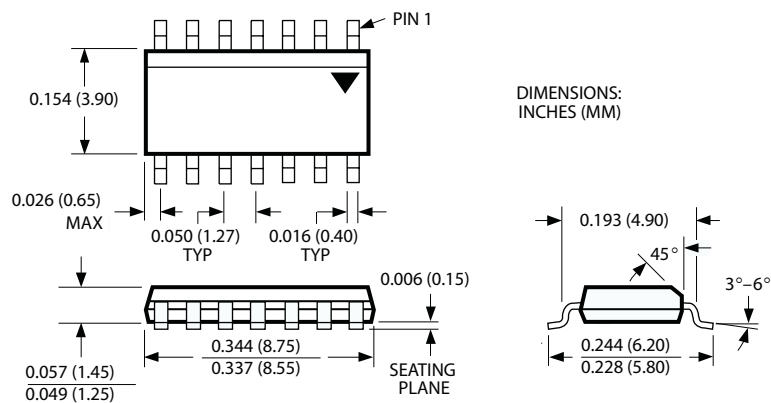
Package Information



8-Pin Plastic DIP (N)



14-Pin Plastic DIP (N)

**8-Pin SOIC (M)****8-Pin MSOP (MM)****14-Pin SOIC (M)**

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