Regulator+Reset IC
Monolithic IC MM1458

Outline

This IC, developed for use in CD-ROM drives, combines a 3V regulator adapted to low power consumption with a much-sought reset function (regulator input monitoring), with internal delay circuit, set to detect 4.2V.

Features

1. Large output current 300mA max.
2. High ripple rejection rate 80dB typ.
3. Internal thermal shutdown circuit.
4. Internal current-limiting circuit.
5. Adjustment-free reset detection voltage 4.2V typ.
6. Easy to set delay time from voltage detection to reset release.

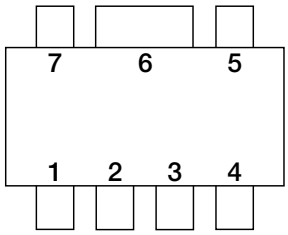
Package

SOP-7

Applications

CD-ROM drive.

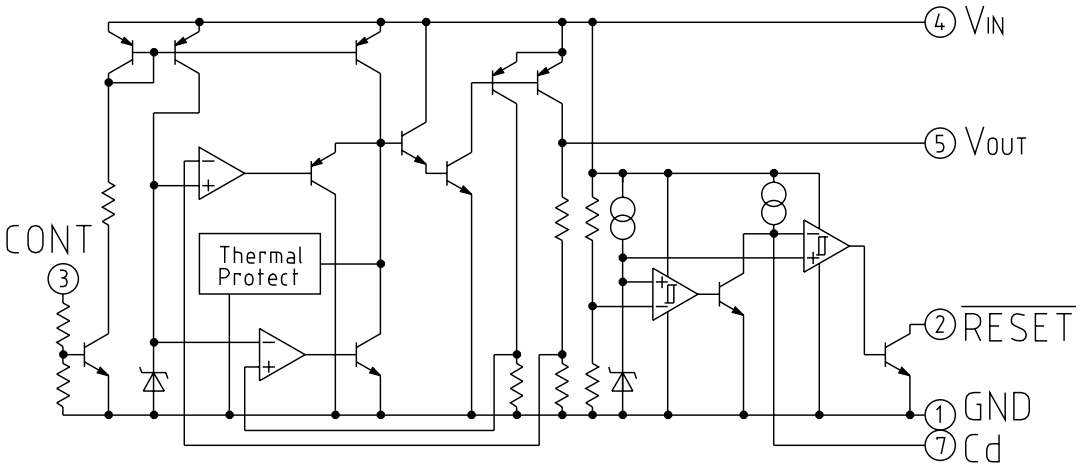
Pin Assignment



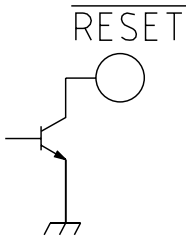
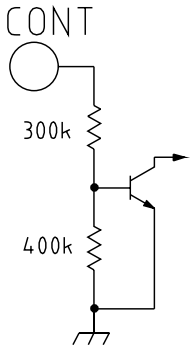
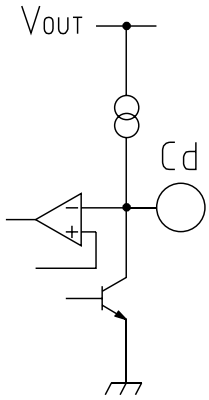
SOP-7

1	GND
2	$\overline{\text{RESET}}$
3	CONT
4	V_{IN}
5	V_{OUT}
6	GND
7	cd

Equivalent Circuit Diagram



Pin Description

<table>
<tr><th>Pin No.</th><th>Pin name</th><th>Functions</th><th>Equivalent circuit diagram</th></tr>
<tr><td>1</td><td>GND</td><td>GND pin</td><td></td></tr>
<tr><td>2</td><td>$\overline{\text{RESET}}$</td><td>Input voltage detection output
Input voltage detection output pin
$\overline{\text{RESET}}$ pin logic
<table>
<tr><th></th><th>$\overline{\text{RESET}}$</th></tr>
<tr><td>V_{IN}<VS</td><td>L</td></tr>
<tr><td>V_{IN}>VS</td><td>H</td></tr>
</table></td><td> 
</td></tr>
<tr><td>3</td><td>CONT</td><td>Output voltage on/off-control pin
<table>
<tr><th>V_{CONT}</th><th>Output</th></tr>
<tr><td>L</td><td>OFF</td></tr>
<tr><td>H</td><td>ON</td></tr>
</table>
Connect cont-terminal with V_{IN}, when it is not used.</td><td> 
</td></tr>
<tr><td>4</td><td>V_{IN}</td><td>Voltage supply input pin</td><td></td></tr>
<tr><td>5</td><td>V_{OUT}</td><td>Regulator Output pin</td><td></td></tr>
<tr><td>6</td><td>GND</td><td>GND pin</td><td></td></tr>
<tr><td>7</td><td>Cd</td><td>Delay time capacitor pin
RESET pin output delay time can be set by the capacitance connected to the Cd pin.
t_{PLH}=100000·C
t_{PLH}: transmission delay time [S]
C: capacitor value [F]</td><td> 
</td></tr>
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Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Operating temperature	T_{OPR}	–20~+80	°C
Storage temperature	T_{STG}	–40~+125	°C
Supply voltage	V_{IN}	–0.3~+10	V
Output current	I_{OUT}	400	mA
Power dissipation	Pd	800*	mW

Note: * When mounted on a 25×40×1^t mm glass epoxy board.

Recommended Operating Conditions

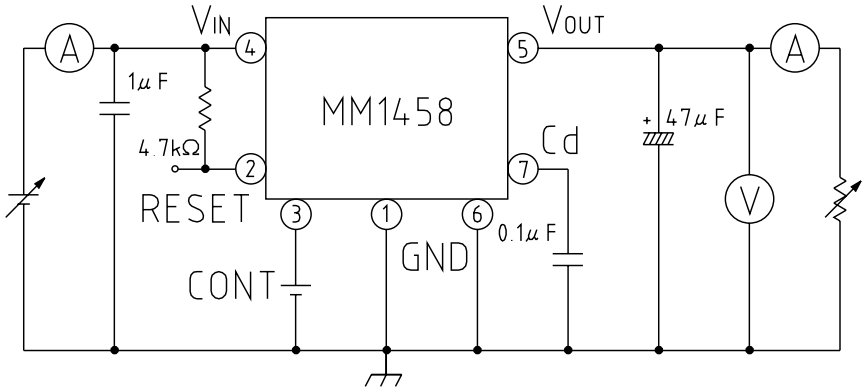
Item	Symbol	Ratings	Unit
Operating temperature	T_{OP}	–20~80	°C
Output current	I_{OP}	0~300	mA
Operating voltage	V_{OP}	0~10	V

Electrical Characteristics (Except where noted otherwise, Ta=25°C, V_{CONT}=1.6V)

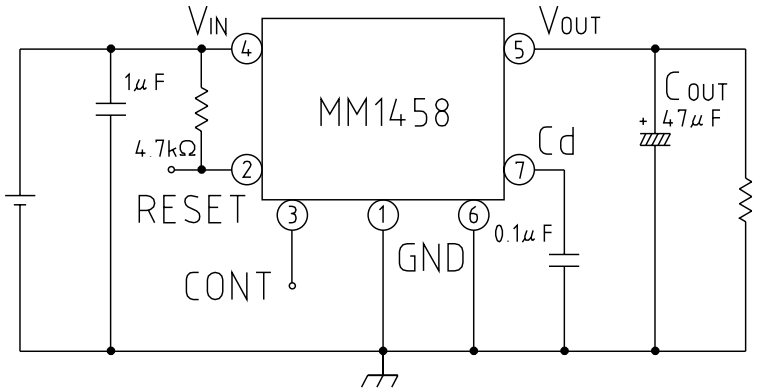
Item	Symbol	Measurement conditions	Min.	Typ.	Max.	Unit
No-load input current 1	Iccq1	V_{IN}=5V, I_{OUT}=0mA		3	8	mA
No-load input current 2	Iccq2	V_{IN}=4V, I_{OUT}=0mA		4		mA
Input current (OFF)	Iccq3	V_{IN}=5V, V_{CONT}=0.4V		250		µA
Regulator						
Output Voltage	V_{OUT}	V_{IN}=5V, I_{OUT}=30mA	3.33	3.40	3.47	V
Input-Output differential Voltage	Vi0	V_{IN}=3.2V, I_{OUT}=150mA		0.15	0.3	V
Line regulation	⊿V1	V_{IN}=4.4~5.5V, I_{OUT}=30mA		10	20	mV
Load regulation	⊿V2	V_{IN}=5V, I_{OUT}=0~300mA		20	120	mV
V_{OUT} temperature coefficient *	⊿V_{OUT} /⊿T	Tj=–20~+80°C, V_{IN}=5V I_{OUT}=30mA		100		ppm/°C
Ripple rejection *	RR	V_{IN}=5V, f=120Hz V_{RIPPLE}=1V_{P-P}, I_{OUT}=30mA	50	80		dB
Output noise voltage *	Vn	V_{IN}=5V, f=20~80kHz I_{OUT}=30mA, C_N=0.01µF		40	120	µVrms
CONT terminal current	I_{ON}	V_{CONT}=1.6V		5	10	µA
High threshold voltage	H		1.6		V_{IN}+0.3	V
Low threshold voltage	L		–0.3		0.4	V
Reset						
Detection voltage	VS	V_{IN}=H→L	4.11	4.20	4.29	V
Vs temperature coefficient *	⊿VS/⊿T	Tj=–20~80°C		100		ppm/°C
Hysteresis voltage	⊿VS	V_{IN}=H→L→H	100		200	mV
Low-level output voltage	V_{OL}	V_{IN}=3.9V, R_L=4.7kΩ		100	200	mV
Output leakage current	I_{OH}	V_{IN}=5V			±0.1	µA
Output current 1	I_{OL}	V_{IN}=3.9V, R_L=0Ω	5			mA
Output current 2 *	I_{OL}	V_{IN}=3.9V, R_L=0Ω, Ta=–20~+80°C	3			mA
"H" transmission delay time *	t_{PLH}	Cd=0.0µF		30	90	µS
Reset delay time	t_{PLH1}	V_{IN}=4V→5V, Cd=0.1µF	5	10	20	mS
"L" transmission delay time *	t_{PHL}			30	90	µS
Threshold operating voltage	V_{OPL}	V_{OL}=0.4V		0.65	0.85	V

Note 1: design guaranteed

Measuring Circuit

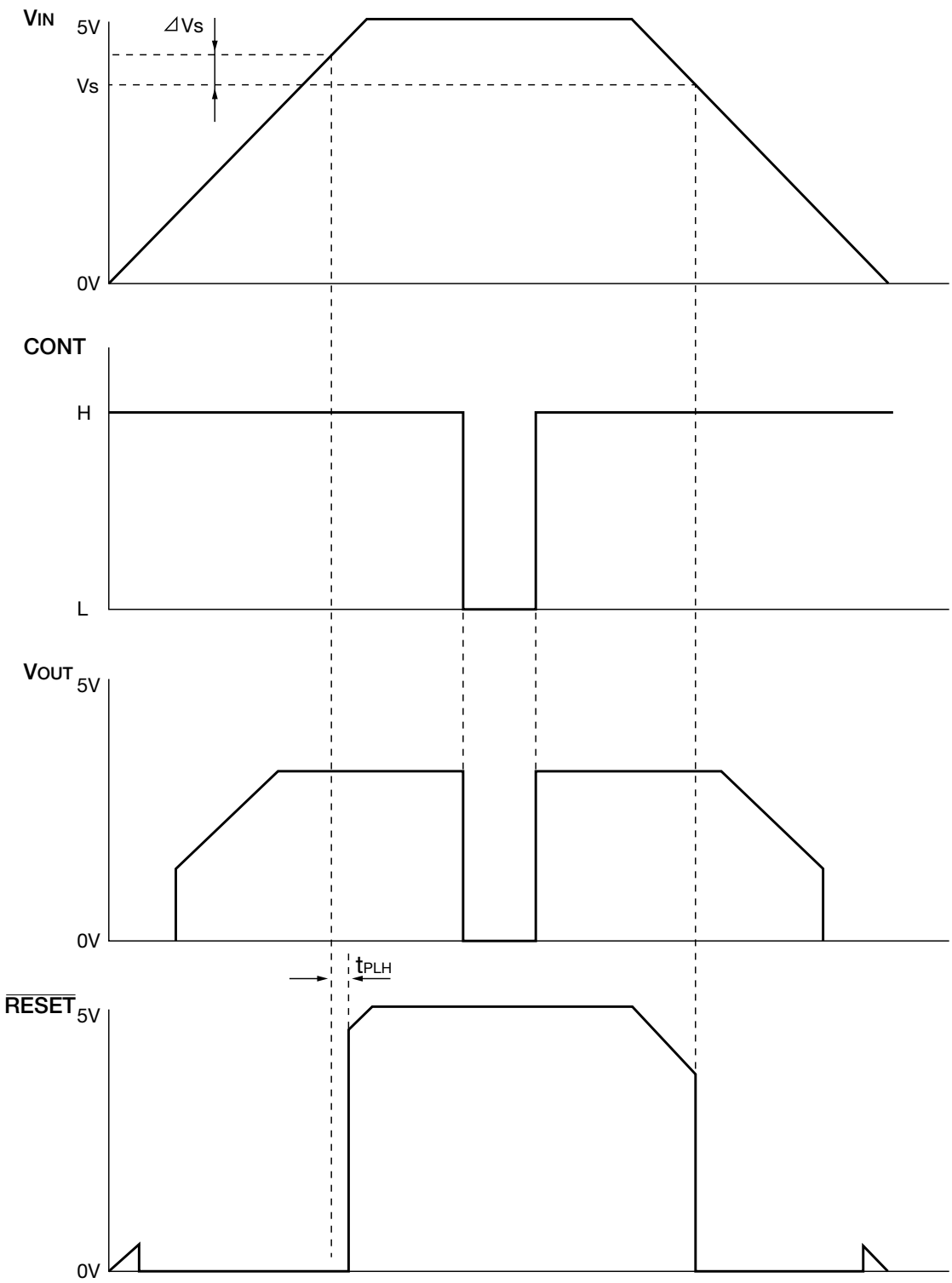


Application Circuit



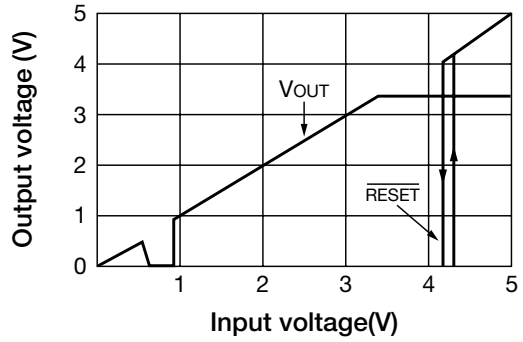
Note 1 : This regulator is not internally compensated and thus requires an external output-capacitor (C_{OUT}) for stability.

Timing Chart

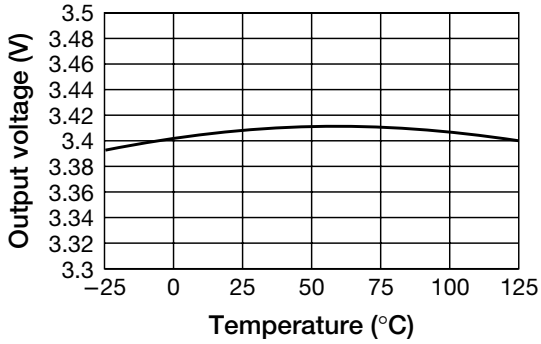


Characteristics

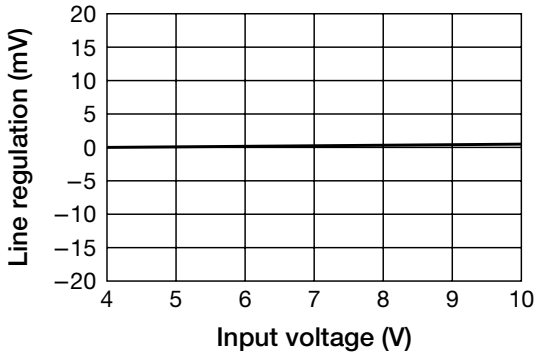
Detection voltage (I_{OUT}=0mA)



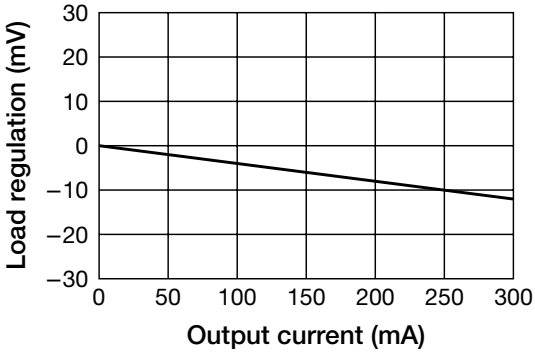
Output voltage vs temperature



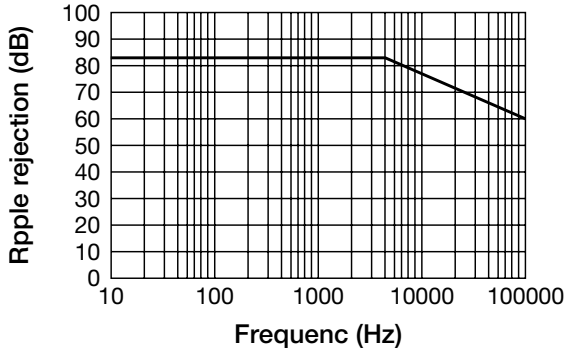
Line regulation



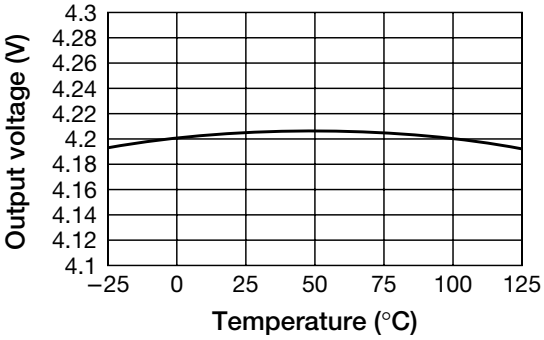
Load regulation



Ripple rejection



Detecting voltage vs temperature



Allowable loss