

NCP3335A

Ultra High Accuracy, Low Iq, 500 mA Low Dropout Regulator

The NCP3335A is a high performance, low dropout regulator. With accuracy of $\pm 0.9\%$ over line and load and ultra-low quiescent current and noise it encompasses all of the necessary features required by today's consumer electronics. This unique device is guaranteed to be stable without a minimum load current requirement and stable with any type of capacitor as small as 1.0 μF . The NCP3335A also comes equipped with sense and noise reduction pins to increase the overall utility of the device. The NCP3335A offers reverse bias protection.

Features

- High Accuracy Over Line and Load ($\pm 0.9\%$ at 25°C)
- Ultra-Low Dropout Voltage at Full Load (260 mV typ.)
- No Minimum Output Current Required for Stability
- Low Noise (35 μVrms w/10 nF C_{nr} and 56 μVrms w/out C_{nr})
- Low Shutdown Current (0.07 μA)
- Reverse Bias Protected
- 2.6 V to 12 V Supply Range
- Thermal Shutdown Protection
- Current Limitation
- Requires Only 1.0 μF Output Capacitance for Stability
- Stable with Any Type of Capacitor (including MLCC)
- Available in 2.5 V, 2.85 V, 3.3 V, 5.0 V and Adjustable Output Voltages
- These are Pb-Free Devices

Applications

- PCMCIA Card
- Cellular Phones
- Camcoders and Cameras
- Networking Systems, DSL/Cable Modems
- Cable Set-Top Box
- MP3/CD Players
- DSP Supply
- Displays and Monitors



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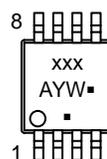


Micro8™
DM SUFFIX
CASE 846A

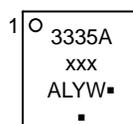


DFN10
MN SUFFIX
CASE 485C

MARKING DIAGRAM



Fixed Version	Adj Version
Pin 1, 2. V_{out}	Pin 1, 2. V_{out}
3. Sense	3. Adj
4. GND	4. GND
5. NR	5. NR
6. \overline{SD}	6. \overline{SD}
7, 8. V_{in}	7, 8. V_{in}



Fixed Version	Adj Version
Pin 1, 2. V_{out}	Pin 1, 2. V_{out}
3. Sense	3. Adj
4. GND	4. GND
5, 6. NC	5, 6. NC
7. NR	7. NR
8. \overline{SD}	8. \overline{SD}
9, 10. V_{in}	9, 10. V_{in}

Micro8

xxx = LIQ for 2.5 V
= LIR for 2.85 V
= LIS for 3.3 V
= LIT for 5.0 V
= LIO for Adj.

A = Assembly Location

L = Wafer Lot

Y = Year

W = Work Week

▪ = Pb-Free Package

(Note: Microdot may be in either location)

DFN10

xxx = 25 for 2.5 V
= 285 for 2.85 V
= 33 for 3.3 V
= 50 for 5.0 V
= ADJ for Adj.

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 12 of this data sheet.

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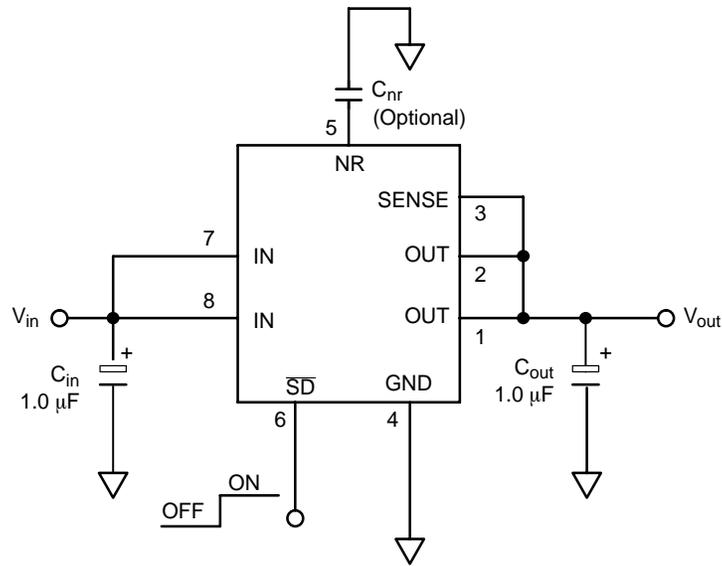


Figure 1. Typical Fixed Version Application Schematic (Micro8 Package)

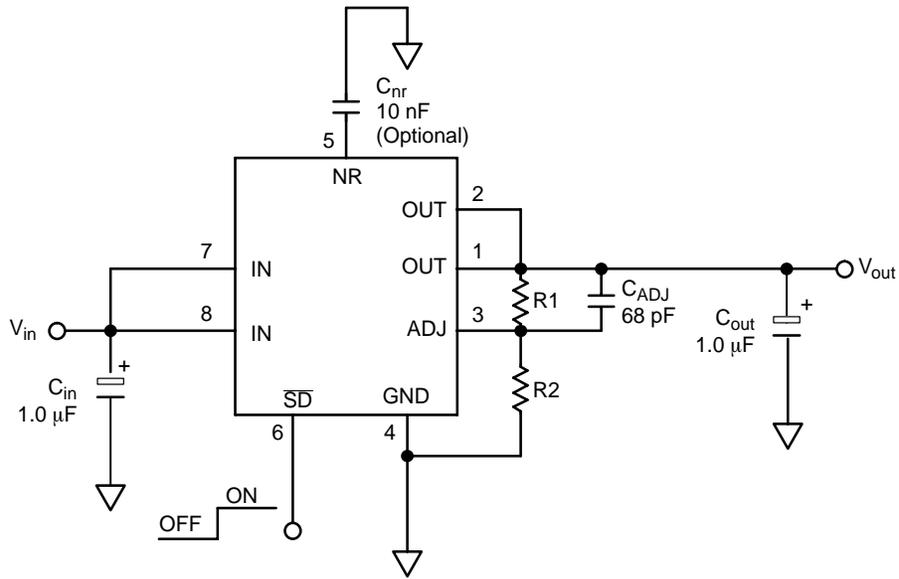


Figure 2. Typical Adjustable Version Application Schematic (Micro8 Package)

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PIN FUNCTION DESCRIPTION

Fixed Version

Micro8 Pin No.	DFN10 Pin No.	Pin Name	Description
1, 2	1, 2	V _{out}	Regulated output voltage. Bypass to ground with C _{out} ≥ 1.0 μF.
3	3	SENSE	For output voltage sensing, connect to Pins 1 and 2.
4	4	GND	Power Supply Ground
5	7	NR	Noise Reduction Pin. This is an optional pin used to further reduce noise.
6	8	SD	Shutdown pin. When not in use, this pin should be connected to the input pin.
7, 8	9, 10	V _{in}	Power Supply Input Voltage
–	5, 6	NC	Not Connected

Adjustable Version

1, 2	1, 2	V _{out}	Regulated output voltage. Bypass to ground with C _{out} ≥ 1.0 μF.
3	3	Adj	Adjustable pin; reference voltage = 1.25 V.
4	4	GND	Power Supply Ground
5	7	NR	Noise Reduction Pin. This is an optional pin used to further reduce noise.
6	8	SD	Shutdown pin. When not in use, this pin should be connected to the input pin.
7, 8	9, 10	V _{in}	Power Supply Input Voltage
–	5, 6	NC	Not Connected

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Input Voltage	V _{in}	–0.3 to +16	V
Output Voltage	V _{out}	–0.3 to V _{in} +0.3 or 10 V*	V
Shutdown Pin Voltage	V _{sh}	–0.3 to +16	V
Thermal Characteristics Thermal Resistance, Junction-to-Air	R _{θJA}	238	°C/W
Operating Junction Temperature Range, Micro8	T _J	–40 to +150	°C
Storage Temperature Range	T _{stg}	–50 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

NOTE: This device series contains ESD protection and exceeds the following tests:

Human Body Model (HBM) JESD 22–A114–B

Machine Model (MM) JESD 22–A115–A

*Which ever is less. Reverse bias protection feature valid only if V_{out} – V_{in} ≤ 7 V.

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ELECTRICAL CHARACTERISTICS – 2.5 V ($V_{out} = 2.5$ V typical, $V_{in} = 2.9$ V, $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$, unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
Output Voltage (Accuracy) $V_{in} = 2.9$ V to 6.5 V, $I_{load} = 0.1$ mA to 500 mA, $T_J = 25^\circ\text{C}$	V_{out}	-0.9% 2.477	2.5	+0.9% 2.523	V
Output Voltage (Accuracy) $V_{in} = 2.9$ V to 6.5 V, $I_{load} = 0.1$ mA to 500 mA, $T_J = 0^\circ\text{C}$ to $+85^\circ\text{C}$	V_{out}	-1.4% 2.465	2.5	+1.4% 2.535	V
Output Voltage (Accuracy), (Note 1) $V_{in} = 2.9$ V to 6.5 V, $I_{load} = 0.1$ mA to 500 mA, $T_J = -40^\circ\text{C}$ to $+150^\circ\text{C}$	V_{out}	-1.5% 2.462	2.5	+1.5% 2.538	V
Line Regulation $V_{in} = 2.9$ V to 12 V, $I_{load} = 0.1$ mA	LineReg		0.04		mV/V
Load Regulation $V_{in} = 2.9$ V, $I_{load} = 0.1$ mA to 500 mA	LoadReg		0.04		mV/mA
Dropout Voltage (See App Note) $I_{load} = 500$ mA (Note 2) $I_{load} = 300$ mA (Note 2) $I_{load} = 50$ mA $I_{load} = 0.1$ mA	V_{DO}			340 230 110 10	mV
Peak Output Current (See Figure 7)	I_{pk}	500	700	800	mA
Short Output Current (See Figure 7)	I_{sc}			900	mA
Thermal Shutdown	T_J		160		$^\circ\text{C}$
Ground Current In Regulation $I_{load} = 500$ mA (Note 2) $I_{load} = 300$ mA (Note 2) $I_{load} = 50$ mA $I_{load} = 0.1$ mA In Dropout $V_{in} = 2.4$ V, $I_{load} = 0.1$ mA In Shutdown $S_D = 0$ V	I_{GND} I_{GNDsh}		9.0 4.6 0.8 –	14 7.5 2.5 190 500	mA μA
Output Noise $C_{nr} = 0$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to 100 kHz, $C_{out} = 10$ μF $C_{nr} = 10$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to 100 kHz, $C_{out} = 10$ μF	V_{noise}		56 35		μVrms μVrms
Shutdown Threshold Voltage ON Threshold Voltage OFF		2.0		0.4	V V
\overline{SD} Input Current, $V_{SD} = 0$ V to 0.4 V or $V_{SD} = 2.0$ V to V_{in}	I_{SD}		0.07	1.0	μA
Output Current In Shutdown Mode, $V_{out} = 0$ V	I_{OSD}		0.07	1.0	μA
Reverse Bias Protection, Current Flowing from the Output Pin to GND ($V_{in} = 0$ V, $V_{out_forced} = 2.5$ V)	I_{OUTR}		10		μA

1. For output current capability for $T_J < 0^\circ\text{C}$, please refer to Figure 9.
2. T_A must be greater than 0°C .

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ELECTRICAL CHARACTERISTICS – 2.85 V ($V_{out} = 2.85$ V typical, $V_{in} = 3.25$ V, $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
Output Voltage (Accuracy) $V_{in} = 3.25$ V to 6.85 V, $I_{load} = 0.1$ mA to 500 mA, $T_J = 25^{\circ}\text{C}$	V_{out}	-0.9% 2.824	2.85	+0.9% 2.876	V
Output Voltage (Accuracy) $V_{in} = 3.25$ V to 6.85 V, $I_{load} = 0.1$ mA to 500 mA, $T_J = 0^{\circ}\text{C}$ to $+85^{\circ}\text{C}$	V_{out}	-1.4% 2.810	2.85	+1.4% 2.890	V
Output Voltage (Accuracy) (Note 3) $V_{in} = 3.25$ V to 6.85 V, $I_{load} = 0.1$ mA to 500 mA, $T_J = -40^{\circ}\text{C}$ to $+150^{\circ}\text{C}$	V_{out}	-1.5% 2.807	2.85	+1.5% 2.893	V
Line Regulation $V_{in} = 3.25$ V to 12 V, $I_{load} = 0.1$ mA	LineReg		0.04		mV/V
Load Regulation $V_{in} = 3.25$ V, $I_{load} = 0.1$ mA to 500 mA	LoadReg		0.04		mV/mA
Dropout Voltage (See App Note) $I_{load} = 500$ mA $I_{load} = 300$ mA $I_{load} = 50$ mA $I_{load} = 0.1$ mA	V_{DO}			340 230 110 10	mV
Peak Output Current (See Figure 7)	I_{pk}	500	700	800	mA
Short Output Current (See Figure 7)	I_{sc}			900	mA
Thermal Shutdown	T_J		160		$^{\circ}\text{C}$
Ground Current In Regulation $I_{load} = 500$ mA (Note 4) $I_{load} = 300$ mA $I_{load} = 50$ mA $I_{load} = 0.1$ mA In Dropout $V_{in} = 2.75$ V, $I_{load} = 0.1$ mA In Shutdown SD = 0 V	I_{GND} I_{GNDsh}		9.0 4.6 0.8 – – 0.07	14 7.5 2.5 190 500 1.0	mA μA
Output Noise $C_{nr} = 0$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to 100 kHz, $C_{out} = 10$ μF $C_{nr} = 10$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to 100 kHz, $C_{out} = 10$ μF	V_{noise}		61 40		μVrms μVrms
Shutdown Threshold Voltage ON Threshold Voltage OFF		2.0		0.4	V V
S_D Input Current, $V_{SD} = 0$ V to 0.4 V or $V_{SD} = 2.0$ V to V_{in}	I_{SD}		0.07	1.0	μA
Output Current In Shutdown Mode, $V_{out} = 0$ V	I_{OSD}		0.07	1.0	μA
Reverse Bias Protection, Current Flowing from the Output Pin to GND ($V_{in} = 0$ V, $V_{out_forced} = 2.85$ V)	I_{OUTR}		10		μA

3. For output current capability for $T_J < 0^{\circ}\text{C}$, please refer to Figure 8.
4. T_A must be greater than 0°C .

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ELECTRICAL CHARACTERISTICS – 3.3 V ($V_{out} = 3.3$ V typical, $V_{in} = 3.7$ V, $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
Output Voltage (Accuracy) $V_{in} = 3.7$ V to 7.3 V, $I_{load} = 0.1$ mA to 500 mA, $T_J = 25^{\circ}\text{C}$	V_{out}	-0.9% 3.270	3.3	+0.9% 3.330	V
Output Voltage (Accuracy) $V_{in} = 3.7$ V to 7.3 V, $I_{load} = 0.1$ mA to 500 mA, $T_J = 0^{\circ}\text{C}$ to $+85^{\circ}\text{C}$	V_{out}	-1.4% 3.254	3.3	+1.4% 3.346	V
Output Voltage (Accuracy) $V_{in} = 3.7$ V to 7.3 V, $I_{load} = 0.1$ mA to 500 mA, $T_J = -40^{\circ}\text{C}$ to $+150^{\circ}\text{C}$	V_{out}	-1.5% 3.250	3.3	+1.5% 3.350	V
Line Regulation $V_{in} = 3.7$ V to 12 V, $I_{load} = 0.1$ mA	LineReg		0.04		mV/V
Load Regulation $V_{in} = 3.7$ V, $I_{load} = 0.1$ mA to 500 mA	LoadReg		0.04		mV/mA
Dropout Voltage (See App Note) $I_{load} = 500$ mA $I_{load} = 300$ mA $I_{load} = 50$ mA $I_{load} = 0.1$ mA	V_{DO}			340 230 110 10	mV
Peak Output Current (See Figure 7)	I_{pk}	500	700	800	mA
Short Output Current (See Figure 7)	I_{sc}			900	mA
Thermal Shutdown	T_J		160		$^{\circ}\text{C}$
Ground Current In Regulation $I_{load} = 500$ mA (Note 5) $I_{load} = 300$ mA $I_{load} = 50$ mA $I_{load} = 0.1$ mA In Dropout $V_{in} = 3.2$ V, $I_{load} = 0.1$ mA In Shutdown $S_D = 0$ V	I_{GND} I_{GNDsh}		9.0 4.6 0.8 – – 0.07	14 7.5 2.5 190 500 1.0	mA μA
Output Noise $C_{nr} = 0$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to 100 kHz, $C_{out} = 10$ μF $C_{nr} = 10$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to 100 kHz, $C_{out} = 10$ μF	V_{noise}		69 46		μVrms μVrms
Shutdown Threshold Voltage ON Threshold Voltage OFF		2.0		0.4	V V
S_D Input Current, $V_{SD} = 0$ V to 0.4 V or $V_{SD} = 2.0$ V to V_{in}	I_{SD}		0.07	1.0	μA
Output Current In Shutdown Mode, $V_{out} = 0$ V	I_{OSD}		0.07	1.0	μA
Reverse Bias Protection, Current Flowing from the Output Pin to GND ($V_{in} = 0$ V, $V_{out_forced} = 3.3$ V)	I_{OUTR}		10		μA

5. T_A must be greater than 0°C .

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ELECTRICAL CHARACTERISTICS – 5.0 V ($V_{out} = 5.0$ V typical, $V_{in} = 5.4$ V, $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
Output Voltage (Accuracy) $V_{in} = 5.4$ V to 9.0 V, $I_{load} = 0.1$ mA to 500 mA, $T_J = 25^{\circ}\text{C}$	V_{out}	-0.9% 4.955	5.0	+0.9% 5.045	V
Output Voltage (Accuracy) $V_{in} = 5.4$ V to 9.0 V, $I_{load} = 0.1$ mA to 500 mA, $T_J = 0^{\circ}\text{C}$ to $+85^{\circ}\text{C}$	V_{out}	-1.4% 4.930	5.0	+1.4% 5.070	V
Output Voltage (Accuracy) $V_{in} = 5.4$ V to 9.0 V, $I_{load} = 0.1$ mA to 500 mA, $T_J = -40^{\circ}\text{C}$ to $+150^{\circ}\text{C}$	V_{out}	-1.5% 4.925	5.0	+1.5% 5.075	V
Line Regulation $V_{in} = 5.4$ V to 12 V, $I_{load} = 0.1$ mA	LineReg		0.04		mV/V
Load Regulation $V_{in} = 5.4$ V, $I_{load} = 0.1$ mA to 500 mA	LoadReg		0.04		mV/mA
Dropout Voltage (See App Note) $I_{load} = 500$ mA $I_{load} = 300$ mA $I_{load} = 50$ mA $I_{load} = 0.1$ mA	V_{DO}			340 230 110 10	mV
Peak Output Current (See Figure 7)	I_{pk}	500	700	830	mA
Short Output Current (See Figure 7)	I_{sc}			930	mA
Thermal Shutdown	T_J		160		$^{\circ}\text{C}$
Ground Current In Regulation $I_{load} = 500$ mA (Note 6) $I_{load} = 300$ mA (Note 6) $I_{load} = 50$ mA $I_{load} = 0.1$ mA In Dropout $V_{in} = V_{out} - 0.1$ V, $I_{load} = 0.1$ mA In Shutdown $S_D = 0$ V	I_{GND} I_{GNDsh}		9.0 4.6 0.8 – – – –	14 7.5 2.5 190 500	mA μA
Output Noise $C_{nr} = 0$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to 100 kHz, $C_{out} = 10$ μF $C_{nr} = 10$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to 100 kHz, $C_{out} = 10$ μF	V_{noise}		93 58		μVrms μVrms
Shutdown Threshold Voltage ON Threshold Voltage OFF		2.0		0.4	V V
S_D Input Current, $V_{SD} = 0$ V to 0.4 V or $V_{SD} = 2.0$ V to V_{in}	I_{SD}		0.07	1.0	μA
Output Current In Shutdown Mode, $V_{out} = 0$ V	I_{OSD}		0.07	1.0	μA
Reverse Bias Protection, Current Flowing from the Output Pin to GND ($V_{in} = 0$ V, $V_{out_forced} = 5.0$ V)	I_{OUTR}		10		μA

6. T_A must be greater than 0°C .

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ELECTRICAL CHARACTERISTICS – Adjustable ($V_{out} = 1.25$ V typical, $V_{in} = 2.9$ V, $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
Reference Voltage (Accuracy) $V_{in} = 2.9$ V to $V_{out} + 4.0$ V, $I_{load} = 0.1$ mA to 500 mA, $T_J = 25^{\circ}\text{C}$	V_{ref}	-0.9% 1.239	1.25	+0.9% 1.261	V
Reference Voltage (Accuracy) $V_{in} = 2.9$ V to $V_{out} + 4.0$ V, $I_{load} = 0.1$ mA to 500 mA, $T_J = 0^{\circ}\text{C}$ to $+85^{\circ}\text{C}$	V_{ref}	-1.4% 1.233	1.25	+1.4% 1.268	V
Reference Voltage (Accuracy) (Note 7) $V_{in} = 2.9$ V to $V_{out} + 4.0$ V, $I_{load} = 0.1$ mA to 500 mA, $T_J = -40^{\circ}\text{C}$ to $+150^{\circ}\text{C}$	V_{ref}	-1.5% 1.231	1.25	+1.5% 1.269	V
Line Regulation $V_{in} = 2.9$ V to 12 V, $I_{load} = 0.1$ mA	LineReg		0.04		mV/V
Load Regulation $V_{in} = 2.9$ V, $I_{load} = 0.1$ mA to 500 mA	LoadReg		0.04		mV/mA
Dropout Voltage (See App Note), $V_{out} = 2.5$ V to 10 V $I_{load} = 500$ mA (Note 8) $I_{load} = 300$ mA $I_{load} = 50$ mA $I_{load} = 0.1$ mA	V_{DO}			340 230 110 10	mV
Peak Output Current (Note 8) (See Figure 7)	I_{pk}	500	700	860	mA
Short Output Current (See Figure 7) $V_{out} \leq 3.3$ V $V_{out} > 3.3$ V	I_{sc}			900 990	mA
Thermal Shutdown	T_J		160		$^{\circ}\text{C}$
Ground Current In Regulation $I_{load} = 500$ mA (Note 8) $I_{load} = 300$ mA (Note 8) $I_{load} = 50$ mA $I_{load} = 0.1$ mA In Dropout $V_{in} = V_{out} - 0.1$ V, $I_{load} = 0.1$ mA In Shutdown $S_D = 0$ V	I_{GND} I_{GNDsh}		9.0 4.6 0.8 – – 0.07	14 7.5 2.5 190 500 1.0	mA μA
Output Noise $C_{nr} = 0$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to 100 kHz, $C_{out} = 10$ μF $C_{nr} = 10$ nF, $I_{load} = 500$ mA, $f = 10$ Hz to 100 kHz, $C_{out} = 10$ μF	V_{noise}		38 26		μV_{rms} μV_{rms}
Shutdown Threshold Voltage ON Threshold Voltage OFF		2.0		0.4	V V
S_D Input Current, $V_{SD} = 0$ V to 0.4 V or $V_{SD} = 2.0$ V to V_{in} $V_{in} \leq 5.4$ V $V_{in} > 5.4$ V	I_{SD}		0.07	1.0 5.0	μA
Output Current In Shutdown Mode, $V_{out} = 0$ V	I_{OSD}		0.07	1.0	μA
Reverse Bias Protection, Current Flowing from the Output Pin to GND ($V_{in} = 0$ V, $V_{out_forced} = V_{out}(\text{nom}) \leq 7$ V) (Note 9)	I_{OUTR}		1.0		μA

7. For output current capability for $T_J < 0^{\circ}\text{C}$, please refer to Figure 9.

8. T_A must be greater than 0°C .

9. Reverse bias protection feature valid only if $V_{out} - V_{in} \leq 7$ V.

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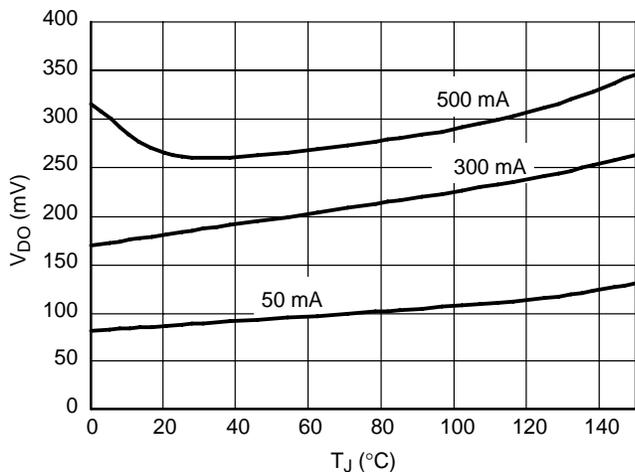


Figure 3. Dropout Voltage vs. Temperature

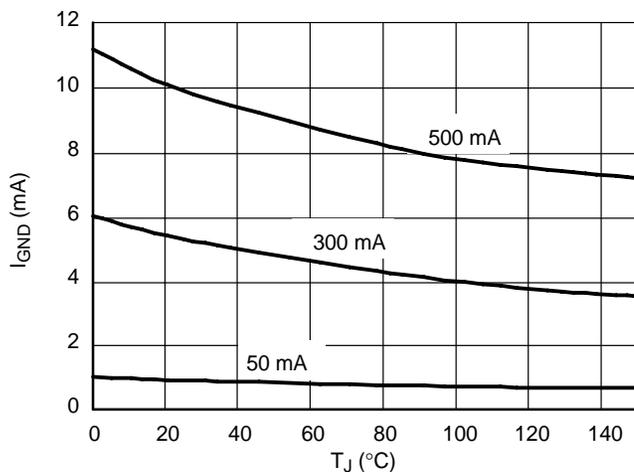


Figure 4. Ground Current vs. Temperature

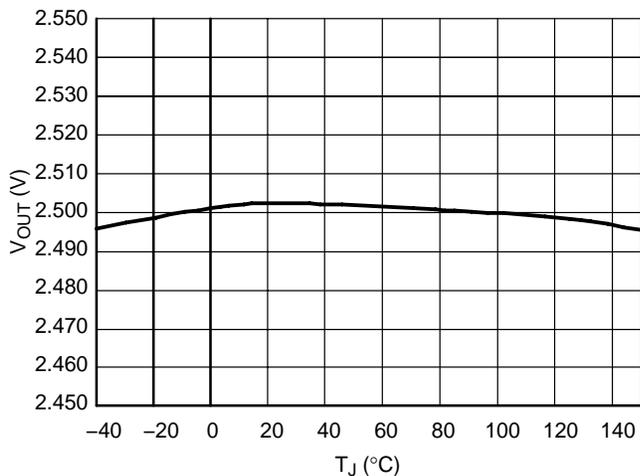


Figure 5. Output Voltage vs. Temperature

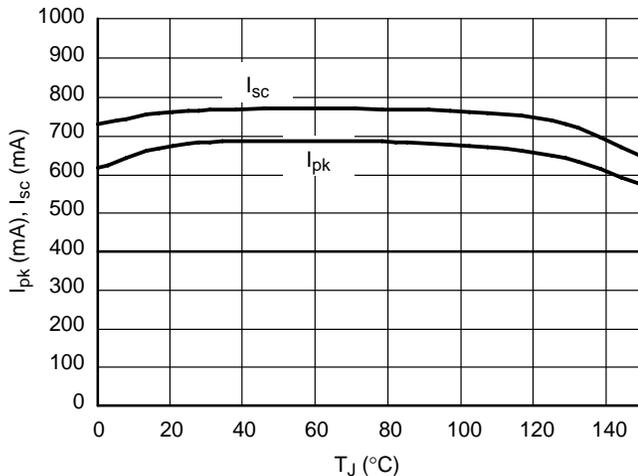
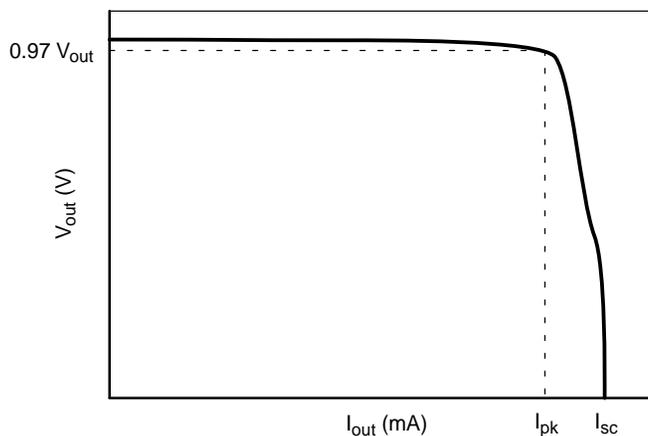


Figure 6. Peak and Short Current vs. Temperature



(For specific values of I_{pk} and I_{sc} , please refer to Figure 6)

Figure 7. Output Voltage vs. Output Current

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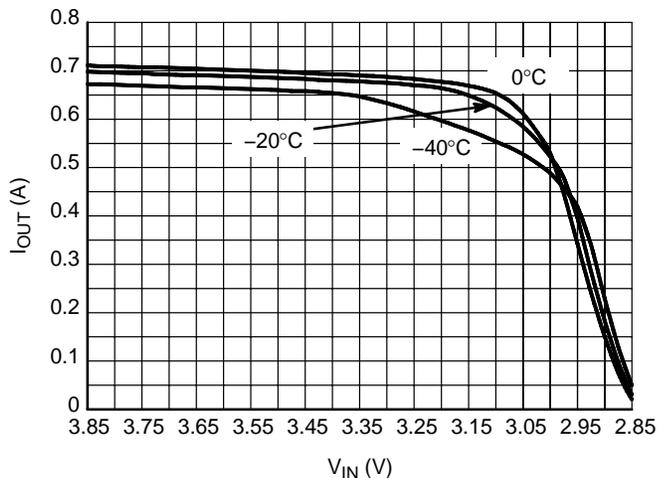


Figure 8. Output Current Capability for the 2.85 V Version

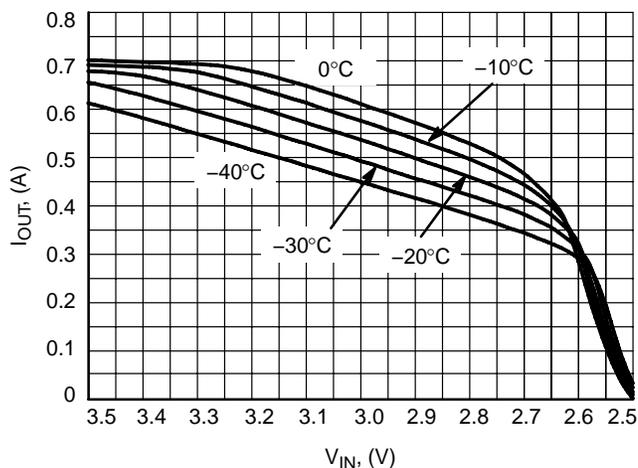


Figure 9. Output Current Capability for the 2.5 V Version

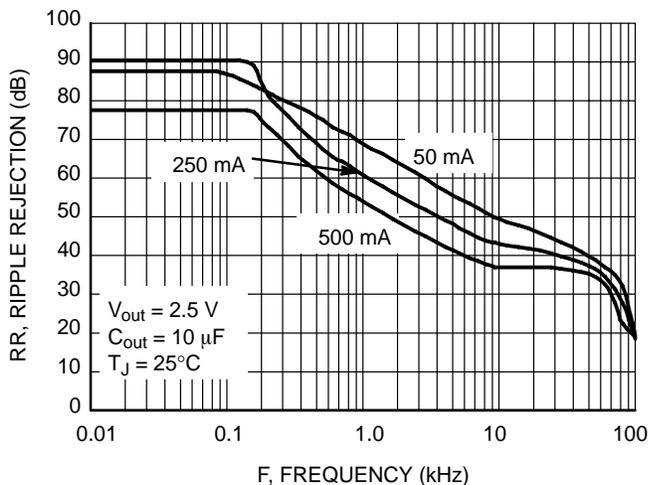


Figure 10. Ripple Rejection vs. Frequency

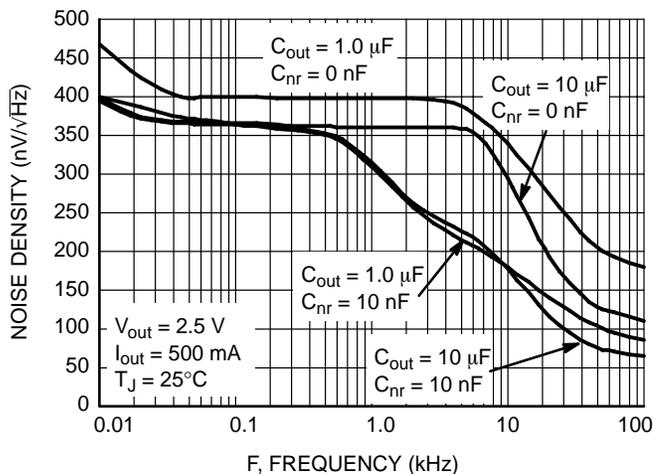


Figure 11. Output Noise Density

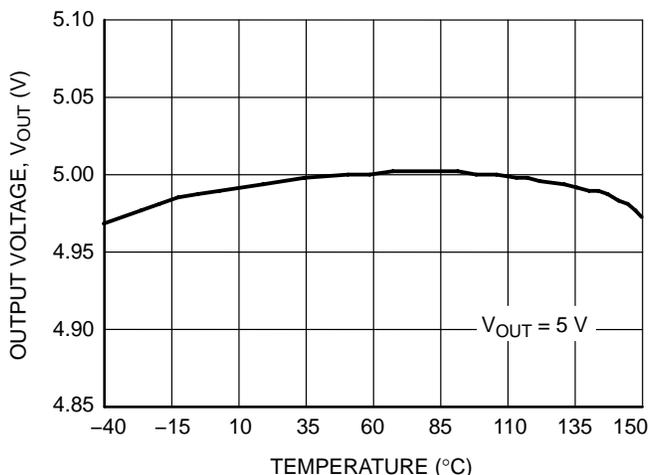


Figure 12. Output Voltage vs. Temperature

APPLICATIONS INFORMATION

Reverse Bias Protection

Reverse bias is a condition caused when the input voltage goes to zero, but the output voltage is kept high either by a large output capacitor or another source in the application which feeds the output pin.

Normally in a bipolar LDO all the current will flow from the output pin to input pin through the PN junction with limited current capability and with the potential to destroy the IC.

Due to an improved architecture, the NCP3335A can withstand up to 7.0 V on the output pin with virtually no current flowing from output pin to input pin, and only negligible amount of current (tens of μA) flowing from the output pin to ground for infinite duration.

Input Capacitor

An input capacitor of at least 1.0 μF , any type, is recommended to improve the transient response of the regulator and/or if the regulator is located more than a few inches from the power source. It will also reduce the circuit's sensitivity to the input line impedance at high frequencies. The capacitor should be mounted with the shortest possible track length directly across the regular's input terminals.

Output Capacitor

The NCP3335A remains stable with any type of capacitor as long as it fulfills its 1.0 μF requirement. There are no constraints on the minimum ESR and it will remain stable up to an ESR of 5.0 Ω . Larger capacitor values will improve the noise rejection and load transient response.

Noise Reduction Pin

Output noise can be greatly reduced by connecting a 10 nF capacitor (C_{nr}) between the noise reduction pin and ground (see Figure 1). In applications where very low noise is not required, the noise reduction pin can be left unconnected.

For the adjustable version, in addition to the 10 nF C_{nr} , a 68 pF capacitor connected in parallel with R1 (see Figure 2)

is recommended to further reduce output noise and improve stability.

Adjustable Operation

The output voltage can be set by using a resistor divider as shown in Figure 2 with a range of 1.25 to 10 V. The appropriate resistor divider can be found by solving the equation below. The recommended current through the resistor divider is from 10 μA to 100 μA . This can be accomplished by selecting resistors in the $\text{k}\Omega$ range. As result, the $I_{adj} * R_2$ becomes negligible in the equation and can be ignored.

$$V_{out} = 1.25 * \left(1 + \frac{R_1}{R_2}\right) + I_{adj} * R_2 \quad (\text{eq. 1})$$

Example:

For $V_{out} = 2.9 \text{ V}$, can use $R_1 = 36 \text{ k}\Omega$ and $R_2 = 27 \text{ k}\Omega$.

$$1.25 * \left(1 + \frac{36 \text{ k}\Omega}{27 \text{ k}\Omega}\right) = 2.91 \text{ V} \quad (\text{eq. 2})$$

Dropout Voltage

The voltage dropout is measured at 97% of the nominal output voltage.

Thermal Considerations

Internal thermal limiting circuitry is provided to protect the integrated circuit in the event that the maximum junction temperature is exceeded. This feature provides protection from a catastrophic device failure due to accidental overheating. This protection feature is not intended to be used as a substitute to heat sinking. The maximum power that can be dissipated, can be calculated with the equation below:

$$P_D = \frac{T_{J(max)} - T_A}{R_{\theta JA}} \quad (\text{eq. 3})$$

For improved thermal performance, contact the factory for the DFN package option. The DFN package includes an exposed metal pad that is specifically designed to reduce the junction to air thermal resistance, $R_{\theta JA}$.

NCP3335A

ORDERING INFORMATION

Device	Nominal Output Voltage	Package	Shipping†
NCP3335ADM250R2G	2.5 V	Micro8 (Pb-Free)	4000 / Tape & Reel
NCP3335ADM285R2G	2.85 V	Micro8 (Pb-Free)	4000 / Tape & Reel
NCP3335ADM330R2G	3.3 V	Micro8 (Pb-Free)	4000 / Tape & Reel
NCP3335ADM500R2G	5.0 V	Micro8 (Pb-Free)	4000 / Tape & Reel
NCP3335ADMADJR2G	Adj.	Micro8 (Pb-Free)	4000 / Tape & Reel
NCP3335AMN250R2G	2.5 V	DFN10 (Pb-Free)	3000 / Tape & Reel
NCP3335AMN285R2G	2.85 V	DFN10 (Pb-Free)	3000 / Tape & Reel
NCP3335AMN330R2G	3.3 V	DFN10 (Pb-Free)	3000 / Tape & Reel
NCP3335AMN500R2G	5.0 V	DFN10 (Pb-Free)	3000 / Tape & Reel
NCP3335AMNADJR2G	Adj.	DFN10 (Pb-Free)	3000 / Tape & Reel

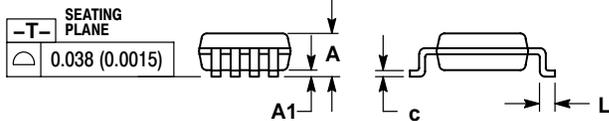
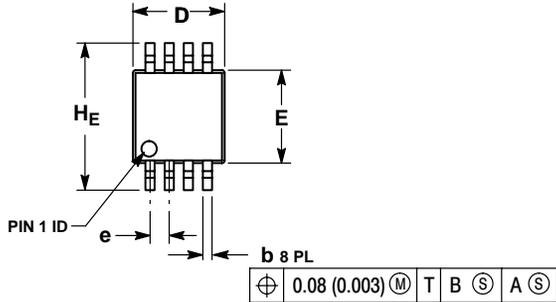
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*Please contact factory for other voltage options.

NCP3335A

PACKAGE DIMENSIONS

Micro8
CASE 846A-02
ISSUE G

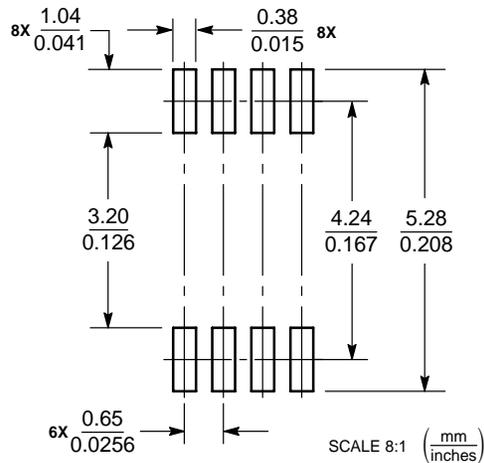


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSION A DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
4. DIMENSION B DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.25 (0.010) PER SIDE.
5. 846A-01 OBSOLETE, NEW STANDARD 846A-02.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	---	---	1.10	---	---	0.043
A1	0.05	0.08	0.15	0.002	0.003	0.006
b	0.25	0.33	0.40	0.010	0.013	0.016
c	0.13	0.18	0.23	0.005	0.007	0.009
D	2.90	3.00	3.10	0.114	0.118	0.122
E	2.90	3.00	3.10	0.114	0.118	0.122
e	0.65 BSC			0.026 BSC		
L	0.40	0.55	0.70	0.016	0.021	0.028
HE	4.75	4.90	5.05	0.187	0.193	0.199

SOLDERING FOOTPRINT*

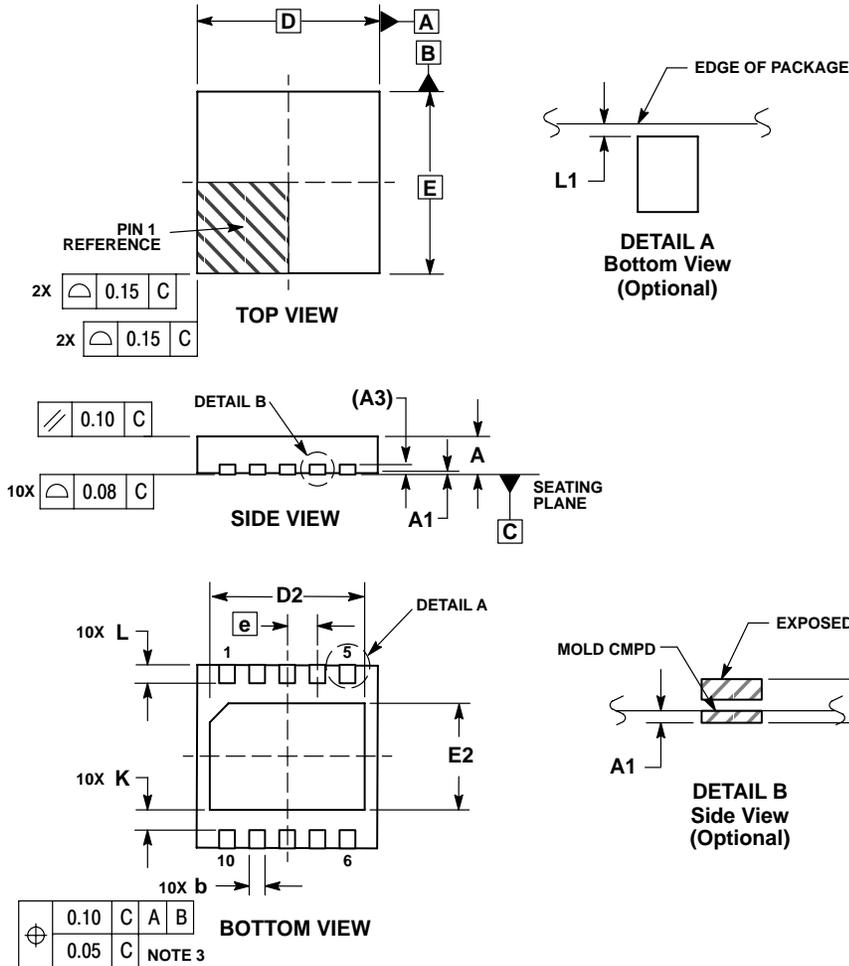


*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NCP3335A

PACKAGE DIMENSIONS

DFN10
CASE 485C-01
ISSUE A



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.25 AND 0.30 MM FROM TERMINAL.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.
5. TERMINAL b MAY HAVE MOLD COMPOUND MATERIAL ALONG SIDE EDGE. MOLD FLASHING MAY NOT EXCEED 30 MICRONS ONTO BOTTOM SURFACE OF TERMINAL b.
6. DETAILS A AND B SHOW OPTIONAL VIEWS FOR END OF TERMINAL LEAD AT EDGE OF PACKAGE.

MILLIMETERS		
DIM	MIN	MAX
A	0.80	1.00
A1	0.00	0.05
A3	0.20	REF
b	0.18	0.30
D	3.00	BSC
D2	2.45	2.55
E	3.00	BSC
E2	1.75	1.85
e	0.50	BSC
K	0.19	TYP
L	0.35	0.45
L1	0.00	0.03

The products described herein NCP3335A, may be covered by one or more of the following U.S. patents; 5,920,184, 5,966,004, and 5,834,926. There may be other patents pending.

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