

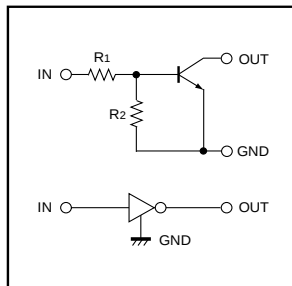
# Digital transistors (built in resistors)

## DTC113ZUA / DTC113ZKA / DTC113ZSA

### ●Features

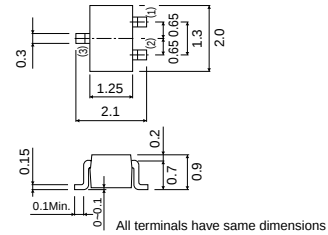
- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) Each bias resistor is a thin-film resistor. Since they are completely insulated, the input can be negatively biased. The insulation also eliminates most of the parasitic effects.
- 3) Only the on / off conditions need to be set for operation, making device design easy.

### ●Equivalent circuit



### ● External dimensions (Unit : mm)

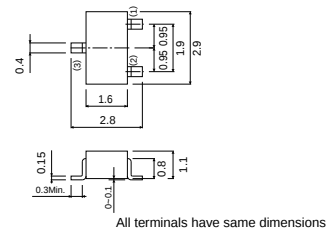
#### DTC113ZUA



ROHM : UMT3  
EIAJ : SC-70

(1) GND  
(2) IN  
(3) OUT  
Abbreviated symbol : 121

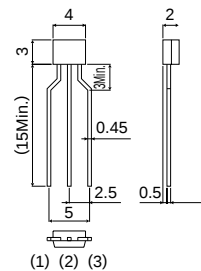
#### DTC113ZKA



ROHM : SMT3  
EIAJ : SC-59

(1) GND  
(2) IN  
(3) OUT  
Abbreviated symbol : E21

#### DTC113ZSA



ROHM : SPT  
EIAJ : SC-72

(1) GND  
(2) OUT  
(3) IN

# DTC113ZUA / DTC113ZKA / DTC113ZSA

## Transistors

### ●Absolute maximum ratings (Ta=25°C)

| Parameter            | Symbol               | Limits(DTC113Z□) |    |     | Unit |
|----------------------|----------------------|------------------|----|-----|------|
|                      |                      | UA               | KA | SA  |      |
| Supply voltage       | V <sub>CC</sub>      | 50               |    |     | V    |
| Input voltage        | V <sub>IN</sub>      | -5 to +10        |    |     | V    |
| Output current       | I <sub>O</sub>       | 100              |    |     | mA   |
|                      | I <sub>C(Max.)</sub> | 100              |    |     |      |
| Power dissipation    | P <sub>d</sub>       | 200              |    | 300 | mW   |
| Junction temperature | T <sub>j</sub>       | 150              |    |     | °C   |
| Storage temperature  | T <sub>stg</sub>     | -55 to +150      |    |     | °C   |

### ●Electrical characteristics (Ta=25°C)

| Parameter            | Symbol                         | Min. | Typ. | Max. | Unit | Conditions                                              |
|----------------------|--------------------------------|------|------|------|------|---------------------------------------------------------|
| Input voltage        | V <sub>I(off)</sub>            | –    | –    | 0.3  | V    | V <sub>CC</sub> =5V, I <sub>O</sub> =100μA              |
|                      | V <sub>I(on)</sub>             | 3    | –    | –    |      | V <sub>O</sub> =0.3V, I <sub>O</sub> =20mA              |
| Output voltage       | V <sub>O(on)</sub>             | –    | 0.1  | 0.3  | V    | I <sub>O</sub> /I <sub>I</sub> =10mA/0.5mA              |
| Input current        | I <sub>I</sub>                 | –    | –    | 7.2  | mA   | V <sub>I</sub> =5V                                      |
| Output current       | I <sub>O(off)</sub>            | –    | –    | 0.5  | μA   | V <sub>CC</sub> =50V, V <sub>I</sub> =0V                |
| DC current gain      | G <sub>I</sub>                 | 33   | –    | –    | –    | V <sub>O</sub> =5V, I <sub>O</sub> =5mA                 |
| Input resistance     | R <sub>I</sub>                 | 0.7  | 1    | 1.3  | kΩ   | –                                                       |
| Resistance ratio     | R <sub>Z</sub> /R <sub>I</sub> | 8    | 10   | 12   | –    | –                                                       |
| Transition frequency | f <sub>T</sub>                 | –    | 250  | –    | MHz  | V <sub>CE</sub> =10V, I <sub>E</sub> = –5mA, f=100MHz * |

\* Transition frequency of the device

### ●Packaging specifications

| Part No.  | Package                      | UMT3   | SMT3   | SPT    |
|-----------|------------------------------|--------|--------|--------|
|           | Packaging type               | Taping | Taping | Taping |
|           | Code                         | T106   | T146   | TP     |
|           | Basic ordering unit (pieces) | 3000   | 3000   | 5000   |
| DTC113ZUA |                              | ○      | –      | –      |
| DTC113ZKA |                              | –      | ○      | –      |
| DTC113ZSA |                              | –      | –      | ○      |

Transistors

●Electrical characteristic curves

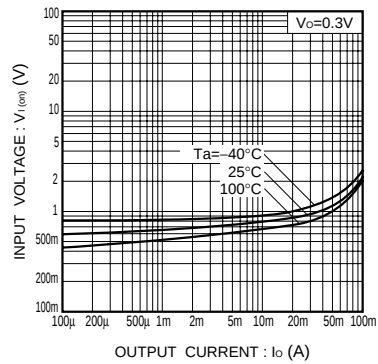


Fig.1 Input voltage vs. output current (ON characteristics)

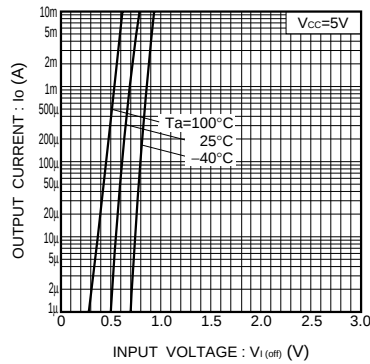


Fig.2 Output current vs. input voltage (OFF characteristics)

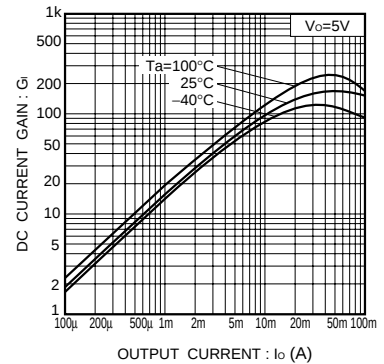


Fig.3 DC current gain vs. output current

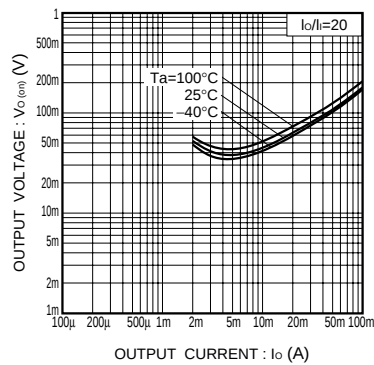


Fig.4 Output voltage vs. output current

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