

No.3823

2SK1728

N-Channel MOS Silicon FET

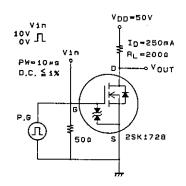
Very High-Speed Switching Applications

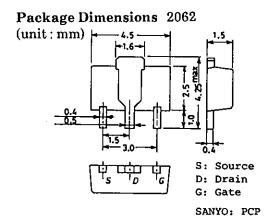
Features

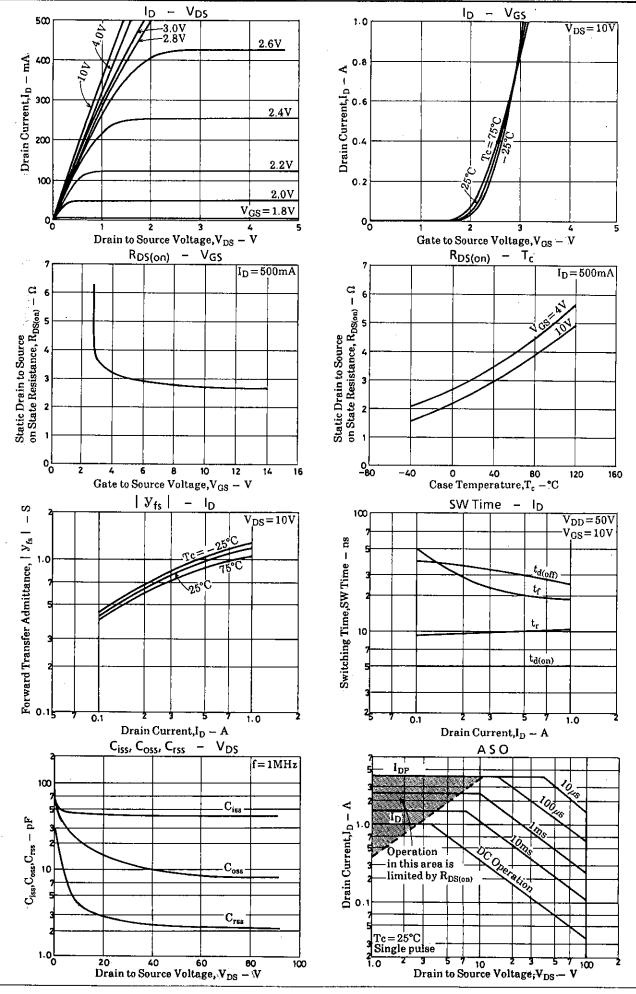
- · Low ON resistance.
- · Very high-speed switching.
- · Low-voltage drive.

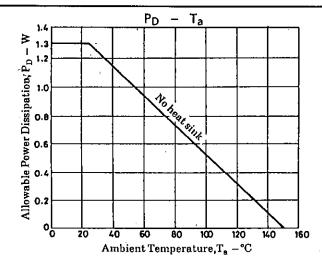
Absolute Maximum Ratings at Ta = 25°C unit						
Drain to Source Voltage	V_{DSS}			100	V	
Gate to Source Voltage	V_{GSS}		±15		v	
Drain Current(DC)	I _D		1		Å	
Drain Current(Pulse)	Î _{DP}	PW ≤ 10 μs, duty cycle ≤ 1%	4		A	
Allowable Power Dissipation	P_{D}	$T_c=25^{\circ}C$	3.5		w	
Tillowable I owel Dissipation	т D	Mounted on ceramic board			W	
		(250mm ² ×0.8mm)		1.3	¥¥	
Channel Temperature	Tch		150		$^{\circ}\mathrm{C}$	
Storage Temperature	Tstg		-55 to + 150		°C	
- · ·	_					
Electrical Characteristics at Ta=	:25°C		min	typ	max	unit
D-S Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 1 \text{mA}, V_{GS} = 0$	100			v .
Zero Gate Voltage	IDSS	$V_{DS} = 100 V, V_{GS} = 0$			100	μ A
Drain Current	200	, 40				,
Gate to Source Leakage Current	I_{GSS}	$V_{GS} = \pm 12V, V_{DS} = 0$			± 10	μ A
Cutoff Voltage	V _{GS(off)}	$V_{DS} = 10V, I_D = 1mA$	1.0		2.0	· v
Forward Transfer Admittance	$ \mathbf{\tilde{y}_{fs}} $	$V_{DS} = 10V, I_D = 500 \text{mA}$	0.6	1.0	-	S
Static Drain to Source	R _{DS(on)}	$I_D = 500 \text{mA}, V_{GS} = 10 \text{V}$		2.7	3.5	$\widetilde{\Omega}$
on State Resistance	R _{DS(on)}	$I_D = 500 \text{mA}, V_{GS} = 4V$		3.2	4.2	Ω
Input Capacitance	Ciss	$V_{DS} = 20V, f = 1MHz$		45		рF
Output Capacitance	Coss	$V_{DS} = 20V, f = 1MHz$		15		pF
Reverse Transfer Capacitance	Crss	$V_{DS} = 20V_f = 1MHz$		3		рF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		5		ns
Rise Time	t_r	// // // // // // // // // // // // //		10		ns
Turn-OFF Delay Time	t _{d(off)}	,		30		ns
Fall Time	t _f	,		20		ns
Diode Forward Voltage	v_{SD}	$I_{S} = 1A, V_{GS} = 0$		1.0		V
====== × vi mara i vivago	, 2n	15-111,165-0		1.0		Y

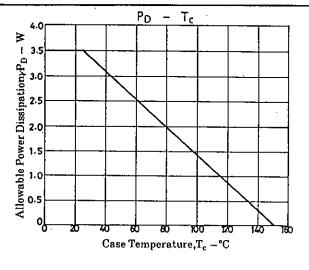
Switching Time Test Circuit











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