# GaAs IC 3 Bit Digital Attenuator 4 dB LSB Positive Control 0.75–2 GHz



**AA239-12** 

(Previously AD239-12)\*

#### **Features**

- Attenuation in 4 dB Steps to 28 dB with High Accuracy
- Single Positive Control Voltage for Each Bit
- +3 V to +5 V Operation
- Small Low Cost SOIC-8 Plastic Package

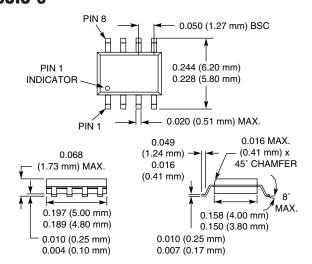
#### **Description**

The AA239-12 is a 3 bit, single positive control, 4 dB step GaAs IC FET digital attenuator in a low cost SOIC-8 plastic package. For positive operation external DC blocking capacitors are required on all RF ports.

The AA239-12 is particularly suited where high attenuation accuracy, low insertion loss and low intermodulation products are required.

Typical applications include cellular, radio, wireless data, wireless local loop and other gain level control circuits.

#### **SOIC-8**



# Electrical Specifications at 25°C (0, +3 V)

Parameter <sup>1</sup>	Frequency <sup>2</sup>	Min.	Тур.	Max.	Unit
Insertion Loss <sup>2</sup>	0.75–2.0 GHz		1.8	2.1	dB
Attenuation Range	0.75–2.0 GHz		28		dB
Attenuation Accuracy <sup>3</sup>	0.75–1.0 GHz 0.75–2.0 GHz	± (0.2 + 3% of Attenuation Setting in dB) ± (0.3 + 3% of Attenuation Setting in dB)		dB dB	
VSWR (I/O)	0.75–2.0 GHz		1.5:1	2.0:1	

## Operating Characteristics at 25°C (0, +3 V)

Parameter	Condition	Frequency	Min.	Тур.	Max.	Unit
Switching Characteristics <sup>4</sup>	Rise, Fall (10/90% or 90/10% RF)			0.7		μs
	On, Off (50% CTL to 90/10% RF)			1.0		μs
	Video Feedthru			50		mV
Input Power for 1 dB Compression	V <sub>S</sub> = +3 V	0.75-2.0 GHz		+20		dBm
	$V_{S} = +5 V$	0.75–2.0 GHz		+26		dBm
Intermodulation Intercept Point (IP3)	For Two-tone Input Power +10 dBm					
	$V_{S} = +3 \text{ V}$	0.75-2.0 GHz		+32		dBm
	$V_{S} = +5 V$	0.75–2.0 GHz		+45		dBm
Control Voltages	V <sub>Low</sub> = 0 to 0.2 V @ 20 μA Max.		•	•		
	$V_{High} = +3 \text{ V } @ 100 \mu \text{A Max. to } +5 \text{ V } @$	200 μA Max.				
	$V_S = V_{High} \pm 0.2 V$					

<sup>1.</sup> All measurements made in a 50  $\Omega$  system, unless otherwise specified.

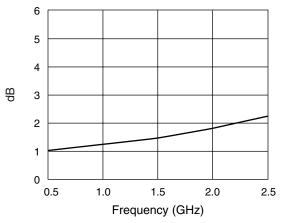
\*Due to conflict with another company's product numbering system, Alpha's attenuator products starting with the letters "AD" are being changed to "AA".

<sup>2.</sup> Insertion loss changes by 0.003 dB/°C

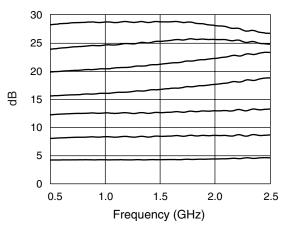
<sup>3.</sup> Attenuation referenced to insertion loss.

<sup>4.</sup> Video feedthru measured with 1 ns risetime pulse and 500 MHz bandwidth.

## Typical Performance Data (0, +3 V)



Insertion Loss vs. Frequency

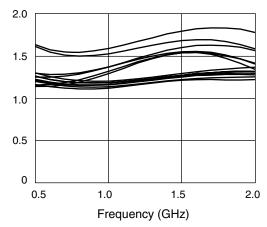


Attenuation vs. Frequency (All States)

#### **Truth Table**

V <sub>1</sub> 16 dB	V <sub>2</sub> 4 dB	V <sub>3</sub> 8 dB	Attenuation J <sub>1</sub> –J <sub>2</sub>
V <sub>High</sub>	V <sub>High</sub>	V <sub>High</sub>	Ins. Loss
V <sub>High</sub>	0	$V_{High}$	4 dB
V <sub>High</sub>	V <sub>High</sub>	0	8 dB
V <sub>High</sub>	0	0	12 dB
0	V <sub>High</sub>	V <sub>High</sub>	16 dB
0	0	V <sub>High</sub>	20 dB
0	V <sub>High</sub>	0	24 dB
0	0	0	28 dB

 $V_{High} = +3 \text{ to } +5 \text{ V } (V_{S} = V_{High} \pm 0.2 \text{ V}).$ 



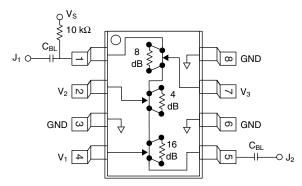
**VSWR vs. Frequency (All States)** 

## **Absolute Maximum Ratings**

Characteristic	Value
RF Input Power	1 W > 500 MHz 0/8 V 0.5 W @ 50 MHz 0/8 V
Supply Voltage	+8 V
Control Voltage	-0.2 V, +8 V
Operating Temperature	-40°C to +85°C
Storage Temperature	-65°C to +150°C

Note: Exceeding these parameters may cause irreversible damage.

### Pin Out



DC blocking capacitors ( $C_{\text{BL}}$ ) and biasing resistor must be supplied externally for positive operation.

 $C_{BL} = 100 \text{ pF}$  for operation >.75 GHz.