

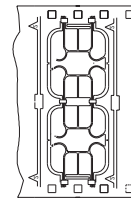


ST16601

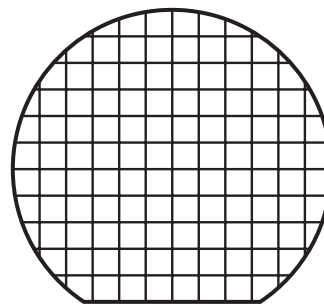
Smartcard MCU With 1088 Bytes EEPROM

DATA BRIEFING

- 8 BIT ARCHITECTURE CPU
- 6 KBytes of USER ROM SECTOR COMBINATIVE
- 1 KByte of SYSTEM ROM
- 128 Bytes of RAM
- 1088 Bytes of EEPROM, SECTOR COMBINATIVE
 - Highly reliable CMOS EEPROM technology
 - 10 year data retention
 - 300,000 Erase/Write cycle endurance
 - Protected One Time Programmable block (32 or 64 bytes)
 - 1 to 16 bytes block either Erase or Write in single cycle programming
- EXTENDED VOLTAGE OPERATION
 - V_{CC} Range: 2.7V to 5.5V
- SERIAL ACCESS, ISO 7816-3 COMPATIBLE
- POWER SAVING STANDBY MODE
- UP TO 5 MHz INTERNAL OPERATING FREQUENCY
- VERY HIGH SECURITY FEATURES INCLUDING EEPROM FLASH ERASE
- CONTACT ASSIGNMENT COMPATIBLE ISO 7816-2
- ESD PROTECTION GREATER THAN 5000V
- 2 OPERATING CONFIGURATIONS
 - ISSUER
 - USER



Micromodule (D3)



Wafer

Discussion

The ST16601 is based on 8 bit CPU core and includes on chip memories: 128 Bytes of RAM, 6 KBytes of USER ROM, 1 KByte of SYSTEM ROM, and 1088 Bytes of EEPROM.

