

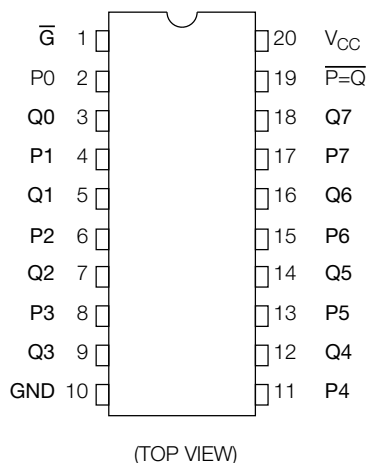
520: with Pull-up Resistor

Features:

- **High Speed:** $t_{pd} = 6.4\text{ns}$ (typ.) at $V_{CC} = 5\text{V}$
- **Low Power Dissipation:** $I_{CC} = 8\mu\text{A}$ (max.) at $T_a = 25^\circ\text{C}^*$
- **High Noise Immunity:** $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (min.)
- **Symmetrical Output Impedance:** $I_{OH} = I_{OL} = 24\text{mA}$ (min.). Capability of driving 50Ω transmission lines.
- **Balanced Propagation Delays:** $t_{pLH} = t_{pHL}$
- **Wide Operating Voltage Range:** V_{CC} (opr) = $2\text{V} \sim 5.5\text{V}$
- **Pin and Function Compatible with 74F521**
- **Available in DIP, SOIC and SOP Packages**

* for AC521 only

Pin Assignment



Truth Table

INPUTS		OUTPUT
P, Q	$\bar{G}$	$\overline{P=Q}$
$P=Q$	L	L
$P \neq Q$	L	H
X	H	H

X: Don't Care

The TC74AC520 and TC74AC521 are advanced high speed CMOS 8-BIT DIGITAL COMPARATORS fabricated with silicon gate and double-layer metal wiring C^2 MOS technology.

They achieve the high speed operation similar to equivalent Bipolar Schottky TTL, while maintaining the CMOS low power dissipation.

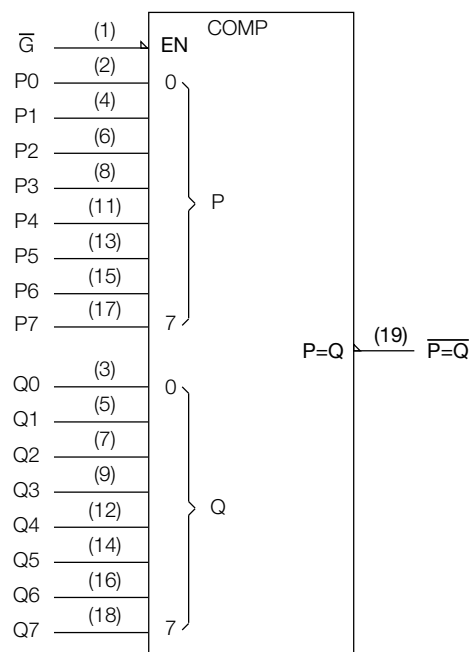
They compare two 8-bit binary or BCD words applied inputs P0~P7, and inputs Q0~Q7, and indicate whether or not they are equal.

The TC74AC520 is equipped with pull-up resistors (20Ω typ.) to inputs Q0~Q7 and features pull-up resistors on the Q inputs for switch data.

A signal active low enable is provided to facilitate cascading of several packages to compare words greater than 8 bits.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

IEC Logic Symbol



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Absolute Maximum Ratings

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage Range	V_{CC}	-0.5~7.0	V
DC Input Voltage	V_{IN}	-0.5~ $V_{CC} + 0.5$	V
DC Output Voltage	V_{OUT}	-0.5~ $V_{CC} + 0.5$	V
Input Diode Current	I_{IK}	± 20	mA
Output Diode Current	I_{OK}	± 50	mA
DC Output Current	I_{OUT}	± 50	mA
DC V_{CC} /Ground Current	I_{CC}	± 100	mA
Power Dissipation	P_D	500 (DIP) */180 (SOP)	mW
Storage Temperature	T_{stg}	-65~150	°C
Lead Temperature 10sec	T_L	300	°C

* 500mW in the range of $T_a = -40^{\circ}\text{C} \sim 65^{\circ}\text{C}$.
From $T_a = 65^{\circ}\text{C}$ to 85°C a derating factor of
-10mW/°C should be applied up to 300mW.

Recommended Operating Conditions

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage	V_{CC}	2.0~5.5	V
Input Voltage	V_{IN}	0~ V_{CC}	V
Output Voltage	V_{OUT}	0~ V_{CC}	V
Operating Temperature	T_{opr}	-40~85	°C
Input Rise and Fall Time	dt/dv	0~100 ($V_{CC} = 3.3 \pm 0.3\text{V}$) 0~20 ($V_{CC} = 5 \pm 0.5\text{V}$)	ns/v

DC Electrical Characteristics

PARAMETER	SYMBOL	TEST CONDITION		V _{CC}	Ta = 25°C			Ta = -40~85°C		UNIT
					Min.	Typ.	Max.	Min.	Max.	
High-Level Input Voltage	V _{IH}	—	—	2.0	1.50	—	—	1.50	—	V
				3.0	2.10	—	—	2.10	—	
				5.5	3.85	—	—	3.85	—	
Low-Level Input Voltage	V _{IL}	—	—	2.0	—	—	0.50	—	0.50	V
				3.0	—	—	0.90	—	0.90	
				5.5	—	—	1.65	—	1.65	
High-Level Output Voltage	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -50μA	2.0	1.9	2.0	—	1.9	—	V
				3.0	2.9	3.0	—	2.9	—	
				4.5	4.4	4.5	—	4.4	—	
			I _{OH} = -4mA	3.0	2.58	—	—	2.48	—	
			I _{OH} = -24mA	4.5	3.94	—	—	3.80	—	
			I _{OH} = -75mA*	5.5	—	—	—	3.85	—	
Low-Level Output Voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 50μA	2.0	—	0.0	0.1	—	0.1	V
				3.0	—	0.0	0.1	—	0.1	
				4.5	—	0.0	0.1	—	0.1	
			I _{OL} =12mA	3.0	—	—	0.36	—	0.44	
			I _{OL} = 24mA	4.5	—	—	0.36	—	0.44	
			I _{OL} = 75mA*	5.5	—	—	—	—	1.65	

* This spec indicates the capability of driving 50Ω transmission lines.
One output should be tested at a time for a 10ms maximum duration.

