

**HD-SDI DATA RETIMER EVALUATION BOARD****EV8301TF****FEATURES**

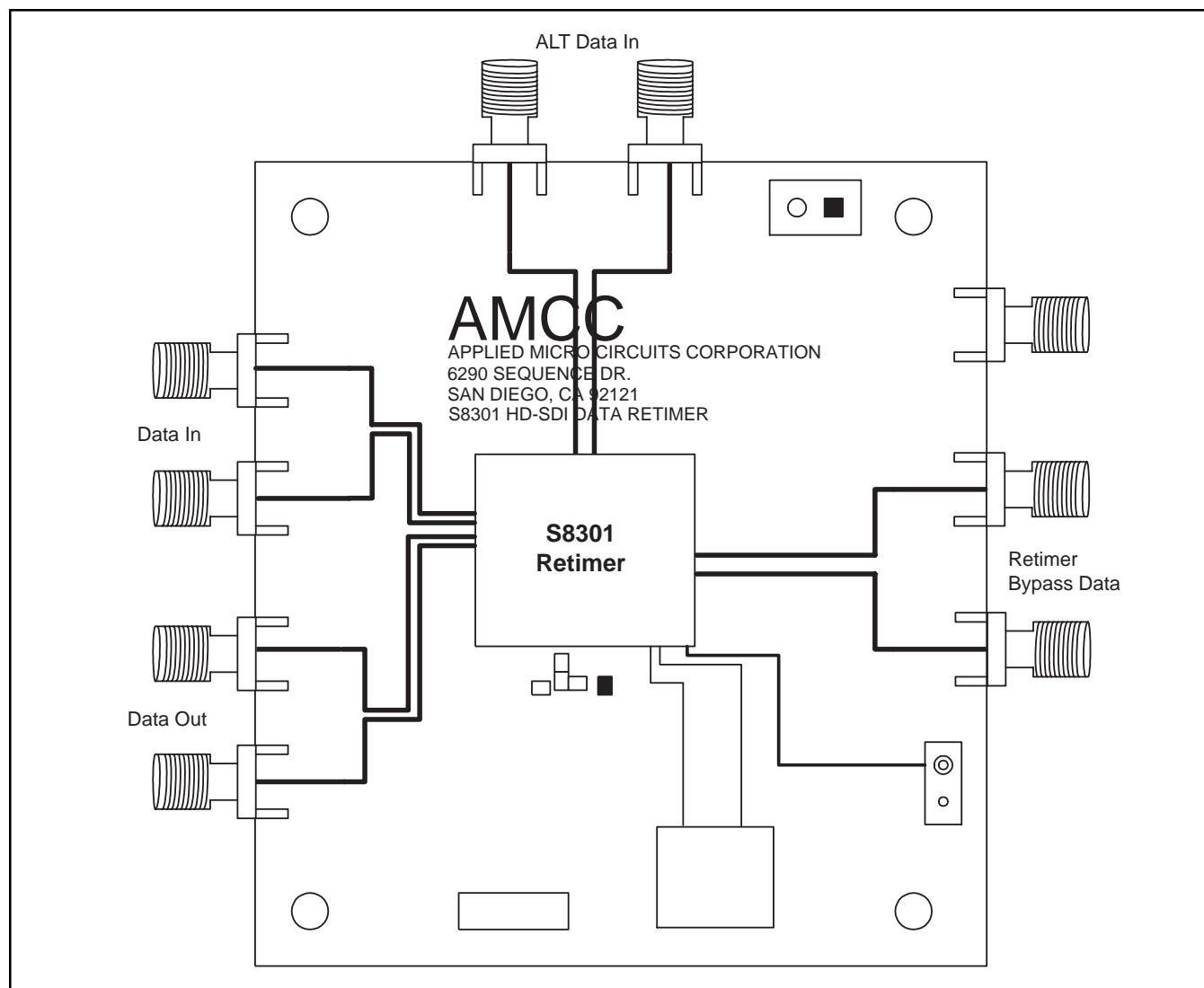
- Manual DIP switch control settings for static control functions
- BER and basic HD-SDI performance testing capability
- +3.3V LVPECL serial interface

**KIT CONTENTS**

- Populated Evaluation Board
- S8301 IC
- Documentation

**EVALUATION BOARD OVERVIEW**

The evaluation board allows users to become familiar with the functionality of the S8301 HD-SDI (High Definition – Serial Digital Interface) data retimer. Specifically, bit error rate and basic HD-SDI performance can be tested using the evaluation board. Figure 1 shows an outline of the S8301 Evaluation Board.

**Figure 1. S8301 Evaluation Board (Top View)**

**Ordering Information**

PREFIX	DEVICE	PACKAGE
EV – Evaluation Board	8301	TF – TQPF

XX  
Prefix

XXXX  
Device

XX  
Package



**Applied Micro Circuits Corporation • 6290 Sequence Dr., San Diego, CA 92121**

**Phone: (619) 450-9333 • (800) 755-2622 • Fax: (619) 450-9885**

**<http://www.amcc.com>**

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