

FEATURES

- Guaranteed 800mA Output
- Three Terminal Adjustable or Fixed 2.85V, 3V, 3.3V, 5V, 10V & 12V
- Very Low Quiescent Current
- Low Dropout Voltage of 1.2 Volts at Full Load
- Extremely Tight Load and Line Regulation
- Very Low Temperature Coefficient
- Fixed 2.8V Device for SCSI-II Active Terminator
- Logic-Controlled Electronic Shutdown
- Internal Overcurrent Limiting & Thermal Overload Protection
- Surface Mount Package SOT-223

APPLICATIONS

- SCSI-II Active Terminator
- Portable/ Palm Top / Notebook Computers
- Battery Chargers
- Disk Drives
- Portable Consumer Equipment
- Portable Instrumentation
- SMPS Post-Regulator

PRODUCT DESCRIPTION

The Calogic CLM1117 is a low power positive voltage regulator designed to meet 800mA output current and comply with SCSI-II specifications with a fixed output voltage of 2.85V. This device is an excellent choice for use in battery-powered applications, as active terminators for the SCSI bus, and portable computers. The CLM1117 features very low quiescent current and very low dropout voltage of 1.2V at a full load and lower as output current decreases. This product is available as an adjustable or fixed 2.85V, 3V, 3.3V, and 5V output voltages.

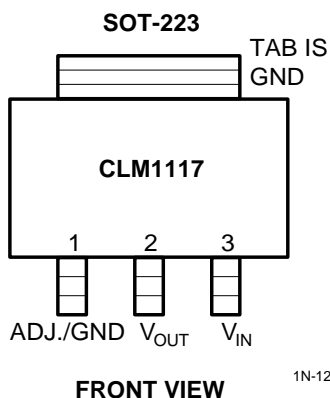
The device is offered in a 3-pin surface mount package SOT-223. The output capacitor of 10 μ F or larger is needed for output stability of CLM1117 as required by most of the other regulator circuits.

ORDERING INFORMATION

Part	Package	Temperature
CLM1117M	SOT-223	Industrial
CLM1117M-X	SOT-223	Industrial

X = Output Voltage. 5V = Standard.
Contact factory for 2.85V, 3.0V, 3.3V, 10V and 12V.

PIN CONNECTIONS



ABSOLUTE MAXIMUM RATINGS

Power Dissipation Internally Limited
 Lead Temp. (Soldering, 5 Seconds) 260°C
 Storage Temperature Range -65°C to +150°C
 Operating Junction Temperature Range
 CLM1117 -40°C to +125°C

Input Voltage 2.5V to 6V
 Input Supply Voltage -20V to +20V
 ESD Rating 2KV Min

ELECTRICAL CHARACTERISTICS: $V_{IN} = V_{OUT} + 1$, $T_A = 25^\circ\text{C}$, $CL = 3.3\mu\text{f}$, unless otherwise noted. **Boldface** apply over full operating temperature range.

PARAMETER	TYP	MIN	MAX	UNITS	CONDITIONS
Output Voltage	2.85	2.82	2.88	V	$I_{OUT} = 10\text{mA}$, $I_{IN} = 4.85\text{V}$ $0 \leq I_{OUT} \leq 800\text{mA}$, $4.25 \leq V_{IN} \leq 10\text{V}$ $0 \leq I_{OUT} \leq 500\text{mA}$, $V_{IN} = 4.05\text{V}$
	2.85	2.79	2.91		
	2.85	2.79	2.91		
	3.0	2.97	3.03		$I_{OUT} = 10\text{mA}$, $V_{IN} = 5.00\text{V}$ $0 \leq I_{OUT} \leq 800\text{mA}$, $4.50 \leq V_{IN} \leq 10\text{V}$
	3.0	2.94	3.06		
	3.30	3.27	3.33		$I_{OUT} = 10\text{mA}$, $I_{IN} = 5.30\text{V}$ $0 \leq I_{OUT} \leq 800\text{mA}$, $4.80 \leq V_{IN} \leq 10\text{V}$
	3.30	3.24	3.36		
	5.0	4.95	5.05		$I_{OUT} = 10\text{mA}$, $I_{IN} = 7.00\text{V}$ $0 \leq I_{OUT} \leq 800\text{mA}$, $6.50 \leq V_{IN} \leq 12\text{V}$
	5.0	4.90	5.10		
Output Voltage Temperature Stability			0.05	%	(Note 1)
Line Regulation	1.00		6.00	mV	$4.25\text{V} \leq V_{IN} \leq 10\text{V}$, $V_{OUT} = 2.85$, $I_{OUT} = 0$ $4.50\text{V} \leq V_{IN} \leq 12\text{V}$, $V_{OUT} = 3.00$, $I_{OUT} = 0$ $4.80\text{V} \leq V_{IN} \leq 12\text{V}$, $V_{OUT} = 3.30$, $I_{OUT} = 0$ $4.50\text{V} \leq V_{IN} \leq 15\text{V}$, $V_{OUT} = 5.00$, $I_{OUT} = 0$
	1.00		7.00		
	1.00		7.00		
	1.00		10.00		
Load Regulation	1.00		10.00	mV	$0 \leq I_{OUT} \leq 800\text{mA}$, $V_{IN} = 4.25\text{V}$, $V_{OUT} = 2.85$ $0 \leq I_{OUT} \leq 800\text{mA}$, $V_{IN} = 4.50\text{V}$, $V_{OUT} = 3.00$ $0 \leq I_{OUT} \leq 800\text{mA}$, $V_{IN} = 4.80\text{V}$, $V_{OUT} = 3.30$ $0 \leq I_{OUT} \leq 800\text{mA}$, $V_{IN} = 6.50\text{V}$, $V_{OUT} = 5.00$
	1.00		12.00		
	1.00		12.00		
	1.00		15.00		
Dropout Voltage (Note 2)	1.00		1.10	V	$I_L = 100\text{mA}$ $I_L = 500\text{mA}$ $I_L = 800\text{mA}$
	1.05		1.15		
	1.10		1.20		
Quiescent Voltage	5.00		10.00	mA	$4.25\text{V} \leq V_{IN} \leq 6.5\text{V}$
Current Limit	800	950	1200	mA	$(V_{IN} - V_{OUT}) = 5\text{V}$
Thermal Regulation	0.01		0.1	%/W	25°C, 30mS Pulse
Ripple Rejection	60		75	dB	$f_{RIPPLE} = 120\text{Hz}$, $(V_{IN} - V_{OUT}) = 3\text{V}$, $V_{RIPPLE} = 1\text{V}_{p-p}$
Long Term Stability			0.03	%	125°C, 1000Hrs
RMS Output Noise			0.003	%	% of V_{OUT} , $10\text{Hz} \leq f \leq 10\text{kHz}$
Thermal Resistance			15	°C/W°	Junction to case, at tab

Note 1: Output temperature coefficient is defined as the worst case voltage change divided by the total temperature range.

Note 2: Dropout voltage is defined as the input to output differential at which the output voltage drops 100mV below its nominal value measured at 1V differential at very low values of programmed output voltage, the minimum input supply voltage of 2V (2.3V over temperature) must be taken into account.

Note 3: Thermal regulation is defined as the change in output voltage at a time T after a change in power dissipation is applied, excluding load or line regulation effect.

APPLICATION NOTES

EXTERNAL CAPACITOR

To ensure the stability of the CLM1117 an output capacitor of at least 10μF (tantalum) or 50μF (aluminum) is required. The value may change based on the application requirements on the output load or temperature range. The capacitor equivalent series resistance (ESR) will effect the CLM1117 stability. The value of the ESR can vary from the type of capacitor used in the applications. The recommended value for ESR is 0.5Ω. The output capacitance could increase in size to above the minimum value. The larger value of output capacitance as high as 100μF can improve the load transient response.

SOLDERING METHODS

The CLM1117 SOT-223 package is designed to be compatible with infrared reflow or vapor-phase reflow soldering techniques. During soldering the non-active or mildly active fluxes may be used. The CLM1117 die is attached to the heatsink lead which exits opposite the input, output, and ground pins.

Hand soldering and wave soldering should be avoided since these methods can cause damage to the device with excessive thermal gradients on the package. The SOT-223 recommended soldering method are as follows: vapor phase reflow and infrared reflow with the component preheated to within 65°C of the soldering temperature range.

THERMAL CHARACTERISTICS

The thermal resistance of CLM1117 is C°/W from junction to tab and 31 C°/W from tab to ambient for a total of 46 C°/W from junction to ambient. The CLM1117 features the internal thermal limiting to protect the device during overload conditions. Special care needs to be taken during continuous load conditions to insure the maximum junction temperature does not exceed 125°C.

Taking the FR-4 printed circuit board and 1/16 thick with 1 ounce copper foil as an experiment (fig.1 & fig.2), the PCB material is effective at transmitting heat with the tab attached to the pad area and a ground plane layer on the backside of the substrate. Refer to table 1 for the results of the experiment.

The thermal interaction from other components in the application can effect the thermal resistance of the CLM1117.

FIGURE 1. CIRCUIT LAYOUT, THERMAL EXPERIMENTS

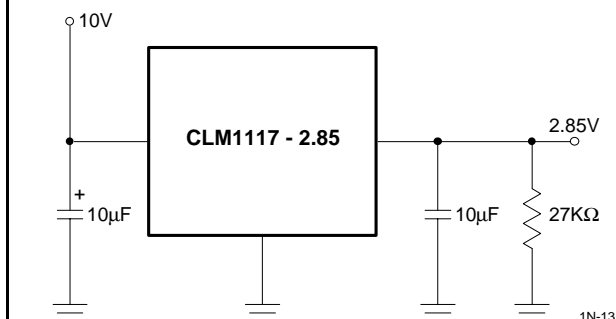
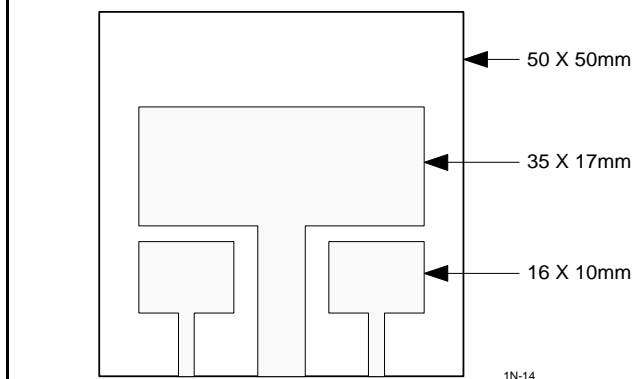


FIGURE 2. SUBSTRATE LAYOUT FOR SOT-223



The actual thermal resistance can be determined with experimentation. CLM1117 power dissipation is calculated as follows:

$$P_D = (V_{IN} - V_{OUT}) (I_{OUT})$$

Maximum Junction Temperature Range:

$$T_J = T_{ambient} (max) + P_D (thermal\ resistance\ (Junction-to-ambient))$$

Maximum Junction Temperature must not exceed 125°C.

$$P_O = (10V - 2.85) (105mA) = (7.15) (105mA) = 750mW$$

TABLE 1.

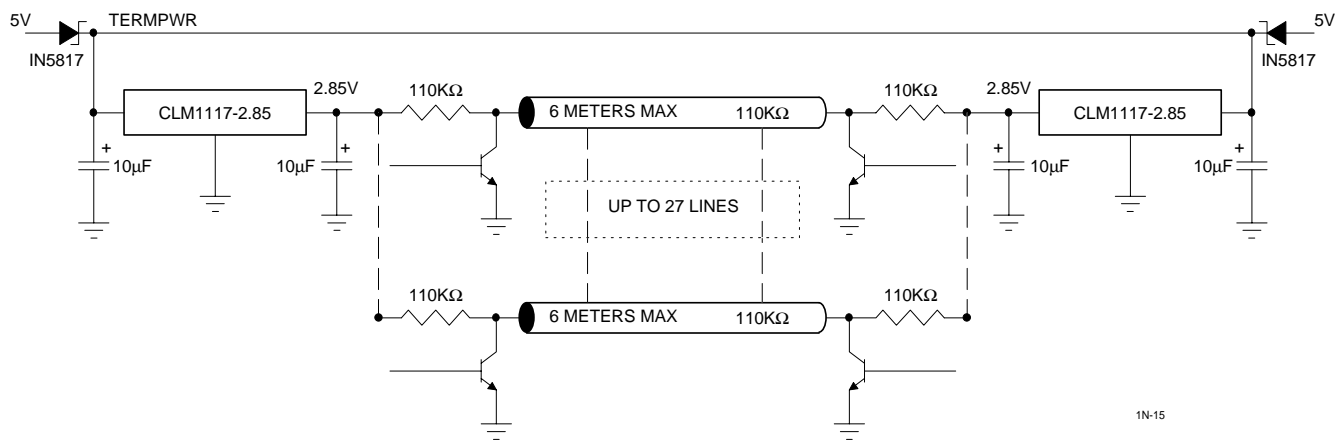
TOTAL PC BOARD AREA	TOPSIDE COPPER AREA	BACKSIDE COPPER AREA	THERMAL RESISTANCE JUNCTION TO AMBIENT
2500mm	2500mm	2500mm	46°C/W°
2500mm	1250mm	2500mm	47°C/W°
2500mm	950mm	2500mm	49°C/W°
2500mm	2500mm	0	51°C/W°
2500mm	1800mm	0	53°C/W°
1600mm	600mm	1600mm	55°C/W°
2500mm	1250mm	0	58°C/W°
2500mm	915mm	0	59°C/W°
1600mm	600mm	0	67°C/W°
900mm	240mm	900mm	72°C/W°
900mm	240mm	0	85°C/W°

ACTIVE VS PASSIVE TERMINATION

The improvement in noise immunity and low quiescent power of the terminator of less than 1W is the key advantage of active termination compared to passive termination. The CLM1117 significantly improves the noise level and signals on the bus by the terminating point due to variations in voltage at the termination point due to variation in the V_{term} power line.

Another advantage of active termination is the improvement in the impedance matching. The low impedance of the device makes the termination resistance more closely matched to the impedance of the cable. This improvement increases the data rate by 10 times, from 1mbyte/s to 10mbyte/s. The CLM1117 increases the protection and the reliability of the application since it features internal current limiting, overvoltage protection, and thermal protection.

ACTIVE TERMINATION CONFIGURATION



1N-15