

XDSL2™



Dual Channel XDSL2 Chip Set

G2237

The XDSL2 ILD2 chip set offers unsurpassed functionality and density for SHDSL. GlobespanVirata's XDSL2 chip set provides full G.991.2 (G.shdsl) compliance for both single and dual copper pair systems. In addition to powering SHDSL products, XDSL2 designs will benefit from full connectivity to legacy SDSL equipment. The XDSL2 chip sets deliver industry leading performance and interoperability as a result of the underlying architecture and transceiver firmware.

DSL equipment suppliers will benefit from GlobespanVirata's high density XDSL2 chip set. Leveraging GlobespanVirata's experience in fully programmable DSP's and high performance AFE's, designs benefit from GlobeSpan's commitment to ongoing product improvements and functional integration through software changes only.

SHDSL offers superior performance to legacy ISDN and SDSL systems through advanced trellis-coding and spectral efficiency. The benefits of SHDSL's trellis coding are improved overall reach as well as substantially improved performance in the presence of noise. Higher performance translates into more customers reached, resulting in greater revenue potential for service providers. As well as improved performance, SHDSL eliminates the spectral compatibility issues associated with 2B1Q SDSL. SHDSL has been engineered to coexist with ADSL and HDSL2 in the public network binders.

GlobespanVirata's XDSL2 SHDSL chip set provides value added features well beyond the scope of existing DSL market segments, enabling system vendors to enter new markets and significantly differentiate their offerings. Examples of such features include ISDSL support, provision for rates up to 4.6 Mbps per channel for MTU/MDU applications, and SHDSL 4 wire support on the same hardware platform.

To accelerate OEM time-to-market, GlobespanVirata provides fully documented, turnkey transceiver reference designs, development systems, and technical support.



The XDSL2 Chip Set*

* Chips as depicted are for representation only. Actual chips may vary from depiction.

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Features and Benefits

- High density transceiver consisting of a 144-pin TQFP DSP/Framer and 28-pin SSOP AFE/LD
- Optimal performance vs power design
- Compliant with G.991.2
- CO or CP selectable
- TDM and UTOPIA interfaces supported
- PAM, QAM, 2B1Q modulation supported
- Supports rates up to 4.6 mb/s per port

Applications

- High Density Access Multiplexers
- T1/E1 Line cards and Repeaters
- Router and Bridge Customer Premises Equipment
- DAML Systems
- Leased Line Services
- Cellular Base Station



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