2.85V HCMOS CERAMIC SMD OSCILLATOR WITH STANDBY

F520L SERIES

FEATURES

- 2.85V Operation
- HCMOS Output
- Standby Function
- Tape and Reel (2,000 pcs. STD)
- Pb Free

OPTIONS

- 5.0V (F550L) Version Available

• MODEL NUMBER SELECTION

Model Number	Frequency Stability[1]	Operating Temperature (°C)	Frequency Range (MHz)
F520L	±100PPM	-10 ~ +70	1.544 ~ 50.000
F520LR	±100PPM	-40 ~ +85	1.544 ~ 50.000
F525L	±50PPM	-10 ~ +70	1.544 ~ 50.000
F525LR	±50PPM	-40 ~ +85	1.544 ~ 50.000
F526L	±25PPM	-10 ~ +70	1.544 ~ 50.000
F526LR	±25PPM	-40 ~ +85	1.544 ~ 50.000
F528L	±20PPM	-10 ~ +70	1.544 ~ 50.000

• ELECTRICAL CHARACTERISTICS

PARAMETERS	MAX (unless otherwise noted)
Frequency Range (FO)	1.544 ~ 50.000 MHz
Storage Temperature Range (TSTG)	-55ºC ~ +125ºC
Supply Voltage (VDD)	2.85V ± 5%
Input Current (IDD)	
1.544 ~ 32.000 MHz	12mA
32.000+ ~ 50.000 MHz	20mA
Output Symmetry (50% VDD)	45% ~ 55%
Rise Time (10% ~ 90% VDD) (TR)	5nS
Fall Time (90% ~ 10% VDD) (TF)	5nS
Output Voltage (VOL)	10% VDD
(VOH)	90% VDD Min
Output Current (IOL)	4mA Min
(IOH)	-4mA Min
Output Load HCMOS	15pF
Standby Current	10µA
Start-up Time (Ts)	10mS
Output Disable Time [2]	150nS
Output Enable Time [2]	10mS

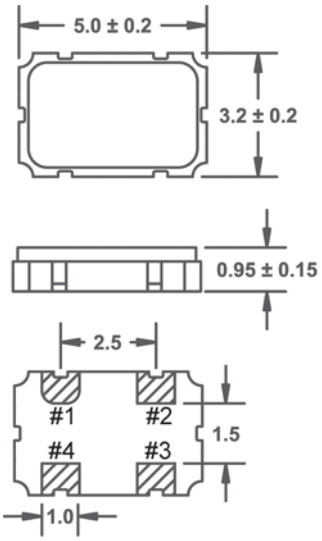
[1] Inclusive of 25ºC tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration.

[2] An internal pullup resistor from pin 1 to pin 4 allows active output if pin 1 is left open.

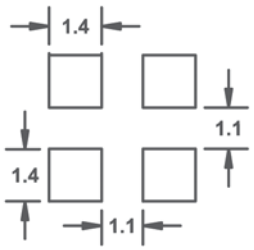
See page 30 for mechanical specifications, test circuits, and output waveform.

Note: A 0.01µF bypass capacitor should be placed between VDD (Pin 4) and GND (Pin 2) to minimize power supply line noise.

All specifications subject to change without notice. *Rev. 10/15/03*



Recommended Solder Pad Layout



Pin Connections

#1 E/D #3 Output
#2 GND #4 V DD

All dimensions are in millimeters.

• ENABLE / DISABLE FUNCTION

$\overline{INH}$ (Pin 1)	OUTPUT (Pin 3)
OPEN [2]	ACTIVE
'1' Level VIH ≥ 70% VDD	ACTIVE
'0' Level VIL ≤ 30% VDD	High Z

See page 60 for tape and reel specifications.