

3.3V LOW PROFILE SMD VCXO

FVS25AXT

FEATURES

- 3.3V Operation
- HCMOS Output
- Enable/Disable
- Tape and Reel (2,000 pcs. STD)
- Pb Free



• ELECTRICAL CHARACTERISTICS

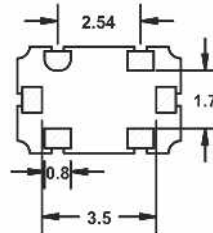
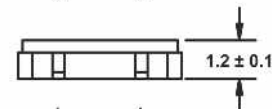
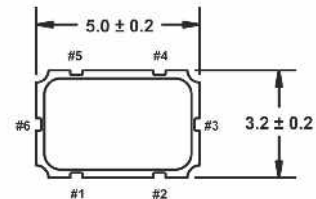
PARAMETERS	MAX (unless otherwise noted)
Frequency Range (Fo)	2.000 ~ 52.000 MHz
Temperature Range	
Operating (TOPR)	-10°C ~ +70°C
Storage (TSTG)	-40°C ~ +85°C
Frequency Stability	±50PPM ¹
Pullability	±100PPM
Supply Voltage (VDD)	3.3V ± 5%
Control Voltage (Vc)	1.65V ± 1.5V
Input Current (IDD)	
2.000 ~ 30.000 MHz	15mA
30.000+ ~ 52.000 MHz	25mA
Output Symmetry (50% VDD)	40% ~ 60%
Rise Time (20% ~ 80% VDD) (TR)	5nS
Fall Time (80% ~ 20% VDD) (TF)	5nS
Output Voltage (VOL)	10% VDD
(VOH)	90% VDD Min
Output Current (IOL)	4.0mA Min
(IOH)	-1.0mA Min
Output Load (HCMOS)	15pF
Start-up Time (Ts)	10mS
Enable/Disable Time ²	150nS
Frequency Linearity	±10%
Modulation Bandwidth	20 kHz

¹ Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock, vibration, and Vc = 1.65V.

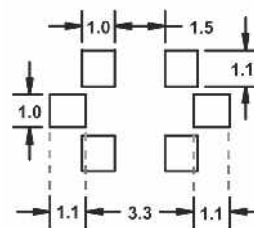
² An internal pullup resistor from pin 1 to pin 4 allows active output if pin 1 is left open.

Note: A 0.01μF bypass capacitor should be placed between VDD (Pin 5) and GND (Pin 2) to minimize power supply line noise.

All specifications subject to change without notice. Rev. 02/10/03



Recommended Solder Pad Layout



Pin Connections

#1 Vc	#4 Output
#2 GND	#5 VDD
#3 N.C.	#6 E/D

All dimensions are in millimeters.

• ENABLE / DISABLE FUNCTION

INH (Pin 6)	OUTPUT (Pin 4)
OPEN ²	ACTIVE
'1' Level VIH ≥ 70% VDD	ACTIVE
'0' Level VIL ≤ 30% VDD	High Z

See page 79 for tape and reel specifications.