

DF005S Thru DF10S



1 AMP SURFACE MOUNT GLASS PASSIVATED SILICON BRIDGE

FEATURES

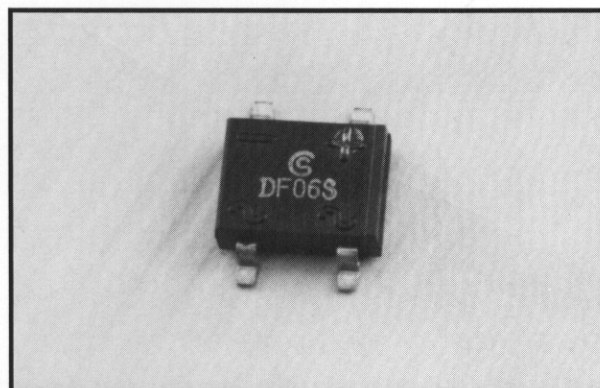
- Rating to 1000V PRV
- Surge overload rating to 45 Amperes peak
- Ideal for printed circuit board
- Reliable low cost construction utilizing molded plastic technique results in inexpensive product
- UL recognized: File #E106441
- UL recognized 94V-O plastic material

Mechanical Data

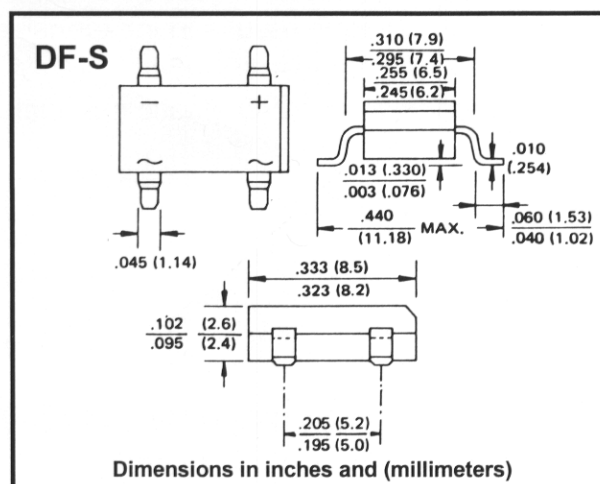
- Case: Molded plastic
- Leads: Tin plated copper
- Leads solderable per MIL-STD-202, Method 208
- Polarity: Symbols molded on body
- Weight: 0.02 ounce, 0.38 grams

Maximum Ratings & Characteristics

- Ratings at 25° C ambient temperature unless otherwise specified
- Single phase, half wave, 60Hz, resistive or inductive load
- For capacitive load, derate current by 20%



Outline Drawing



		DF005S	DF01S	DF02S	DF04S	DF06S	DF08S	DF10S	Units
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Output Current @ $T_A = 40^\circ\text{C}$	$I_{(AV)}$	1.0							A
Peak Forward Surge Current 8.3 ms Single Half-Sine-Wave Superimposed On Rated Load	I_{FSM}	50							A
Maximum DC Forward Voltage AT 1.0A DC	V_F	1.1							V
Maximum DC Reverse Current @ $T_A = 25^\circ\text{C}$ At Rated DC Blocking Voltage @ $T_A = 125^\circ\text{C}$	I_R	10 500							μA
$I^2 t$ Rating for Fusing ($t < 8.3\text{ms}$)	$I^2 t$	10.4							A^2S
Typical Thermal Resistance per Element	C_J	25							pF
Typical Thermal Resistance	R_{THJC}	40							$^\circ\text{C/W}$
Operating Temperature Range	T_J	-55 to +125							$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 to +150							$^\circ\text{C}$