

2SJ527(L),2SJ527(S)

Silicon P Channel MOS FET
High Speed Power Switching

HITACHI

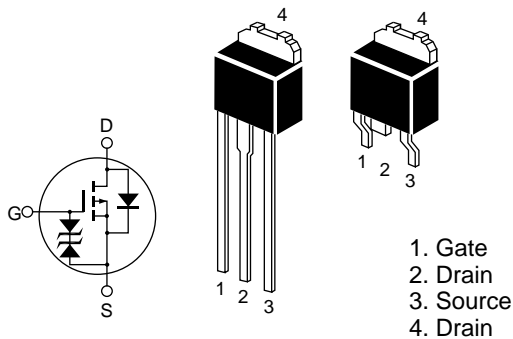
ADE-208-640A (Z)
2nd. Edition
Jun 1998

Features

- Low on-resistance
 $R_{DS(on)} = 0.3 \Omega$ typ.
- Low drive current
- 4 V gate drive devices
- High speed switching

Outline

DPAK-1



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	−60	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	−5	A
Drain peak current	I _{D(pulse)} ^{Note1}	−20	A
Body-drain diode reverse drain current	I _{DR}	−5	A
Avalanche current	I _{AP} ^{Note3}	−5	A
Avalanche energy	E _{AR} ^{Note3}	2.1	mJ
Channel dissipation	P _{ch} ^{Note2}	20	W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	−55 to +150	°C

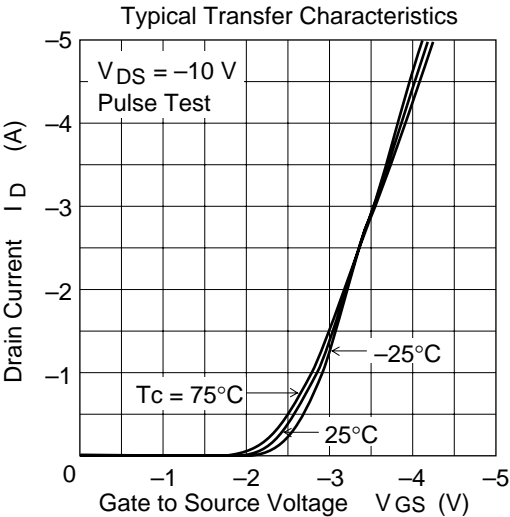
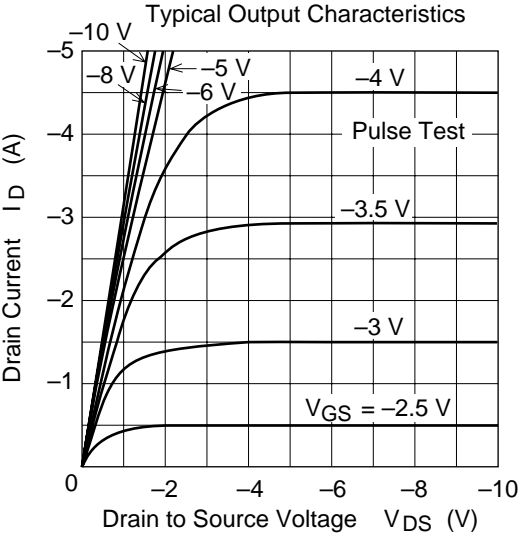
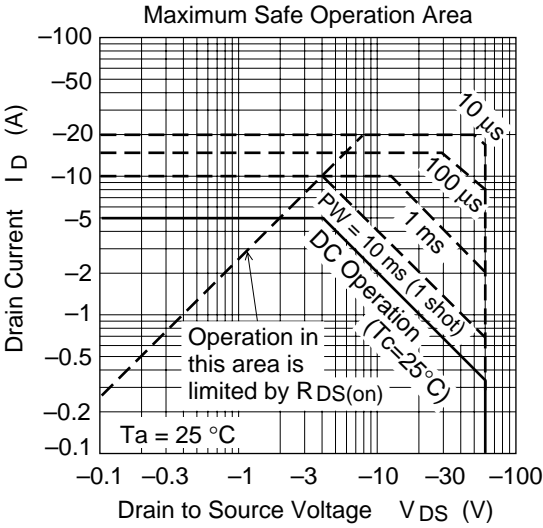
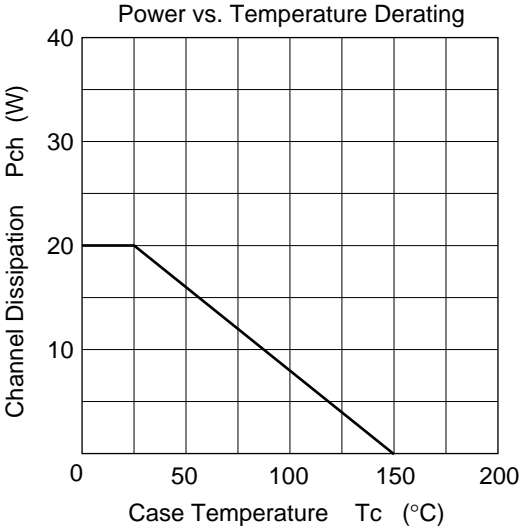
Note: 1. PW ≤ 10μs, duty cycle ≤ 1 %
2. Value at Tc = 25°C
3. Value at Tch = 25°C, Rg ≥ 50 Ω

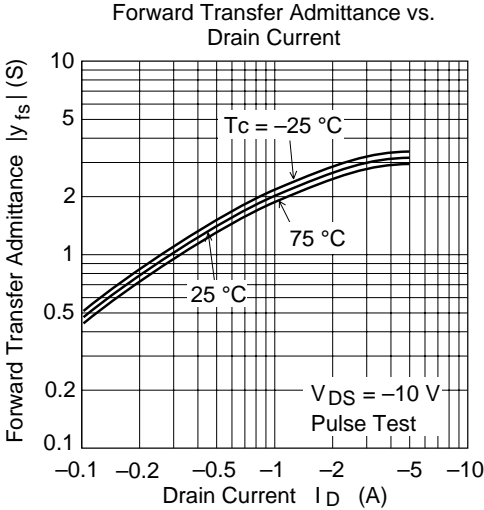
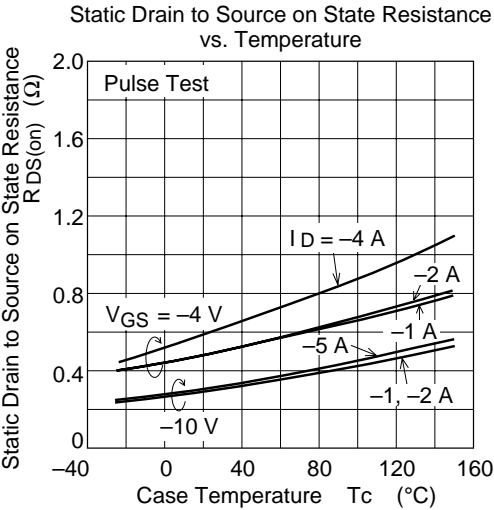
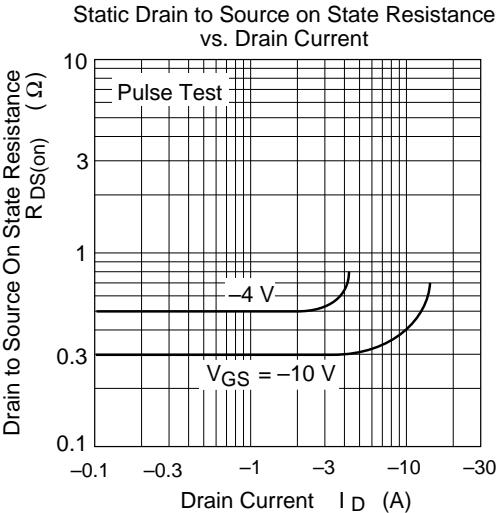
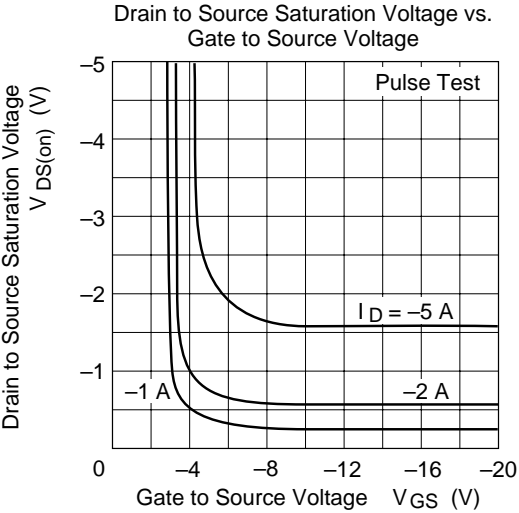
Electrical Characteristics (Ta = 25°C)

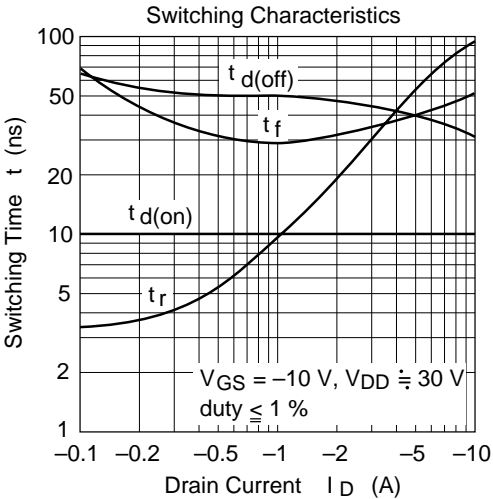
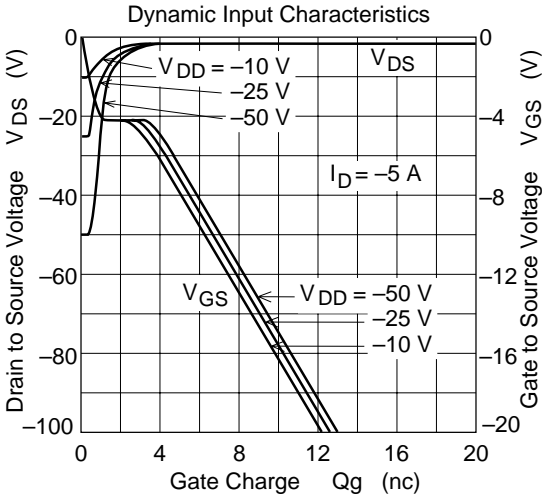
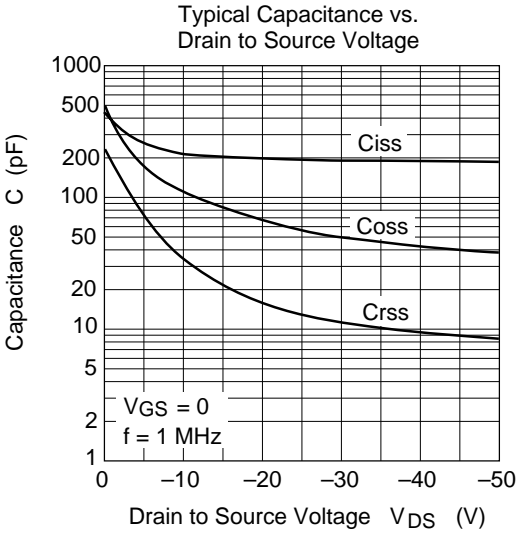
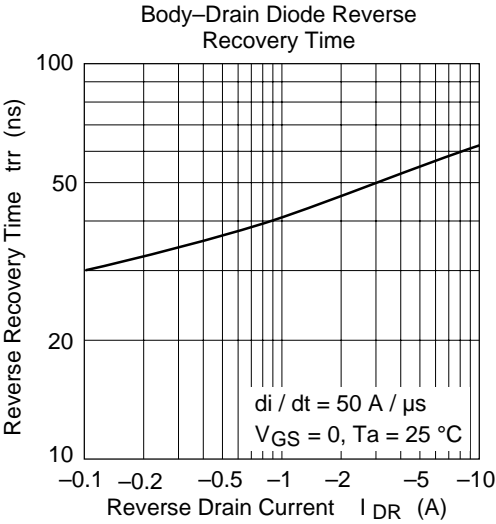
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR)DSS}	−60	—	—	V	I _D = −10mA, V _{GS} = 0
Gate to source breakdown voltage	V _{(BR)GSS}	±20	—	—	V	I _G = ±100μA, V _{DS} = 0
Zero gate voltege drain current	I _{DSS}	—	—	−10	μA	V _{DS} = −60 V, V _{GS} = 0
Gate to source leak current	I _{GSS}	—	—	±10	μA	V _{GS} = ±16V, V _{DS} = 0
Gate to source cutoff voltage	V _{GS(off)}	−1.0	—	−2.0	V	I _D = −1mA, V _{DS} = −10V
Static drain to source on state resistance	R _{DS(on)}	—	0.3	0.4	Ω	I _D = −3A, V _{GS} = −10V ^{Note4}
	R _{DS(on)}	—	0.5	0.8	Ω	I _D = −3A, V _{GS} = −4V ^{Note4}
Forward transfer admittance	y _{fs}	1.8	3	—	S	I _D = −3A, V _{DS} = −10V ^{Note4}
Input capacitance	C _{iss}	—	220	—	pF	V _{DS} = −10V
Output capacitance	C _{oss}	—	110	—	pF	V _{GS} = 0
Reverse transfer capacitance	C _{rss}	—	35	—	pF	f = 1MHz
Turn-on delay time	t _{d(on)}	—	10	—	ns	V _{GS} = −10V, I _D = −3A
Rise time	t _r	—	30	—	ns	R _L = 10Ω
Turn-off delay time	t _{d(off)}	—	45	—	ns	
Fall time	t _f	—	35	—	ns	
Body–drain diode forward voltage	V _{DF}	—	−1.35	—	V	I _F = −5A, V _{GS} = 0
Body–drain diode reverse recovery time	t _{rr}	—	55	—	ns	I _F = −5A, V _{GS} = 0 diF/ dt = 50A/μs

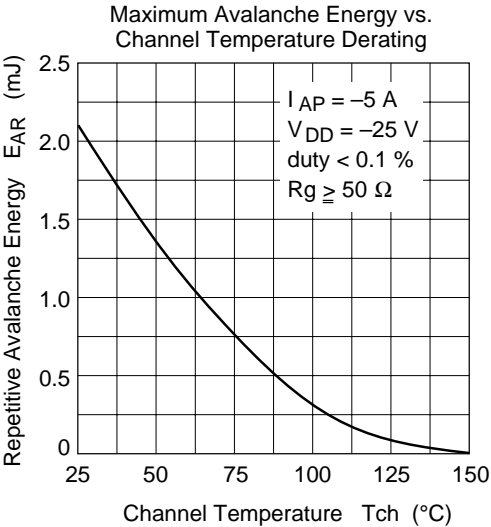
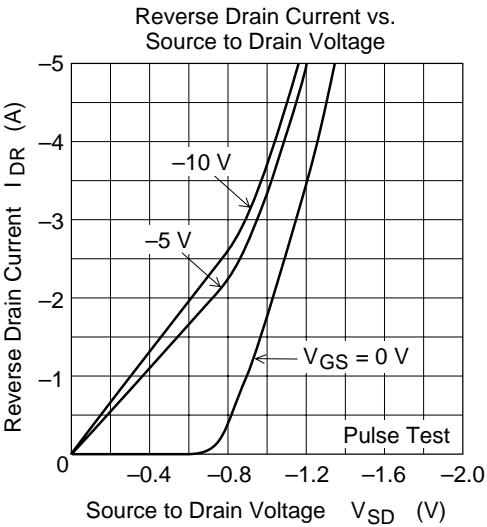
Note: 4. Pulse test

Main Characteristics

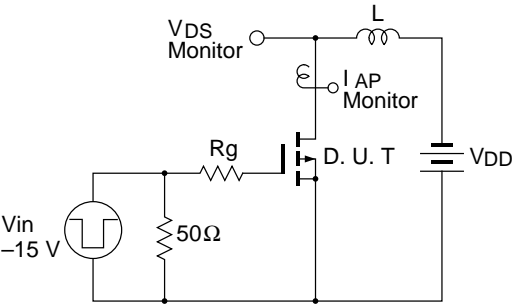






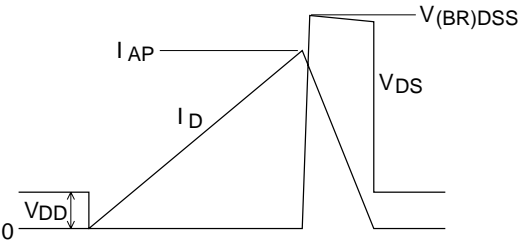


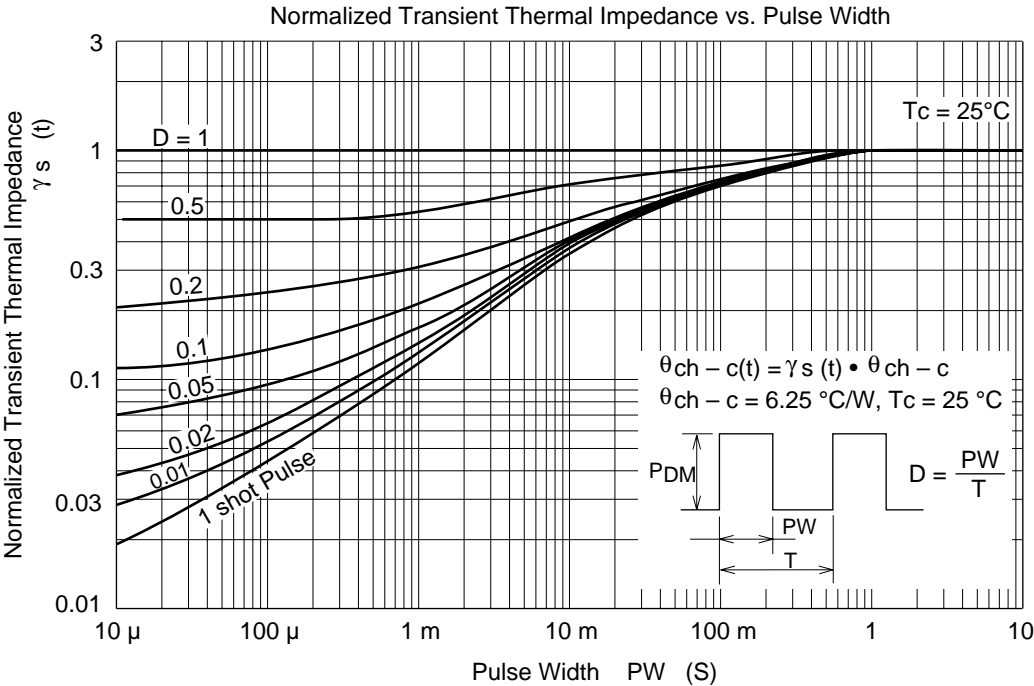
Avalanche Test Circuit



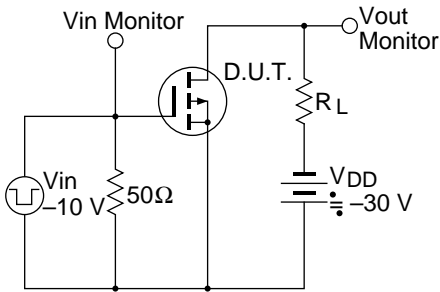
Avalanche Waveform

$$E_{AR} = \frac{1}{2} \cdot L \cdot I_{AP}^2 \cdot \frac{V_{DSS}}{V_{DSS} - V_{DD}}$$

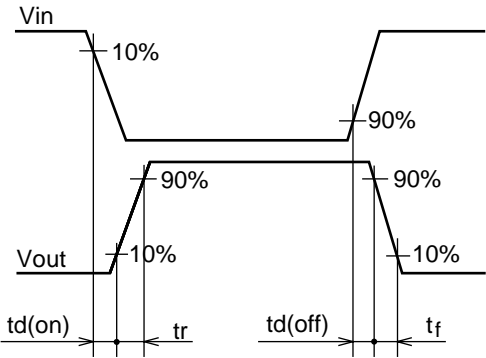




Switching Time Test Circuit

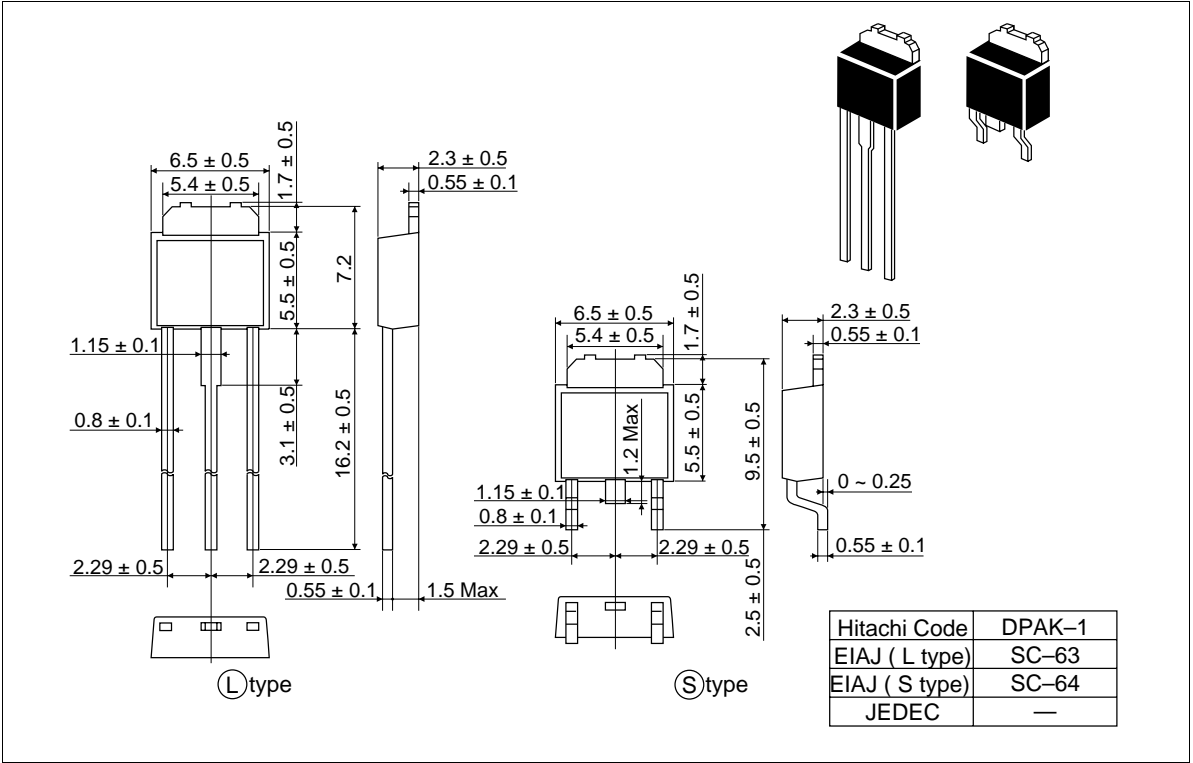


Waveform



Package Dimensions

Unit: mm



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