

HIGH AND MEDIUM POWER SPST & SPDT DIODE SWITCHES REFLECTIVE 0.20–12.4 GHz

SERIES HA, HC, HD

GENERAL SPECIFICATIONS

Frequency Coverage:	0.20 to 12.4 GHz.
RF Impedance:	50 OHMS.
Temperature Information:	Operating temperature from -55° to +85°C.
Switching Speed:	10% to 90% or 90% to 10% of RF. There is an additional 250 nanosec of driver delay.

Outline	A in [mm]	B in [mm]	C in [mm]	D in [mm]	E in [mm]	E in [mm]
1	4.00 [101,6]	3.00 [76,2]	3.600 [91,44]	2.800 [71,12]	1.25 [31,75]	1.75 [44,45]
2	3.00 [76,2]	2.00 [50,8]	2.600 [66,04]	1.800 [45,7]	0.75 [19,1]	1.25 [31,75]
3	2.25 [57,15]	1.25 [31,75]	1.850 [46,99]	1.050 [26,67]	0.38 [9,7]	0.88 [22,35]
4	2.00 [50,8]	1.00 [25,4]	1.600 [40,64]	0.800 [20,3]	0.25 [6,4]	0.75 [19,1]
5	2.25 [57,15]	1.80 [45,7]	1.850 [46,99]	1.600 [40,64]	0.65 [16,51]	1.15 [29,21]
6	2.00 [50,8]	2.00 [50,8]	1.600 [40,64]	1.800 [45,7]	0.75 [19,1]	1.25 [31,75]

KEY: Inches[Millimeters] .XX ±.03 .XXX ±.010 [.X ±0.8 .XX ±0.25]

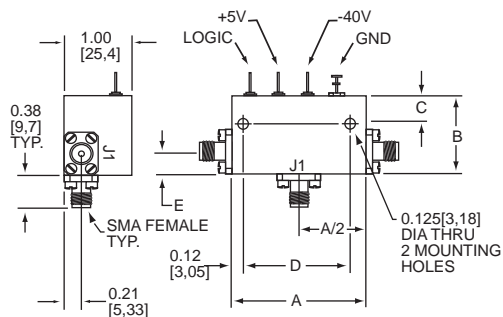


FIGURE 1 OUTLINE WITH DRIVER J1-INPUT

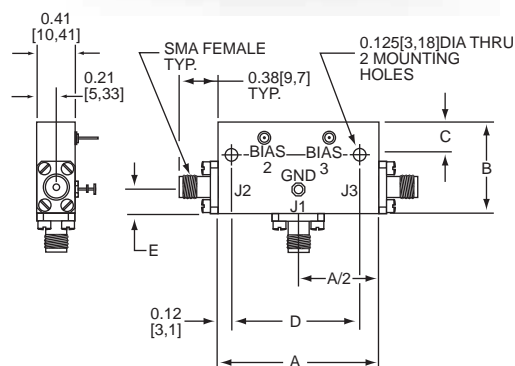


FIGURE 2 OUTLINE WITHOUT DRIVER J1-INPUT

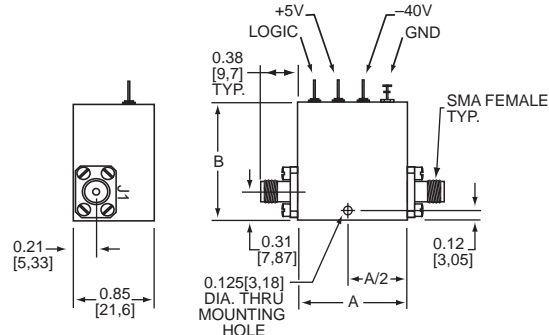


FIGURE 3

ELECTRICAL PERFORMANCE

Type	Model No.	Frequency Range (GHz)	Insertion Loss Maximum dB	LOGIC "ON"		VSWR Maximum	Isolation Minimum dB	Switching Speed	Peak (KW)	CW Watts	Outline	DC POWER		
				J2	J3							+15VDC -150 mA	-70VDC -25 mA	-40VDC -20 mA
SPDT	HC-16	0.20-0.40	0.50	1	0	1.30	25	2 μsec	0.40	20	1	✓	—	✓
SPDT	HC-31D	0.50-1.00	0.80	1	0	1.30	25	2 μsec	0.40	20	1	✓	—	✓
SPDT	HA-32D	1.00-1.11	0.70	0	1	1.25	35	5 μsec	2.0	50	2	✓	✓	—
SPDT	HC-46D	1.20-1.40	0.90	1	0	1.30	35	2 μsec	0.25	125	1	✓	—	✓
SPST	HD-64D	4.00-8.00	1.20	0	NA	1.45	50	2 μsec	0.25	40	3	✓	—	✓
SPST	HD-79D	8.00-12.40	1.80	0	NA	1.45	35	2 μsec	0.25	20	3	✓	—	✓
SPDT	HC-89D	9.00-9.40	1.70	1	0	1.30	35	2 μsec	0.20	20	1	✓	—	✓

KEY: Inches[Millimeters] .XX ±.03 .XXX ±.010 [.X ±0.8 .XX ±0.25]



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