

FEATURES

- Differential D and Q
- Extended 100E VEE range of $-4.2V$ to $-5.5V$
- VBB output for single-ended use
- 700ps max. propagation delay
- High frequency outputs
- Separate and common select
- Internal $75K\Omega$ input pulldown resistors
- Available in 28-pin PLCC package

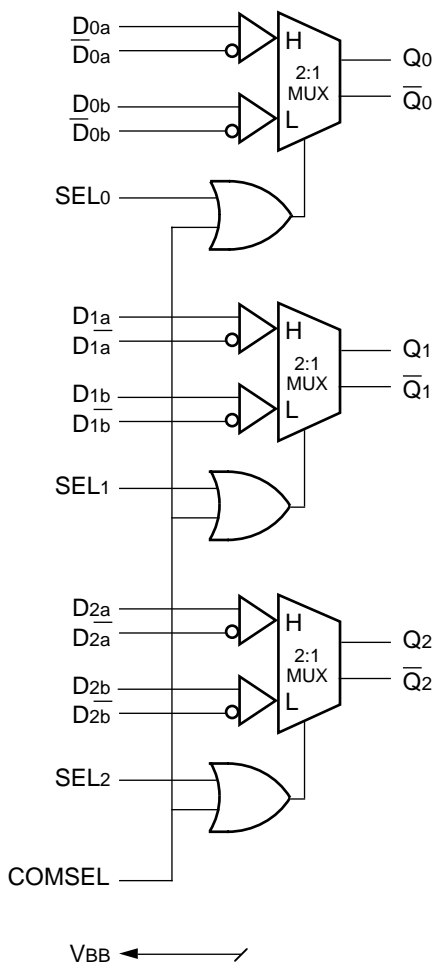
DESCRIPTION

The SY10/100E457 are 3-bit differential 2:1 multiplexers. The fully differential data path makes the devices ideal for multiplexing low skew clock or other skew sensitive signals. Multiple VBB pins are provided to ease AC coupling input signals.

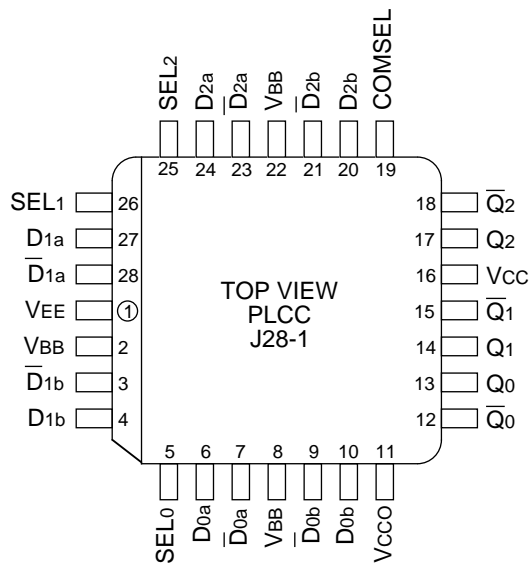
The higher frequency outputs provide the device with a $>1.0GHz$ bandwidth to meet the needs of the most demanding system clock.

Both separate selects and a common select are provided to make the device well suited for both data path and random logic applications.

BLOCK DIAGRAM



PIN CONFIGURATION



PIN NAMES

Pin	Function
$D_n[0:2], \bar{D}_n[0:2]$	Differential Data Inputs
SEL	Individual Select Input
COMSEL	Common Select Input
VBB	VBB Reference Output
$Q[0:2], \bar{Q}[0:2]$	Differential Data Outputs
VCCO	Vcc to Output

DC ELECTRICAL CHARACTERISTICSV_{EE} = V_{EE} (Min.) to V_{EE} (Max.); V_{CC} = V_{CCO} = GND

Symbol	Parameter	T _A = 0°C			T _A = +25°C			T _A = +85°C			Unit	Condition
		Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.		
V _{BB}	Output Reference Voltage										V	—
	10E	−1.38	—	−1.27	−1.35	—	−1.25	−1.31	—	−1.19		
	100E	−1.38	—	−1.26	−1.38	—	−1.26	−1.38	—	−1.26		
I _{IH}	Input HIGH Current	—	—	150	—	—	150	—	—	150	μA	—
I _{EE}	Power Supply Current										mA	—
	10E	—	92	110	—	92	110	—	92	110		
	100E	—	92	110	—	92	110	—	106	127		
V _{PP} (DC)	Input Sensitivity	50	—	—	50	—	—	50	—	—	mV	1
V _{CMR}	Common Mode Range	−1.5	—	0	−1.5	—	0	−1.5	—	0	V	2

NOTES:

1. Differential input voltage required to obtain a full ECL swing on the outputs.
2. V_{CMR} is referenced to the most positive side of the differential input signal. Normal operation is obtained when the input signals are within the V_{CMR} range and the input swing is greater than V_{PP} (min.) and <1V.

AC ELECTRICAL CHARACTERISTICSV_{EE} = V_{EE} (Min.) to V_{EE} (Max.); V_{CC} = V_{CCO} = GND

Symbol	Parameter	T _A = 0°C			T _A = +25°C			T _A = +85°C			Unit	Condition
		Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.		
t _{PLH} t _{PHL}	Propagation Delay to Output D (Diff) D SEL COMSEL	375 325 350 375	475 475 500 525	650 700 725 750	375 325 350 375	475 475 500 525	650 700 725 750	375 325 350 375	475 475 500 525	650 700 725 750	ps	—
t _{skew}	Within-Device Skew	—	40	—	—	40	—	—	40	—	ps	1
t _{skew}	Duty Cycle Skew t _{PLH} –t _{PHL}	—	±10	—	—	±10	—	—	±10	—	ps	2
V _{PP} (AC)	Minimum Input Swing	150	—	—	150	—	—	150	—	—	mV	3
t _r t _f	Rise/Fall Time 20–80%	150	275	450	150	275	450	150	275	450	ps	—

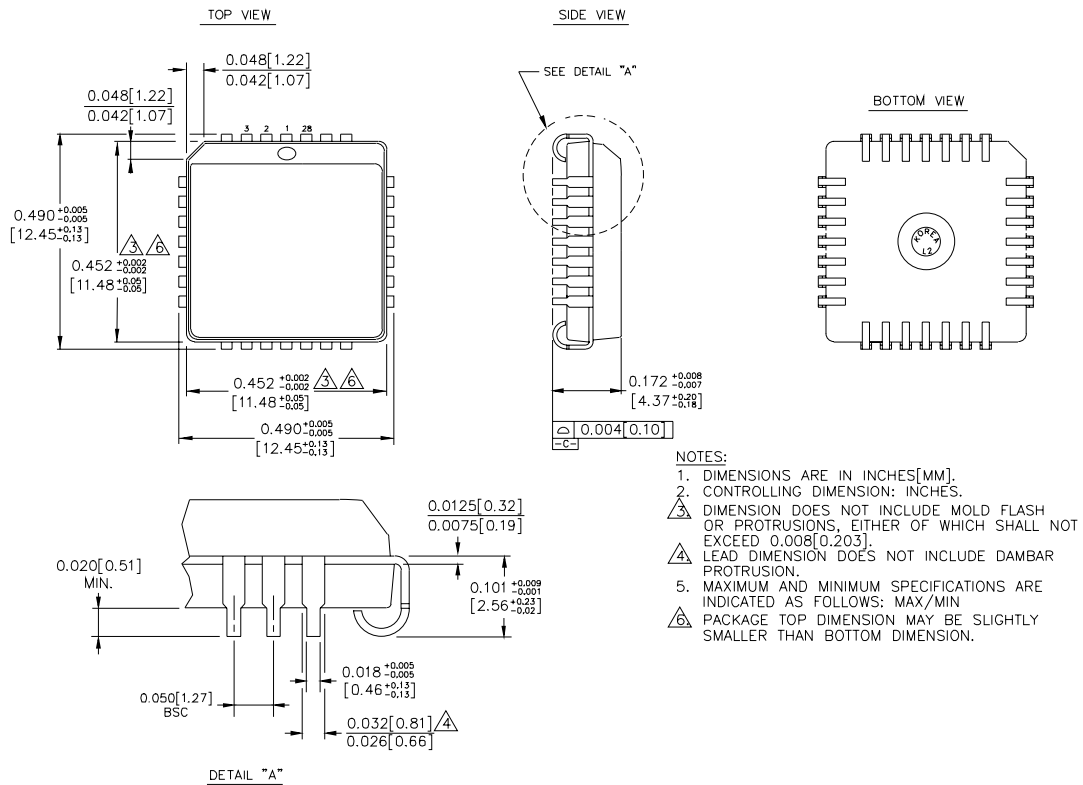
NOTES:

1. Within-device skew is defined as identical transitions on similar paths through a device.
2. Duty cycle skew guarantee holds only for differential operation when the delays are measured from the cross point of the inputs to the cross point of the outputs.
3. Minimum input swing for which AC parameters are guaranteed.

PRODUCT ORDERING CODE

Ordering Code	Package Type	Operating Range
SY10E457JC	J28-1	Commercial
SY10E457JCTR	J28-1	Commercial
SY100E457JC	J28-1	Commercial
SY100E457JCTR	J28-1	Commercial

28 LEAD PLCC (J28-1)



Rev. 03

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