

# *ME* **DISPLAYS**

**SPECIFICATIONS  
FOR  
LCD MODULE  
SC1602H**

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**• FEATURES**

<i>Number of Characters</i>	<i>16 characters x 2 lines</i>
<i>Display Font</i>	<i>5 x 7 dots with Cursor</i>
<i>Built-in Controller</i>	<i>HD44780 or Comp</i>
<i>Input Data</i>	<i>4 Bits or 8-Bits Interface</i>
<i>Power Supply</i>	<i>+5 V Single Power</i>
<i>Duty Cycle</i>	<i>1/16 Duty</i>
<i>Options</i>	<i>EL/LED Backlight, TN/STN</i>

**• MECHANICAL PARAMETERS**

<i>Module Size</i>	<i>84.0W x 44.0H x 8.8T mm</i>
<i>Module Size with LED Backlight</i>	<i>84.0W x 44.0H x 12.7T mm</i>
<i>Viewing Area Size</i>	<i>66.0W x 16.0H mm</i>
<i>Dot Size</i>	<i>0.56 x 0.66 mm</i>
<i>Dot Pitch</i>	<i>0.60 x 0.70 mm</i>

**• ABSOLUTE MAXIMUM**

<b>Item</b>	<b>Symbol</b>	<b>Min.</b>	<b>Max</b>	<b>Unit</b>
Power Supply for Logic	Vdd	-0.3	+7.0	V
Power Supply for LCD Drive	Vlcd	Vdd-11.5	Vdd+0.3	V
Input Voltage	Vi	-0.3	Vdd+0.3	V
Operating Temperature	Ta	0	+50	C
Storage Temperature	Tstg	-10	+70	C

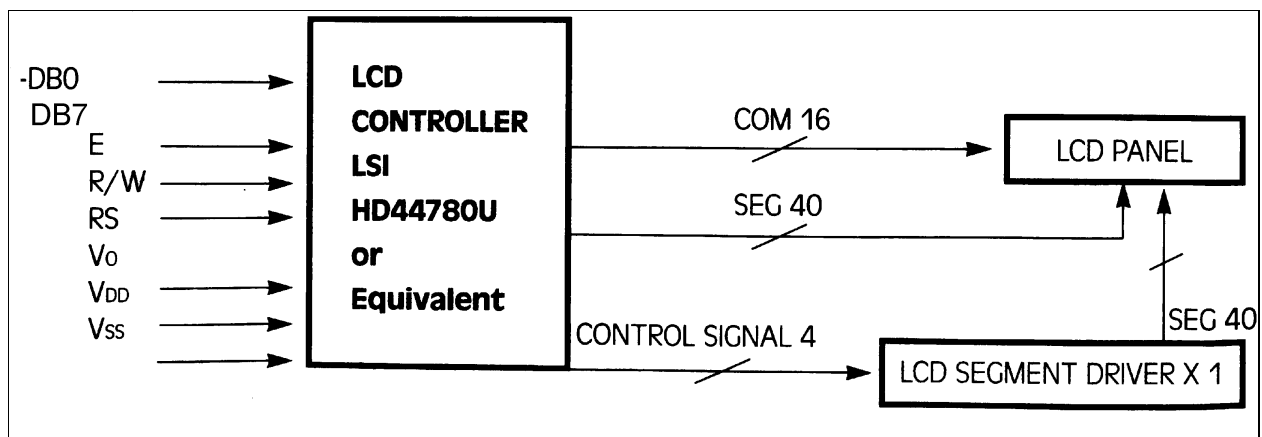
**• ELECTRICAL CHARACTERISTICS**(Vdd=5.0V  $\pm$  5%, Ta=25C)

<b>Item</b>	<b>Symbol</b>	<b>Conditions</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>U</b>
Power Supply for Logic	Vdd	-	4.5	--	5.5	V
Op. Voltage for LCD	Vdd-Vo	-	-	5.0	-	V
Input "High" Voltage	Vih	-	2.2	-	Vdd	V
Input "Low" Voltage	Vil	-	-0.3	-	0.6	V
Output "High" Voltage	Voh	- Ioh = 0.2mA	2.4	-	-	V
Output "Low" Voltage	Vol	Iol = 1.2mA	-	-	0.4	V
Power Supply Current	Idd	Vdd = 5.0V	-	-	3.0	mA

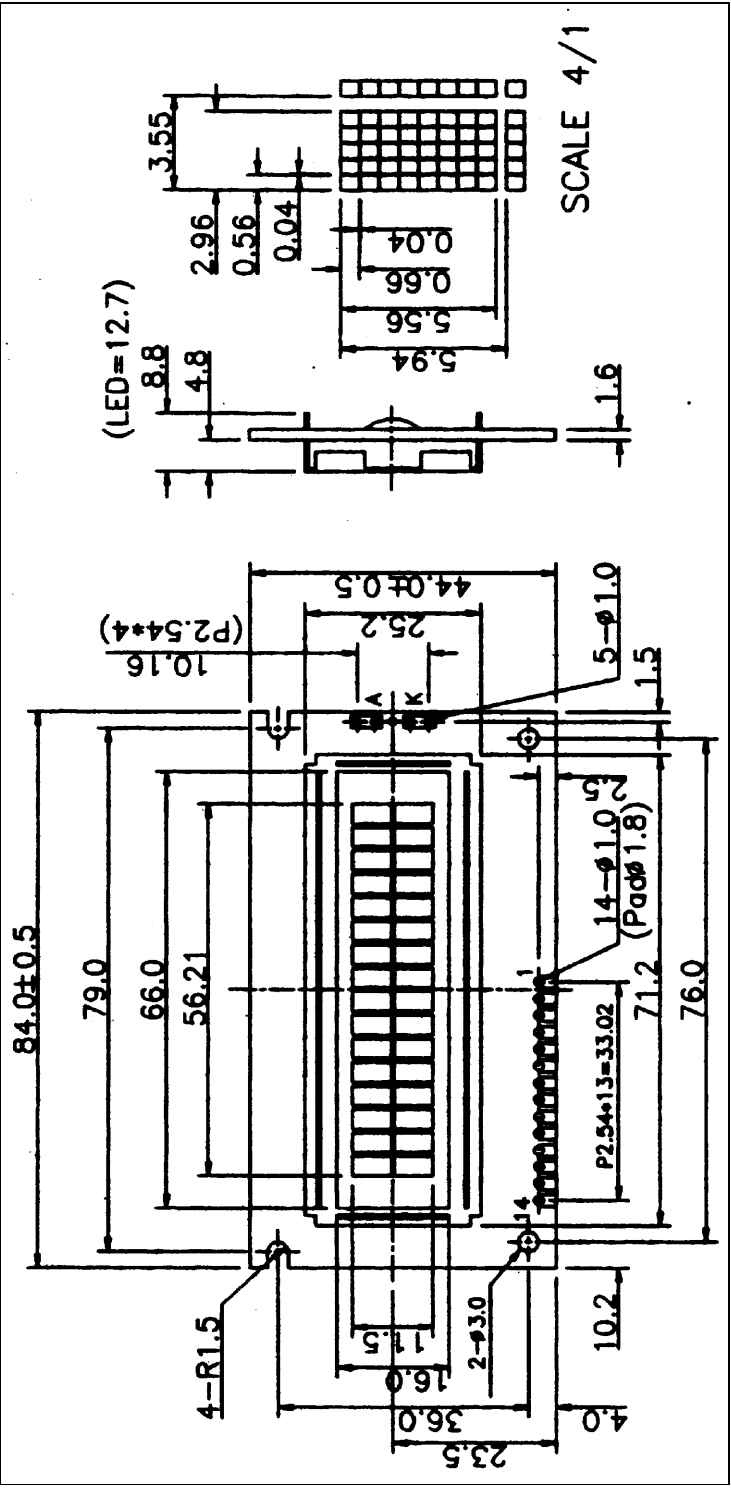
## • PIN ASSIGNMENT

No.	Symbol	Level	Pin Description	Function
1	Vss	--	Ground	0V
2	Vdd	--	Supply Voltage for Logic	5V
3	Vo	--	LCD Contrast Adjust	By User
4	RS	H/L	Register Select	H: Data, L: Instruction Code
5	R/W	H/L	Read / Write	H: Data Read, L: Data Write
6	E	H, H→L	Enable	Enable Signal
7	DB0	H/L	Data Bit 0	8-Bit Interface
8	DB1	H/L	Data Bit 1	8-Bit Interface
9	DB2	H/L	Data Bit 2	8-Bit Interface
10	DB3	H/L	Data Bit 3	8-Bit Interface
11	DB4	H/L	Data Bit 4	4 or 8-Bit Interface
12	DB5	H/L	Data Bit 5	4 or 8-Bit Interface
13	DB6	H/L	Data Bit 6	4 or 8-Bit Interface
14	DB7	H/L	Data Bit 7	4 or 8-Bit Interface

## • BLOCK DIAGRAM



• DIAGRAM SC1602H



- ENVIRONMENTAL CONDITIONS**

Item	Operating		Non-Operating		Remarks
	Min	Max	Min	Max	
Ambient Temperature	0°C	50°C	-20°C	70°C	Normal Temperature Type
(Ta)	-20°C	70°C	-30°C	80°C	Extended Temperature Type
Vibration	--	0.5G	--	2G	G=9.8 m/s <sup>2</sup>
Shock	--	3G	--	50G	XYZ Directions
Corrosion Gas	No Corrosion Gas				

- OPTICAL CHARACTERISTICS**

**TN TYPE**

Item	Symbol	Condition	Min	Typ	Max	Unit
Viewing Angle	Ø2-Ø1	K=1.4	20	--	--	Deg
Contrast Ratio	K	Ø=25° θ=0°	--	3	--	--
Response Time (Rise)	tr	Ø=25° θ=0°	--	200	250	ms
Response Time (Fall)	tf	Ø=25° θ=0°	--	200	300	ms

**STN TYPE**

Item	Symbol	Condition	Min	Typ	Max	Unit
Viewing Angle	Ø2-Ø1	K=1.4	40	--	--	Deg
Contrast Ratio	K	Ø=25° θ=0°	3	--	--	--
Response Time (Rise)	tr	Ø=25° θ=0°	--	200	300	ms
Response Time (Fall)	tf	Ø=25° θ=0°	--	250	350	ms

## • BACKLIGHT CHARACTERISTICS

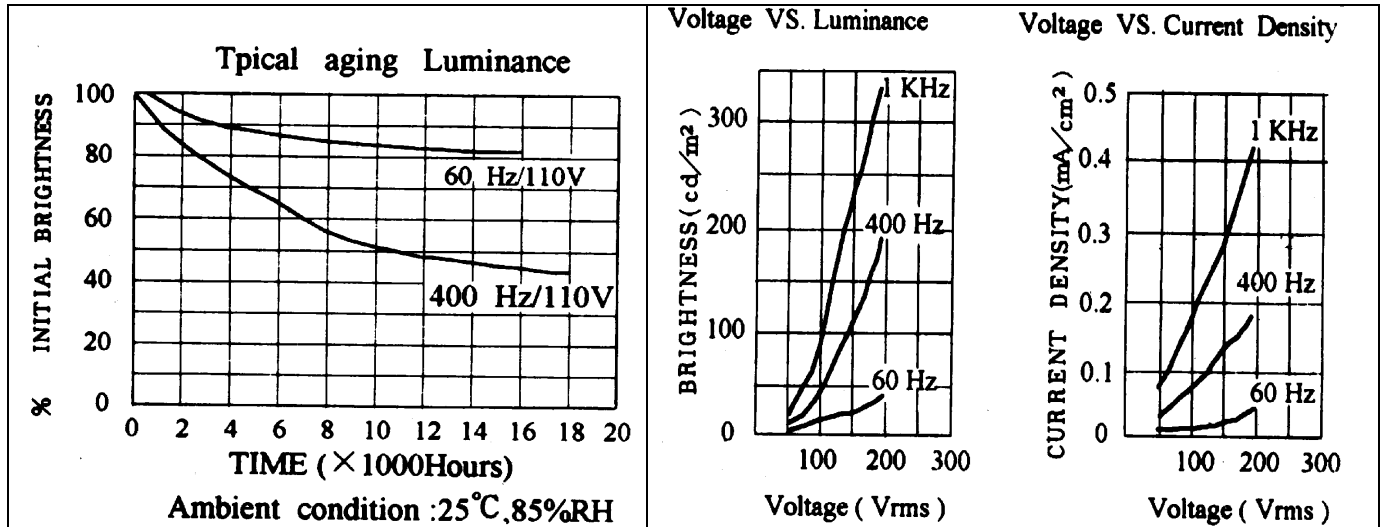
### EL BACKLIGHT

Luminous Intensity :  $60 \text{ cd/m}^2$

Power Consumption :  $\sim 0.17 \text{ mA/cm}^2$

Driving Condition : 150 Vrms AC Max 50~1000 Hz

Life : 2K Hours (Normal Type), 8K Hours (Long Life Type)



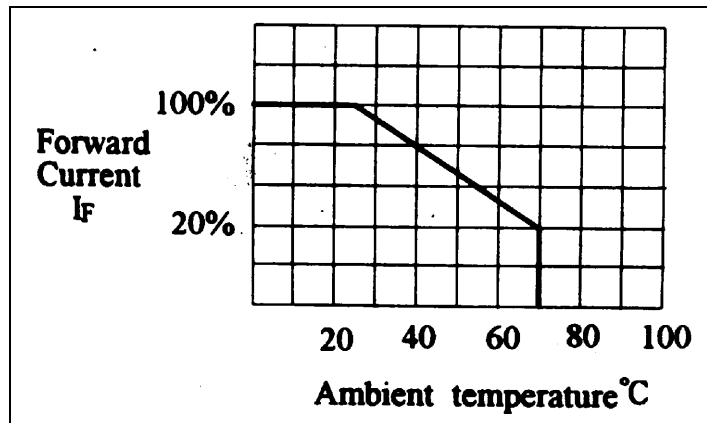
### LED BACKLIGHT

Low Voltage Driving (DC) is available without inverter

No noise occurrence

Luminous Intensity :  $50 \text{ cd/m}^2$

Life : 20K Hours



## • AC CHARACTERISTICS

Item	Symbol	Min	Max	Unit
Enable Cycle Time	$T_{CYCE}$	500	--	ns
Enable Pulse Width	High Level	$PW_{EH}$	230	ns
Enable Rise/Fall Time	$t_{Er}, t_{Ef}$	--	20	ns
Address Set-up Time	RS, R/W to E	$t_{AS}$	40	ns
Address Hold Time		$t_{AH}$	10	ns
Data Set-up Time		$t_{DSW}$	80	ns
Data Delay Time		$t_{DDR}$	160	ns
Data Hold Time (Writing)		$t_H$	10	ns
Data Hold Time (Reading)		$t_{DHR}$	5	ns
Clock Oscillation Frequency	$f_{OSC}$	270 (TYP)		KHz

## • TIMING CHARACTERISTICS

