

TV Video Modulator

The MC1373 is an RF oscillator and dual-input modulator to generate a TV signal from baseband video inputs.

Applications include video games, home computer display, video tape recorders, and test equipment.

The very low level of intermodulation products, compact package and small external component count make this device superior to simple discrete circuits.

- Single 5.0 Vdc Supply
- Channel 3 or 4 Operation
- Excellent Oscillator Stability to 100 MHz
- Color and Sound Compatibility
- Dual Input Modulator for Ease of Signal Handling
- Low Intermodulation (-50 dB, 920 kHz Beat)
- Overmodulation Protection

MAXIMUM RATINGS (T_A = 25°C, unless otherwise noted.)

Rating	Value	Unit
Supply Voltage	8.0	Vdc
Operating Ambient Temperature Range	0 to +70	°C
Storage Temperature Range	-65 to +150	°C
Junction Temperature	150	°C
Power Dissipation Package Derate above 25°C	1.25 10	W mW/°C

TV VIDEO MODULATOR CIRCUIT

SILICON MONOLITHIC INTEGRATED CIRCUIT



P SUFFIX
PLASTIC PACKAGE
CASE 626

PIN CONNECTIONS

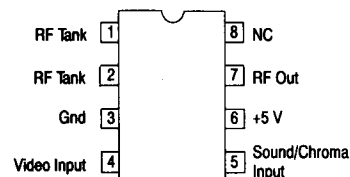
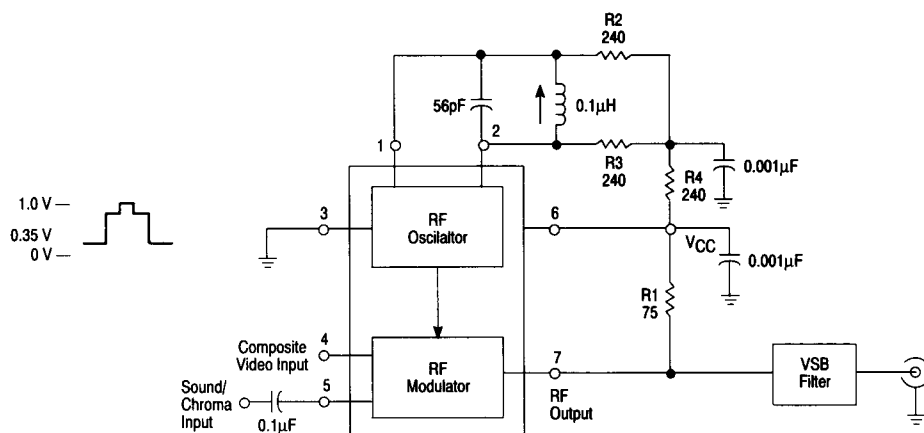


Figure 1. Block Diagram and Application Circuit



MC1373

RECOMMENDED OPERATING CONDITIONS

Supply Voltage	5.0	Vdc
Luma Input Voltage — Sync Tip	1.0	Vdc
Peak White	0.35	

ELECTRICAL CHARACTERISTICS (V_{CC} = +5 Vdc, T_A = 25°C, Test Circuit 1 unless otherwise noted.)

Characteristics	Min	Typ	Max	Unit
Operating Supply Voltage	4.75	5.0	5.25	V
Supply Current	—	12	—	mA

RF MODULATOR

Luma Input Dynamic Range (Pin 4, see Figure 3)	0	—	1.5	V
RF Output Voltage (f = 67.25 MHz, V ₄ = 1.0 V)	—	15	—	mVrms
Luma Conversion Gain ($\Delta V_7/\Delta V_4$, V ₄ = 0.1 Vdc to 1.0 Vdc) (See Figure 3)	—	0.8	—	V/V
Chroma Conversion Gain ($\Delta V_7/\Delta V_5$; V ₅ = 1.5 Vp-p; V ₄ = 1.0 Vdc) (See Figure 3)	—	0.95	—	V/V
Chroma Linearity (Pin 7, V ₅ = 1.5 Vp-p) (See Figure 3)	—	1.0	—	%
Luma Linearity (Pin 7, V ₄ = 0 Vdc to 1.5 Vdc) (See Figure 3)	—	2.0	—	%
Input Current (Pin 4)	—	—	-20	μA
Input Resistance (Pin 5)	—	800	—	Ω
Input Resistance (Pin 4)	100	—	—	kΩ
Input Capacitance (Pins 4, 5)	—	—	5.0	pF
Residual 920 kHz (Measure at Pin 7) (Note 1)	—	60	—	dB
Output Current (Pin 7, V ₄ = 0 V) (See Figure 3)	—	1.5	—	mA

TEMPERATURE CHARACTERISTICS (V_{CC} = 5.0 V, T_A = 0° to 70°C, IC only)

RF Oscillator Deviation (f _o = 67.25 MHz)	—	±250	—	kHz
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NOTES: 1. RF Reference Level = 6.0 mV @ Pin 7. Load Impedance = 75 Ω
 RF + 4.5 MHz = -13 dB.
 RF + 3.58 Mhz = -20 dB.

Figure 2. Test Circuit 1

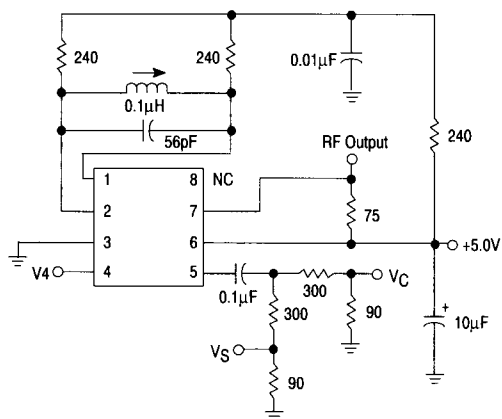
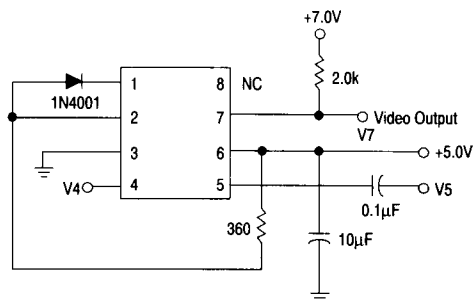
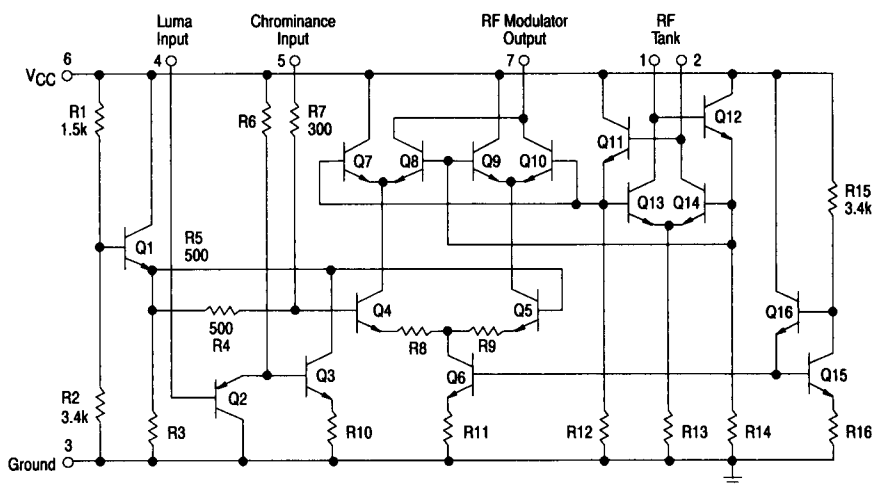


Figure 3. Test Circuit 2



MC1373

Figure 4. Schematic Diagram



SCHEMATIC DESCRIPTION

The RF oscillator consists of differential amplifier Q13 and Q14 cross-coupled through emitter followers Q11 and Q12. The oscillator will operate at the parallel resonant frequency of the network connected between Pins 1 and 2. The oscillator output is used to switch the doubly balanced RF modulator, Q4 through Q10. Transistors Q2 and Q3 provide level shifting and a high input impedance to the luminance input Pin 4. The bases of transistors Q4 and Q5 are both biased through resistors R4 and R5, respectively, to the same DC reference voltage at Q1 emitter. The base voltage at Q5 may only be offset in a negative direction by luminance signal current source Q3. This design insures that overmodulation due to the luminance signal will never occur. The chrominance signal is externally AC coupled to Pin 5 where it is reduced by resistor dividers R7 and R4, and added to the luminance signal in Q4. The resultant differential composite video currents are switched at the appropriate RF frequency in Q7 through Q10. The output signal current is presented at Pin 7.

Transistors Q15, Q16 and resistors R15, R16 provide a highly stable voltage reference for biasing the current source Q6.

OPERATIONAL DESCRIPTION

Pins 1 and 2 — RF Tank. A tuned circuit connected between these pins determines the RF oscillator frequency. The tuned circuit must provide a low DC resistance shunt. Applying a DC offset voltage between these pins results in baseband composite video at the RF Modulator Output.

Pin 3 — Ground.

Pin 4 — Luminance Input. Input to RF modulator. This pin accepts a DC coupled luminance and sync signal. The amplitude of the RF signal output increases with positive voltage applied to the pin, and ground potential results in zero output (i.e., 100% modulation). A signal with positive-going sync should be used.

Pin 5 — Chrominance/Sound Input. Input to the RF modulator. This pin accepts an AC coupled chrominance

signal. The signal is reduced by an internal resistor divider before being applied to the RF modulator. The resistor divider consists of a 300 Ω series resistor and a 500 Ω shunt resistor. A 4.5 MHz FM audio signal may be added to the input by selecting an appropriate series input resistor to provide the correct Luminance:Sound ratio.

Pin 6 — VCC. Positive supply voltage.

Pin 7 — RF Modulator Output. Common collector of output modulator stage. Output impedance and stage gain may be selected by choice of resistor connected between this pin and DC supply.

Pin 8 — No Connection.

APPLICATIONS INFORMATION

RF Modulator and Oscillator

The coil and capacitor connected between Pins 1 and 2 should be selected to have a parallel resonance at the carrier frequency of the desired TV channel. The values of 56 pF and 0.1 μ H shown in Figure 1 were chosen for a Channel 4 carrier frequency of 67.25 MHz. For Channel 3 operation, the resonant frequency should be 61.25 MHz ($C = 75$ pF, $L = 0.1$ μ H). Resistors R2 and R3 are chosen to provide an adequate amplitude of switching voltage, whereas R4 is used to lower the maximum DC level of switching voltage below VCC, thus preventing saturation within the IC.

Composite Luminance and Sync should be DC coupled to Luminance Input, Pin 4. This signal must be within the Luma Input Dynamic Range to insure linearity. Since an increase in DC voltage applied to Pin 4 results in an increase in RF output, the input signal should have positive-going sync to generate an NTSC compatible signal. As long as the input signal is positive, overmodulation is prevented by the integrated circuit.

Chrominance information should be AC coupled to Chrominance Input, Pin 5. This pin is internally connected to a resistor divider consisting of a series 300 Ω and a shunt 500 Ω resistor. The input impedance is thus 800 Ω , and a coupling capacitor should be appropriately chosen.