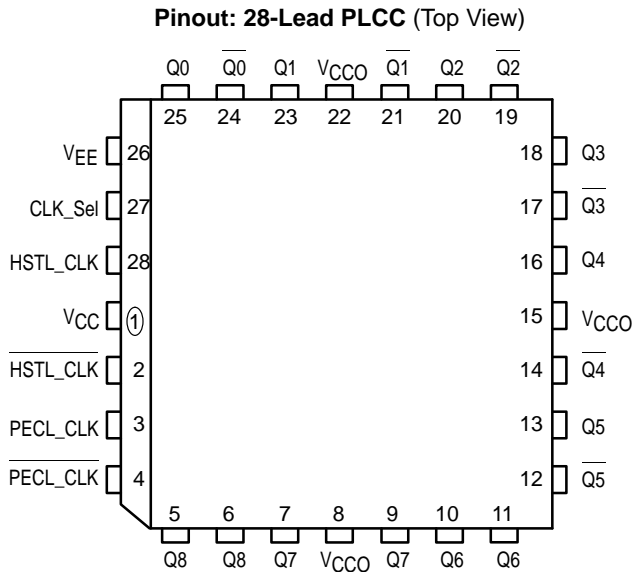


Low-Voltage 1:9 Differential
ECL/HSTL to HSTL Clock Driver

The MPC9111 is a low skew 1-to-9 differential HSTL compatible output fanout buffer. The device is functionally equivalent to the MC100LVE111 device. The device accepts either LVPECL or HSTL compatible input levels and provides 9 low skew differential HSTL compatible outputs. The device operates from a single 3.3V V_{CC} supply.

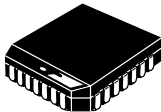
- 800ps Part-to-Part Skew
- 250ps Output-to-Output Skew
- Open Emitter HSTL Compatible Outputs
- Differential Design
- 28-Lead PLCC
- 3.3V V_{CC}

The MPC911 HSTL outputs are not realized in the conventional manner. To minimize part-to-part and output-to-output skew the HSTL compatible output levels are generated with an open emitter architecture. The outputs are pulled down with 50Ω to ground rather than the typical 50Ω to V_{DDQ} pullup of a “standard” HSTL output. Because the HSTL outputs are pulled to ground the MPC911 does not utilize the V_{DDQ} supply of the HSTL standard. The output levels are derived from V_{CC}, an internal regulator minimizes the output level variation with V_{CC} variations.



MPC911

LOW-VOLTAGE
1:9 DIFFERENTIAL ECL/HSTL
TO HSTL CLOCK DRIVER



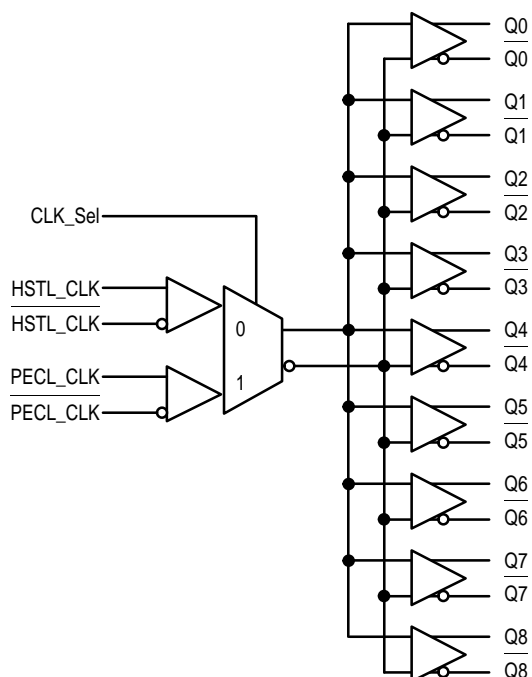
FN SUFFIX
PLASTIC PACKAGE
CASE 776-02

PIN NAMES

Pins	Function
HSTL_CLK, HSTL_CLK	Differential HSTL Input
PECL_CLK, PECL_CLK	Differential PECL Input
Q0-Q8, Q0-Q8	Differential Outputs



LOGIC SYMBOL



HSTL DC CHARACTERISTICS

Symbol	Characteristic	0°C			25°C			70°C			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
V _{OH}	Output HIGH Voltage	0.9			0.9			0.9			V
V _{OL}	Output LOW Voltage			0.5			0.5			0.5	V
V _{IH}	Input HIGH Voltage	V _{ref} + 0.10		1.9	V _{ref} + 0.10		1.9	V _{ref} + 0.10		1.9	V
V _{IL}	Input LOW Voltage	-0.3		V _{ref} - 0.10	-0.3		V _{ref} - 0.10	-0.3		V _{ref} - 0.10	V
V _X	Input Crossover Volt	0.68		0.9	0.68		0.9	0.68		0.9	V
V _{ref}	Input Reference Volt	0.68		0.9	0.68	0.75	0.9	0.68		0.9	

LV PECL DC CHARACTERISTICS


Symbol	Characteristic	0°C			25°C			70°C			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
V _{IH}	Input HIGH Voltage ¹	2.135		2.420	2.135		2.420	2.135		2.420	V
V _{IL}	Input LOW Voltage ¹	1.490		1.825	1.490		1.825	1.490		1.825	V
V _{CC}	Power Supply Voltage	3.0		3.6	3.0		3.6	3.0		3.6	V
I _{IH}	Input HIGH Current			150			150			150	μA
I _{CC}	Power Supply Current			100			100			110	mA

1. These values are for V_{CC} = 3.3V. Level Specifications will vary 1:1 with V_{CC}.

AC CHARACTERISTICS

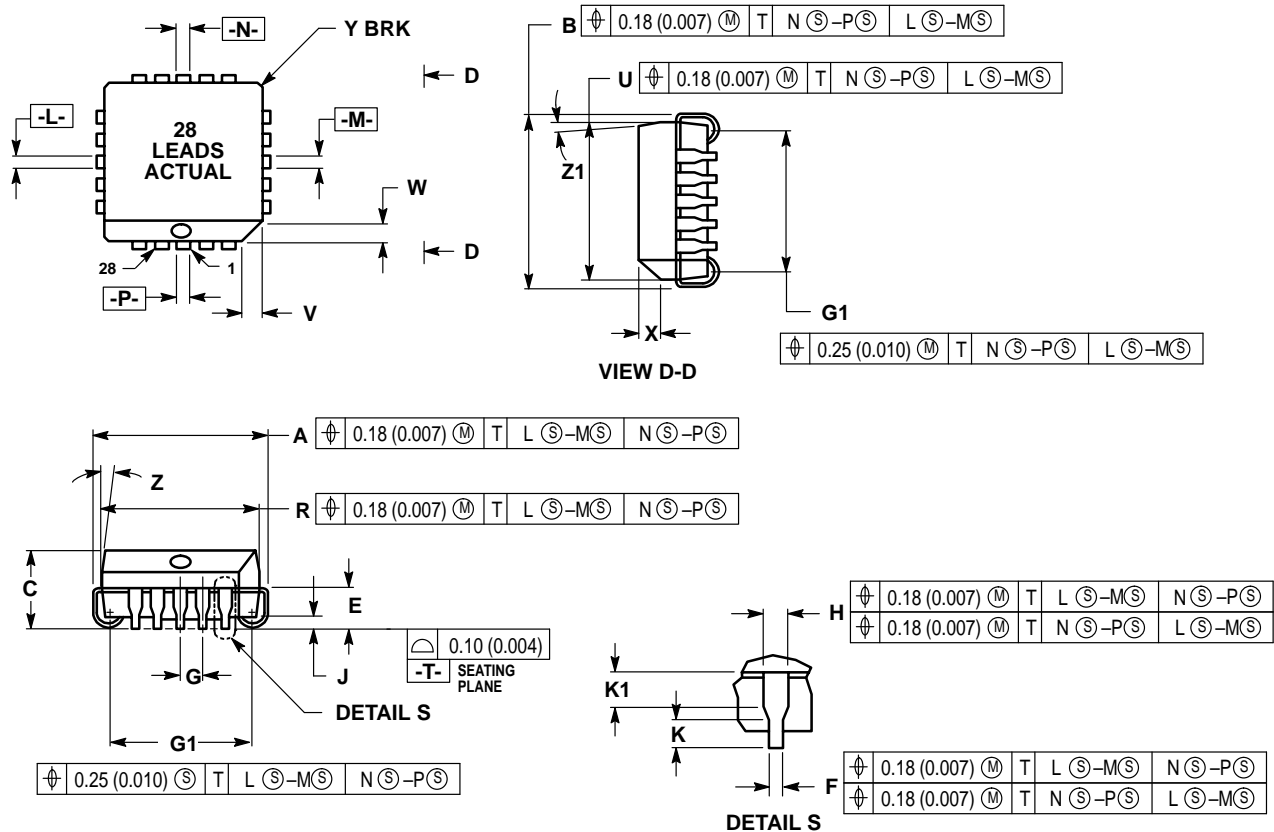
Symbol	Characteristic	0°C			25°C			70°C			Unit	Condition
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max		
t _{PLH} t _{PHL}	Propagation Delay to Output IN (differential)	HSTL 1.4 PECL 1.3	2.0 1.9	2.3 2.0	1.6 1.4	1.9 1.9	2.4 2.1	1.8 1.5	2.3 2.0	2.6 2.3	ns	Note 1
t _{skew}	Within-Device Skew Part-to-Part Skew (Diff)			250 900			250 800			250 800	ps	Note 2
V _{PP}	Minimum Input Swing PECL_CLK	600			600			600			mV	Note 3
V _{CMR}	Common Mode Range PECL_CLK	V _{CC} -1.5		V _{CC} -0.8	V _{CC} -1.5		V _{CC} -0.8	V _{CC} -1.5		V _{CC} -0.8	V	Note 4
t _r /t _f	Output Rise/Fall Time	500 600	800 1200	1200 1800	500 600	800 1200	1200 1800	500 600	800 1200	1200 1800	ps	20%–80%

1. The differential propagation delay is defined as the delay from the crossing points of the differential input signals to the crossing point of the differential output signals.
2. The within-device skew is defined as the worst case difference between any two similar delay paths within a single device.
3. V_{PP}(min) is defined as the minimum input differential voltage which will cause no increase in the propagation delay. The V_{PP}(min) is AC limited for the MPC911 as a differential input as low as 50 mV will still produce full HSTL levels at the output.
4. V_{CMR} is defined as the range within which the V_{IH} level may vary, with the device still meeting the propagation delay specification. The V_{IL} level must be such that the peak to peak voltage is less than 1.0 V and greater than or equal to V_{PP}(min).

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OUTLINE DIMENSIONS

FN SUFFIX
PLASTIC PACKAGE
CASE 776-02
ISSUE D



NOTES:

1. DUE TO SPACE LIMITATION, CASE 776-02 SHALL BE REPRESENTED BY A GENERAL (SMALLER) CASE OUTLINE DRAWING RATHER THAN SHOWING ALL 28 LEADS.
2. DATUMS -L-, -M-, -N-, AND -P- DETERMINED WHERE TOP OF LEAD SHOULDER EXIT PLASTIC BODY AT MOLD PARTING LINE.
3. DIM G1, TRUE POSITION TO BE MEASURED AT DATUM -T-, SEATING PLANE.
4. DIM R AND U DO NOT INCLUDE MOLD PROTRUSION. ALLOWABLE MOLD PROTRUSION IS 0.25 (0.010) PER SIDE.
5. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
6. CONTROLLING DIMENSION: INCH.
7. 776-01 IS OBSOLETE, NEW STANDARD 776-02.

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