



M.S.KENNEDY CORP.

**30W TRIPLE
DC-DC
CONVERTERS**
**QCH28512T/
RCH28512T**

4707 Dey Road Liverpool, N.Y. 13088

(315) 701-6751

HI-REL DESIGN

- REPLACES APEX DT28512T/F
- WAVE SOLDERABLE PACKAGE
- ALL CERAMIC CAPACITORS
- NO OPTOCOUPERS
- SURFACE MOUNT MAGNETICS
- WITHSTANDS 3000 G Y1 AXIS
- OVER-VOLTAGE PROTECTION (OVP) OPTION

OTHER FEATURES – TRIPLE OUTPUT

- NO DERATING – -55°C TO $+125^{\circ}\text{C}$
- WIDE SUPPLY RANGE – 16 TO 40 VOLTS
- HIGH ISOLATION – 500V
- HIGH POWER DENSITY – 23 W/in³
- REMOTE SHUTDOWN
- EXTERNAL SYNCH INPUT (FULLY TTL COMPATIBLE OPTION)

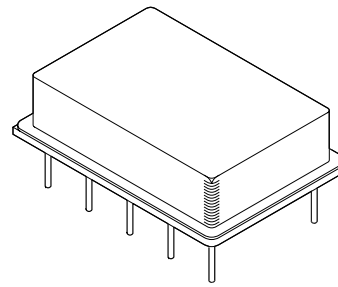
DESCRIPTION

The QCH28512T/RCH28512T series of DC-DC converters provides the ruggedness, reliability and features required to meet the advanced challenges of today's hi-rel market. This family of converters uses all surface-mounted hybrid components including magnetics, all ceramic capacitors and ultrasonic wirebonds to give long life in full temperature range and high acceleration environments. There is no power derating anywhere in the -55°C to 125°C operating range.

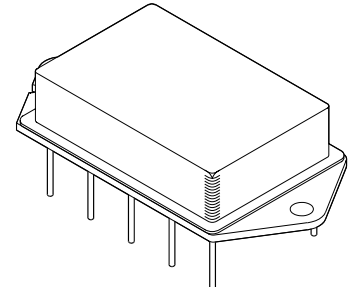
This product family is tolerant of many types of faults including load short, load overload, loss of voltage feedback, large forward and reverse voltages applied to the synchronization pin and input overvoltage. This converter family will operate during input transients to 80 volts. The output voltages have little overshoot during the input voltage transient such that typical logic and linear parts on the outputs will stay well within data sheet absolute maximums.

Other operational features include low duty factor current limit and dynamic current limit threshold. The current limit operates in a cyclic on-off mode with an on time of about 2 milliseconds and an off time of 20 milliseconds for a duty factor of about 10 percent. This limits the converter dissipation to about 1 watt, so there is no danger of overheating during an output fault at 125°C . The dynamic current limit threshold allows over 50 percent more current to flow for a short time after power application, release from shutdown or recovery from previous current limit allowing large load capacitances to be charged during turn-on.

This converter family is a push-pull 400 kHz forward converter with full isolation between the input side, output side and case. This isolation is achieved without optocouplers for an even greater margin of reliability. The converter is housed in a power DIP package and is hermetically sealed. The RCH28512T has tabs for bolting the device down.

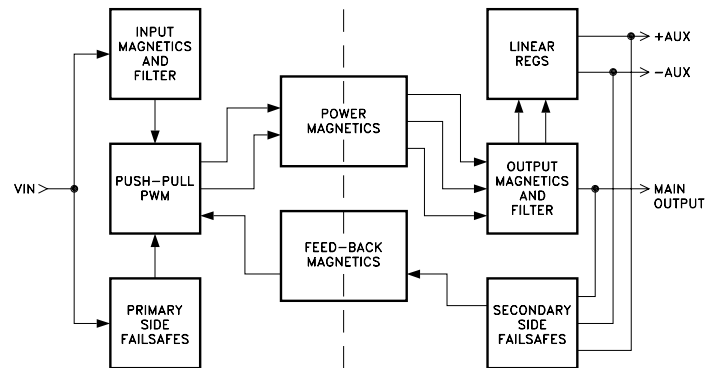


QCH28512T



RCH28512T

BLOCK DIAGRAM



EXTERNAL CONNECTIONS

- | | |
|------------------|-----------------|
| 1 POSITIVE INPUT | 10 INPUT COMMON |
| 2 +5 OUT | 9 SYNCH |
| 3 OUTCOMMON | 8 INHIBIT |
| 4 -12 OUT | 7 CASE GND |
| 5 +12 OUT | 6 CASE GND |

QCH28512T/RCH28512T

ABSOLUTE MAXIMUM RATINGS SPECIFICATIONS

ABSOLUTE MAXIMUM RATINGS

INPUT VOLTAGE RANGE
INPUT VOLTAGE TRANSIENT RANGE
OUTPUT WITHSTAND VOLTAGE
STORAGE TEMPERATURE
OPERATING TEMPERATURE
TEMPERATURE, pin soldering 10 seconds

0 to 50 VDC
80V@50mS
25 VDC
-65 to +150°C
-55 to +125°C
300 °C MAX

SPECIFICATIONS

Unless noted: $-55^{\circ}\text{C} \leq T_{\text{CASE}} \leq 125^{\circ}\text{C}$, $16\text{Vdc} \leq V_{\text{IN}} \leq 40\text{Vdc}$, $P_{\text{MAIN}} = 20\text{W}$, $P_{+\text{AUX}} = P_{-\text{AUX}} = 5\text{W}$.

PARAMETER	SYMBOL	TEST CONDITIONS	GROUP A ¹²		TYPICAL	MAX	UNITS	COMMENT
			SUBGROUP	MIN				
INPUT VOLTAGE	V _{IN}	No load to full load	1, 2, 3	0 16 50	28	50 40 80	Vdc Vpk	Normal Operating ¹ Transient
UVLO	V _{UVLO}		1, 2, 3	10	12.5	14	Vdc	
INPUT CURRENT	I _{IN}	NoLoad Full load ² Inhibited	1, 2, 3	 1.25 0.0001	0.12 1.57 0.002	 1.65 0.005	Adc	
OUTPUT CURRENT ³	I _{MAIN}		1 2, 3		4.00	6.00	Adc	
	I _{+AUX}		1 2, 3		0.42	0.83		
	I _{-AUX}		1 2,3		-0.42	-0.83		
OUTPUT VOLTAGE ^{2,4}	V _{MAIN}		1 2,3	4.95	5.0	5.05	Vdc	
	V _{+AUX}		1 2, 3	11.85	12.00	12.15		
	V _{-AUX}		1 2, 3	-11.85	-12.00	-12.15		
OVP ⁴	V _{OVP}		1 2, 3	5.3 5.14	5.6	5.9 6.10	Vdc	
OUTPUT POWER	P _{OUT}	P _{MAIN} P _{±AUX} P _{TOT} P _{OVERLOAD}	1, 2, 3	0 0 0 30	20 10	30 20 30 65 ⁽⁵⁾	W	
EFFICIENCY ²	η		1 2, 3		63		%	
LINE REGULATION	VR _{LINE}	V _{IN} 16-40V	1 2, 3 1 2, 3		5 10 50 120	10 50 75 150	mV	Main Aux
LOAD REGULATION	VR _{LOAD}	I _{MAIN} 0↔4A ±I _{AUX} 0↔0.42A	1 2, 3 1 2, 3		5 20 50 150	20 50 75 250	mV	Main Aux

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PARAMETER	SYMBOL	TEST CONDITIONS	GROUP A ^{1,2}		TYPICAL	MAX	UNITS	COMMENT
			SUBGROUP	MIN				
CROSS REGULATION	V_{RCROSS}	$\pm I_{\text{AUX}}$	1		5	20	mV	Main
		$0 \leftrightarrow 0.42\text{A}$	2, 3		20	50		
		I_{MAIN}	1		50	75		$\pm\text{Aux}$
		$0 \leftrightarrow 4\text{A}$	2, 3		150	250		
CURRENT LIMIT	I_{LIM}		1	115	125		%	
			2, 3					
LOAD FAULT POWER	P_{D}	Overload ⁶	1, 2, 3		14	25	W	
		Short Ckt.			6			
INHIBIT PIN VOLTAGE ⁷	V_{ENH}	Enabled	1, 2, 3	12		16.8	V dc	
	V_{ENL}	Inhibited		0		5		
INHIBIT PIN CURRENT ⁷	I_{ENH}	Enabled	1, 2, 3	0		10	$\mu\text{A dc}$	
	I_{ENL}	Inhibited		650	1200	1800		
ISOLATION	R_{ISO}	500V dc	1, 2, 3	100			Meg Ω	Primary to Secondary to Case
CAPACITIVE LOAD	C_{L}		1, 2, 3		330	500	μF	Main
					10	22		$\pm\text{Aux}$
INPUT CURRENT RIPPLE ²	I_{RIP}		4		100		mA rms	
			5, 6		120	250		
OUTPUT VOLTAGE RIPPLE	V_{RIP}		4		15	45	mV rms	Main
			5, 6		30	90		
			4		12	36		$\pm\text{Aux}$
			5, 6		25	75		
			4, 5, 6	350	400	450	kHz	
SYNC. FREQUENCY	F_{SYNC}		4, 5, 6	300		650	kHz	
SYNC. PIN VOLTAGE ⁸	V_{HI}		1, 2, 3	2.4		—	V dc	
	V_{LO}			—		0.4		
SYNC. PIN CURRENT	I_{SYNH}		1, 2, 3		-400		$\mu\text{A dc}$	
	I_{SYNL}				-500	-750		
TURN ON TIME ⁹	T_{ON}		4		4	8	mS	
TURN ON OVERSHOOT	V_{TON}	$C_{\text{L}}=0$	4		0	300	mVdc	Main
			4			900		$\pm\text{Aux}$
STEP LOAD TRANSIENT, RESPONSE	V_{TLOAD}	$C_{\text{L}}=0$, $I_{\text{MAIN}}=50 \leftrightarrow 100\%$	4	-800		+800	mVdc	Main
		$C_{\text{L}}=0$, $I_{\text{AUX}}=50 \leftrightarrow 100\%$	4		500	750		$\pm\text{Aux}$

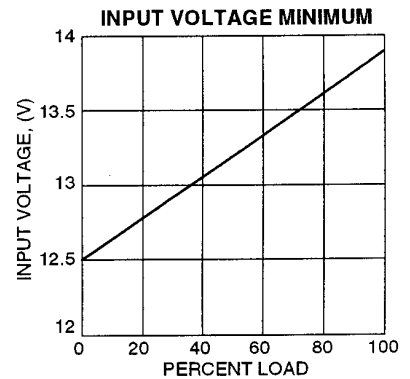
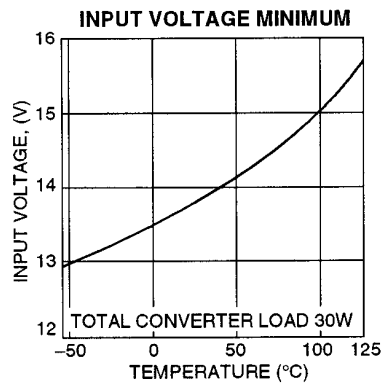
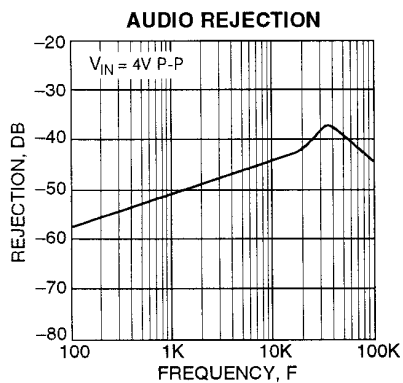
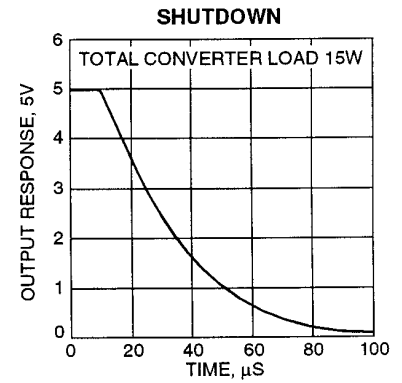
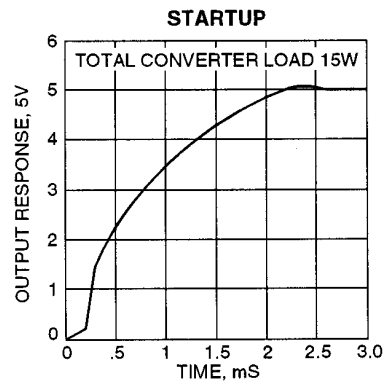
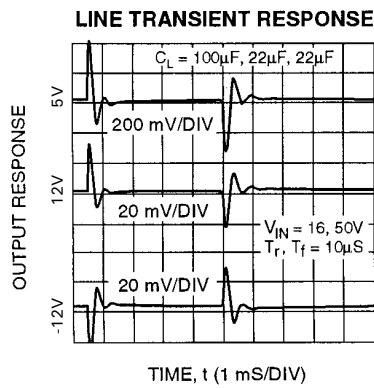
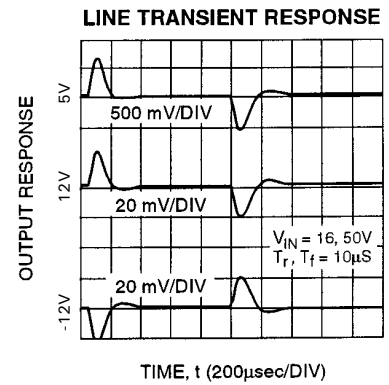
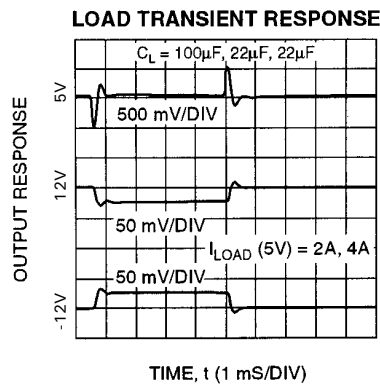
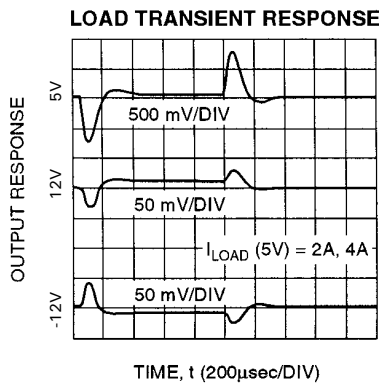
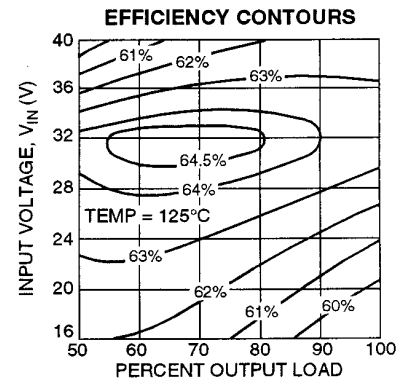
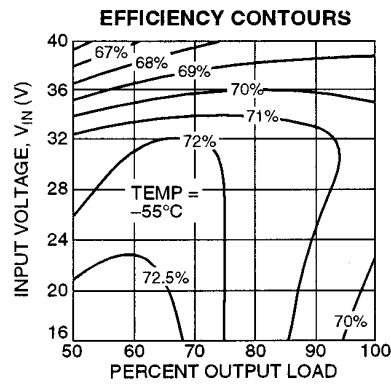
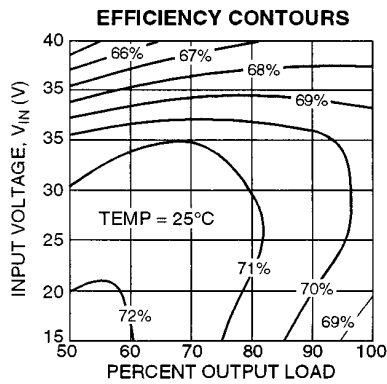
QCH28512T/RCH28512T

SPECIFICATIONS

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PARAMETER	SYMBOL	TEST CONDITIONS	GROUP A ¹²		TYPICAL	MAX	UNITS	COMMENT
			SUBGROUP	MIN				
STEP LOAD TRANSIENT ¹⁰ RECOVERY	TR _{LOAD}	C _L =0	4 4		0.05 3	0.10 5	mS	Main ±Aux
STEP LINE TRANSIENT, RESPONSE	VT _{LINE}	C _L =0	4 4		150 100	250 250	mVdc	Main ±Aux
STEP LINE TRANSIENT ¹⁰ RECOVERY	TR _{LINE}	C _L =0	4 4		0.05 3	0.10 5	mS	Main ±Aux
LOAD FAULT TRANSIENT, RESPONSE	VT _{FLT}	C _L =0	4 4		0	300 900	mVdc	Main ±Aux
LOAD FAULT TRANSIENT ¹⁰ , RESPONSE	TR _{FLT}	C _L =0	4 4		40	100 TBD	mS	Main ±Aux
INHIBIT DELAY ¹¹	TD _{INH}	C _L =0	4 4		0.1	0.2 TBD	mS	Main ±Aux
INHIBIT RECOVERY ⁹	TR _{INH}	C _L =0	4 4		4	8 TBD	mS	Main ±Aux

- NOTES: 1. Under Voltage Lock Out (UVLO) is approx. 10.5-13.5 Vdc.
2. Vin = 28±1 V dc. Nominal output power: P_{MAIN} = 20W, P_{+AUX} = 5W, P_{-AUX} = 5W.
3. Full power output must be equal to or less than 30 watts. Short duration Aux current is 2A peak.
4. Over-Voltage Protection (OVP) guaranteed by design not to exceed 6V dc on main output under any static condition at room temperature. On models with OVP option, only. Therefore this parameter is limited to <5.5V on these models.
5. Short Term Over-Load (STOL).
6. Continuous overload. Auxiliary outputs are protected with thermal shutdown.
7. Internal Pull-Up typically 22k ohms to V_{IN}. For 80-volt transients, input device must be able to sink at least 3.5 mA.
8. Internal Pull-Up approximately 22k ohms to typically 12V. This is an open collector, TTL compatible, edge triggered active low input. A valid low must exist for 20% to 80% duty ratio. The converter takes approximately 2 mS to synchronize. For "TTL Sync" option V_{MAX} ≤ 55V.
9. Time for outputs to reach 90% of final value. Measured from valid Inhibit High V_{ENH} or valid V_{IN}.
10. Measured from start of transient to response within 90% of final value.
11. Time for outputs to reach 50% of initial value. Measured from valid Inhibit Low V_{ENL} or V_{IN} below UVLO.
12. As per MIL-PRF-38534.



SYNCHRONIZATION

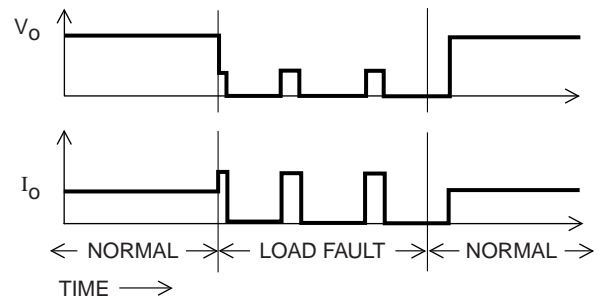
The synch pin allows the converter to be synchronized in frequency and phase with an external signal. This signal should be TTL compatible (0.8 max V_{INL} and 2.0 V min V_{INH}). Overdrive up to 10V is not harmful. Transient voltage withstand is $\pm 50V$ with respect to input common. The frequency applied to this pin must be between 350 and 600 kHz with duty factor anywhere from 20 to 80 percent.

SHUTDOWN

The shutdown pin turns off the power conversion circuits and puts the converter in standby mode. The shutdown pin is referenced to the input common pin. In standby mode the converter only draws 2.2 mA typical (5.0 mA maximum) input current from V_{IN} . For normal operation this pin should be open circuited and allowed to float. For shutdown pull this pin below 1 volt. The maximum open circuit voltage that will appear on this pin is 15 volts. See graphs for typical time response of the converter to assert and release shutdown.

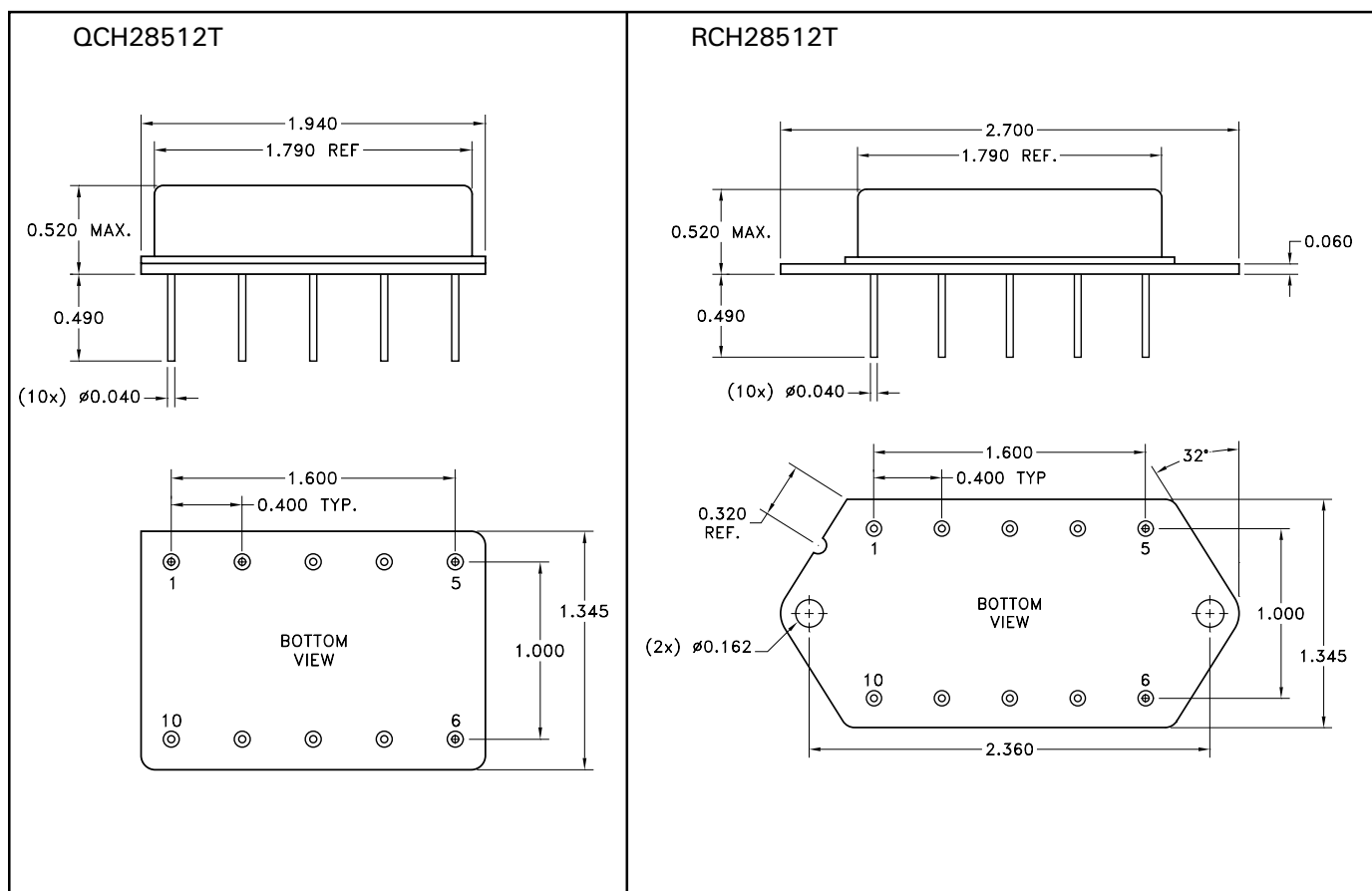
LOAD FAULT RESPONSE

The QCH28512T/RCH28512T family of DC-DC converters use a common fault protection approach. Load fault conditions include short-circuit and overload conditions. The QCH28512T/RCH28512T converters respond to load faults on the main output (+5V) by turning off all power conversion circuits for a period of time and then attempting to restart. The figure below shows the fault protection response to an output overload, the turn on plus detect fault time is typically two milliseconds and the off time is typically twenty milliseconds. The net "on" duty factor during a fault is only 10 percent, resulting in low converter dissipation and immunity from overheating at 125°C. An added benefit to this method is that the amount of output current that defines a fault is programmed to increase by 1.5X during the first few hundred microseconds of operation after power-up, load fault recovery or release of the shutdown pin. This allows QCH28512T/RCH28512T converters to bring up capacitive and other difficult loads more reliably than some competing converters.



The auxiliary outputs have individual overcurrent protection and independent fault protection. A rapid response thermal shutdown feature adds to the reliability of the converter by protecting each auxiliary output from load faults. Substantial current may be drawn from either auxiliary output but is limited to safe levels by independent current sensing circuitry on each. If the fault or overload continues for extended periods the thermal shut down circuitry will cycle the output to limit the maximum internal hotspot temperature to a safe level.

MECHANICAL SPECIFICATIONS



NOTE: ALL DIMENSIONS ARE ± 0.010 INCHES UNLESS OTHERWISE LABELED.
ESD Triangle indicates Pin 1.

ORDERING INFORMATION

QCH28512T

SCREENING
 BLANK = INDUSTRIAL; E = EXTENDED RELIABILITY;
 H = CLASS H; K = CLASS K
TRIPLE OUTPUT
OUTPUT VOLTAGES
 512 = 5V AND $\pm 12V$
NOMINAL INPUT VOLTAGE
GENERAL PART NUMBER (QCH = NO TABS; RCH = TABS)

The above example is an industrial grade 5V and $\pm 12V$ triple output converter without tabs

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