

LM3700/LM3701

Microprocessor Supervisory Circuit with Low Line Output

General Description

The LM3700/LM3701 series of microprocessor supervisory circuits provide the maximum flexibility for monitoring power supplies and battery controlled functions in systems without backup batteries. The LM3700/LM3701 series are available in a 9-bump micro SMD package.

Built-in features include the following:

Reset: Reset is asserted during power-up, power-down, and brownout conditions. $\overline{\text{RESET}}$ is guaranteed down to V_{CC} of 1.0V.

Low Line Output: This early power failure warning indicator goes low when the supply voltage drops to a value which is 2% higher than the reset threshold voltage.

Features

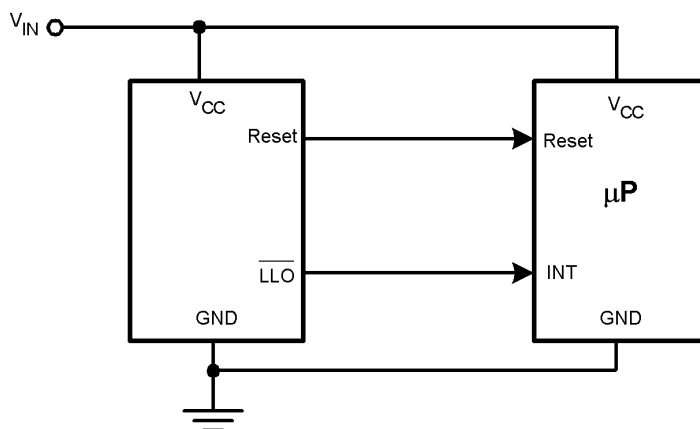
- Standard Reset Threshold voltage: 3.08V
- Custom Reset Threshold voltages: For other voltages between 2.2V and 5.0V in 10mV increments, contact National Semiconductor Corp.

- No external components required
- $\overline{\text{RESET}}$ (LM3700) or RESET (LM3701) outputs
- Precision supply voltage monitor
- Factory programmable Reset Timeout Delay
- Available in micro SMD package for minimum footprint
- $\pm 0.5\%$ Reset threshold accuracy at room temperature
- $\pm 2\%$ Reset threshold accuracy over temperature extremes
- Reset assertion down to 1V V_{CC} ($\overline{\text{RESET}}$ option only)
- 28 μA V_{CC} supply current

Applications

- Embedded Controllers and Processors
- Intelligent Instruments
- Automotive Systems
- Critical μP Power Monitoring

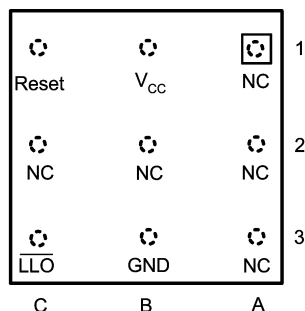
Typical Application



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Connection Diagram

Top View
(looking from the coating side)
micro SMD 9 Bump Package
BPA09

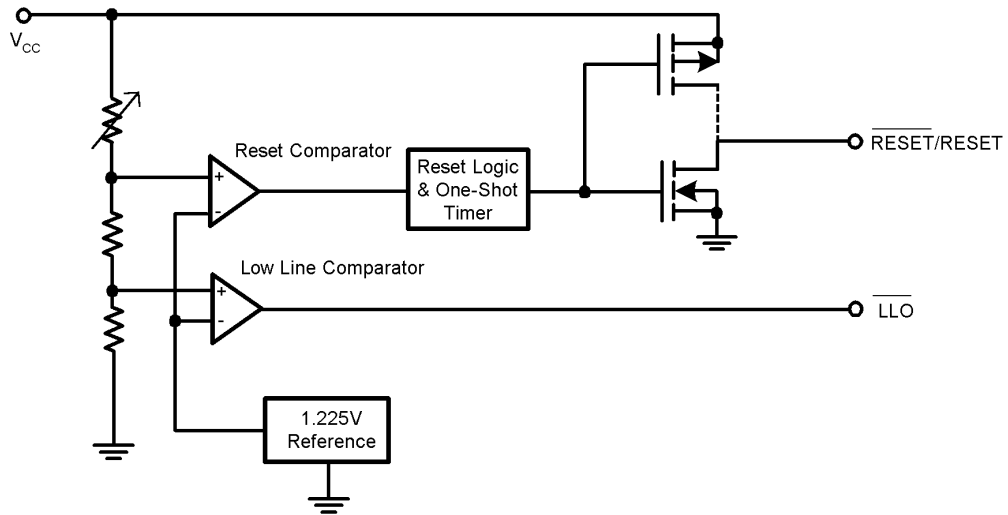


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Pin Descriptions

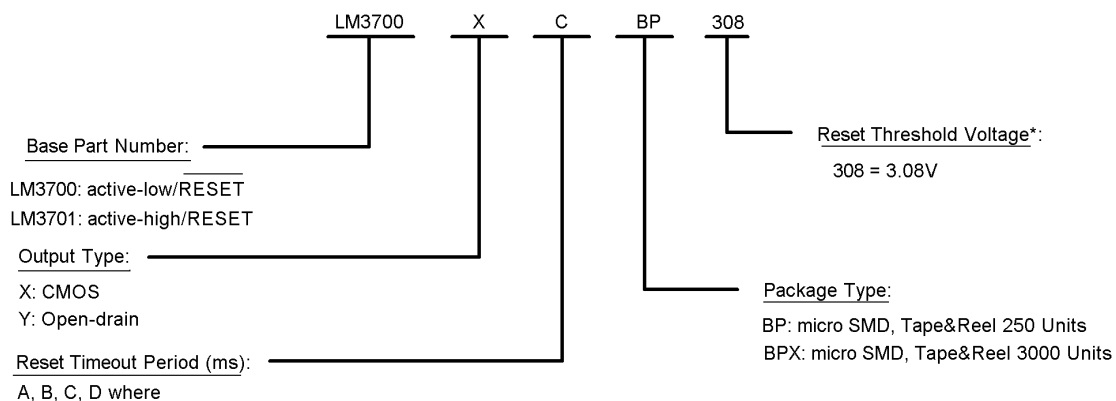
Bump No.	Name	Function
B1	V_{CC}	Power Supply input.
C1	$\overline{\text{RESET}}$	Reset Logic Output. Pulses low for t_{RP} (Reset Timeout Period) when triggered, and stays low whenever V_{CC} is below the reset threshold or when $\overline{\text{MR}}$ is below V_{MRT} . It remains low for t_{RP} after either V_{CC} rises above the reset threshold, or after $\overline{\text{MR}}$ input rises above V_{MRT} (LM3700 only).
	RESET	Reset Logic Output. RESET is the inverse of $\overline{\text{RESET}}$ (LM3701 only).
C3	$\overline{\text{LLO}}$	Low-Line Logic Output. Early Power-Fail warning output. Low when V_{CC} falls below V_{LLOT} (Low-Line Output Threshold). This output can be used to generate an NMI (Non-Maskable Interrupt) to provide an early warning of imminent power-failure.
B3	GND	Ground reference for all signals.
A1, A2, A3, C2	NC	No Connect.
B2	NC	No Connect. Test input used at factory only. Leave floating.

Block Diagram



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Ordering Information



* = available upon request. Contact National Semiconductor

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*For other voltages between 2.2V and 5.0V, please contact National Semiconductor sales office.

LM3700/LM3701

Part Number	Output	Reset Timeout Period	Package Marking
LM3700XCBP-308	totem-pole	200ms	%%I0
LM3700XCBPX-308	totem-pole	200ms	%%I0
LM3701XCBP-308	totem-pole	200ms	%%I1
LM3701XCBPX-308	totem-pole	200ms	%%I1

%% is the datecode and will vary with time.

Table Of Functions

Part Number	Active Low Reset	Active High Reset	Output (X = totem-pole) (Y = open-drain)	Reset Timeout Period	Low Line Output
LM3700	x		X, Y*	Customized	x
LM3701		x	X	Customized	x

* = available upon request. Contact National

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage (V_{CC}) -0.3V to 6.0V
 All Other Inputs -0.3V to $V_{CC} + 0.3V$

ESD Ratings (Note 2)

Human Body Model 1.5kV
 Machine Model 150V

Power Dissipation

(Note 3)

Operating Ratings (Note 1)Temperature Range $-40^{\circ}\text{C} \leq T_J \leq 85^{\circ}\text{C}$ **LM3700/LM3701 Series Electrical Characteristics**

Limits in the standard typeface are for $T_J = 25^{\circ}\text{C}$ and limits in **boldface type** apply over full operating range. Unless otherwise specified: $V_{CC} = +2.2V$ to $5.5V$.

Symbol	Parameter	Conditions	Min	Typ	Max	Units
POWER SUPPLY						
V_{CC}	Operating Voltage Range: V_{CC}	LM3700	1.0		5.5	V
		LM3701	1.2		5.5	
I_{CC}	V_{CC} Supply Current	All inputs = V_{CC} ; all outputs floating		28	50	μA
RESET THRESHOLD						
V_{RST}	Reset Threshold	V_{CC} falling	-0.5 -2	V_{RST}	+0.5 +2	%
		V_{CC} falling: $T_A = 0^{\circ}\text{C}$ to 70°C	-1.5		+1.5	
V_{RSTH}	Reset Threshold Hysteresis			$0.0032 \cdot V_{RST}$		mV
t_{RP}	Reset Timeout Period	Reset Timeout Period = A	1	1.4	2	ms
		Reset Timeout Period = B	20	28	40	
		Reset Timeout Period = C	140	200	280	
		Reset Timeout Period = D	1120	1600	2240	
t_{RD}	V_{CC} to Reset Delay	V_{CC} falling at $1\text{mV}/\mu\text{s}$		20		μs
RESET (LM3701)						
V_{OL}	RESET	$V_{CC} > 2.25V$, $I_{SINK} = 900\mu\text{A}$			0.3	V
		$V_{CC} > 2.7V$, $I_{SINK} = 1.2\text{mA}$			0.3	
		$V_{CC} > 4.5V$, $I_{SINK} = 3.2\text{mA}$			0.4	
V_{OH}	RESET	$V_{CC} > 1.2V$, $I_{SOURCE} = 50\mu\text{A}$	0.8 V_{CC}			V
		$V_{CC} > 1.8V$, $I_{SOURCE} = 150\mu\text{A}$	0.8 V_{CC}			
		$V_{CC} > 2.25V$, $I_{SOURCE} = 300\mu\text{A}$	0.8 V_{CC}			
		$V_{CC} > 2.7V$, $I_{SOURCE} = 500\mu\text{A}$	0.8 V_{CC}			
		$V_{CC} > 4.5V$, $I_{SOURCE} = 800\mu\text{A}$	$V_{CC} - 1.5V$			
I_{LKG}	Output Leakage Current	$V_{RESET} = 5.5V$			1.0	μA
RESET (LM3700)						
V_{OL}	RESET	$V_{CC} > 1.0V$, $I_{SINK} = 50\mu\text{A}$			0.3	V
		$V_{CC} > 1.2V$, $I_{SINK} = 100\mu\text{A}$			0.3	
		$V_{CC} > 2.25V$, $I_{SINK} = 900\mu\text{A}$			0.3	
		$V_{CC} > 2.7V$, $I_{SINK} = 1.2\text{mA}$			0.3	
		$V_{CC} > 4.5V$, $I_{SINK} = 3.2\text{mA}$			0.4	
V_{OH}	RESET	$V_{CC} > 2.25V$, $I_{SOURCE} = 300\mu\text{A}$	0.8 V_{CC}			V
		$V_{CC} > 2.7V$, $I_{SOURCE} = 500\mu\text{A}$	0.8 V_{CC}			
		$V_{CC} > 4.5V$, $I_{SOURCE} = 800\mu\text{A}$	$V_{CC} - 1.5V$			

LM3700/LM3701 Series Electrical Characteristics (Continued)

Limits in the standard typeface are for $T_J = 25^\circ\text{C}$ and limits in **boldface type** apply over full operating range. Unless otherwise specified: $V_{CC} = +2.2\text{V}$ to 5.5V .

Symbol	Parameter	Conditions	Min	Typ	Max	Units
LLO						
V _{OL}	LLO Output Voltage	V _{CC} > 2.25V, I _{SINK} = 900μA			0.3	V
		V _{CC} > 2.7V, I _{SINK} = 1.2mA			0.3	
		V _{CC} > 4.5V, I _{SINK} = 3.2mA			0.4	
V _{OH}		V _{CC} > 2.25V, I _{SOURCE} = 300μA	0.8 V _{CC}			
		V _{CC} > 2.7V, I _{SOURCE} = 500μA	0.8 V _{CC}			
		V _{CC} > 4.5V, I _{SOURCE} = 800μA	V _{CC} – 1.5V			
LLO OUTPUT						
V _{LLOT}	LLO Output Threshold (V _{LLO} – V _{RST} , V _{CC} falling)		1.01•V _{RST}	1.02•V _{RST}	1.03•V _{RST}	V
V _{LLOTH}	Low-Line Comparator Hysteresis			0.0032•V _{RST}		mV
t _{CD}	Low-Line Comparator Delay	V _{CC} falling at 1mV/μs		20		μs

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. **Operating Ratings** indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed conditions.

Note 2: The Human Body model is a 100 pF capacitor discharged through a $1.5\text{ k}\Omega$ resistor into each pin. The machine model is a 200 pF capacitor discharged directly into each pin.

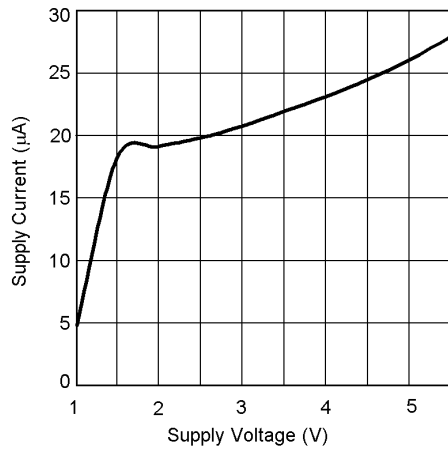
Note 3: The maximum allowable power dissipation is a function of the maximum junction temperature, $T_J(\text{MAX})$, the junction-to-ambient thermal resistance, θ_{J-A} , and the ambient temperature, T_A . The maximum allowable power dissipation at any ambient temperature is calculated using:

$$P(\text{MAX}) = \frac{T_J(\text{MAX}) - T_A}{\theta_{J-A}}$$

Where the value of θ_{J-A} for the micro SMD package is $220^\circ\text{C}/\text{W}$.

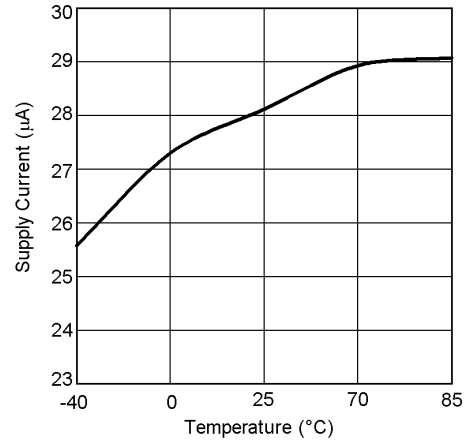
Typical Performance Characteristics

Supply Current vs Supply Voltage



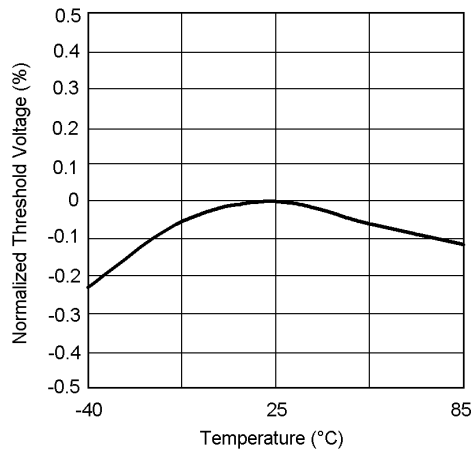
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3.3V Supply Current vs Temperature



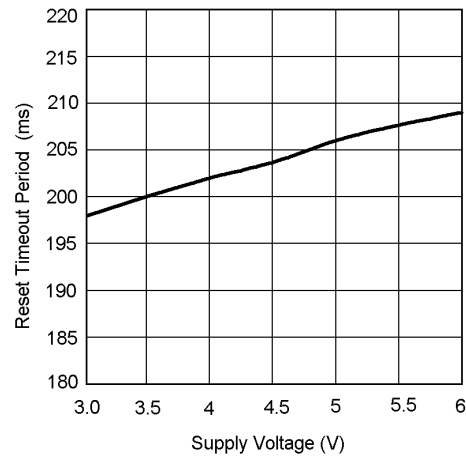
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Normalized Reset Threshold Voltage vs Temperature



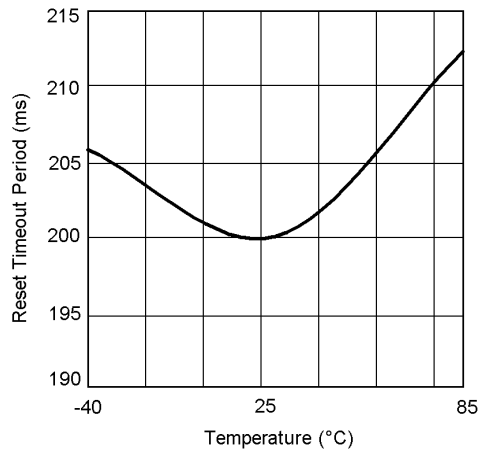
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Reset Timeout Period vs V_{CC}



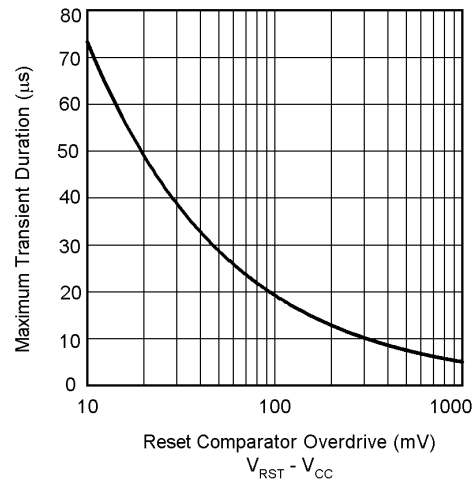
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Reset Timeout Period vs Temperature



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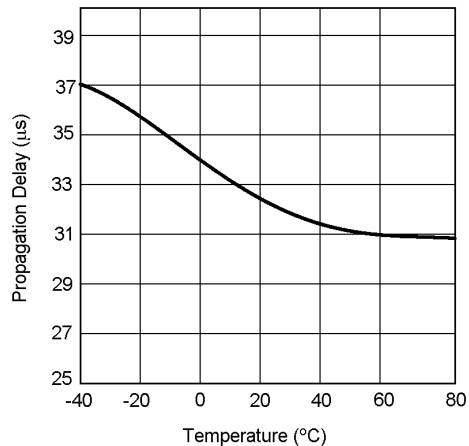
Max. Transient Duration vs Reset Comparator Overdrive ($V_{CC} = 3.3V$)



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Typical Performance Characteristics (Continued)

Low-Line Comparator Propagation Delay vs Temperature



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Circuit Information

Reset Output

The Reset input of a μP initializes the device into a known state. The LM3700/LM3701 microprocessor supervisory circuits assert a forced reset output to prevent code execution errors during power-up, power-down, and brownout conditions.

$\overline{\text{RESET}}$ is guaranteed valid for $V_{CC} > 1V$. Once V_{CC} exceeds the reset threshold, an internal timer maintains the output for the reset timeout period. After this interval, reset goes high. The LM3700 offers an active-low $\overline{\text{RESET}}$; The LM3701 offers an active-high RESET.

Any time V_{CC} drops below the reset threshold (such as during a brownout), the reset activates. When V_{CC} again rises above the reset threshold, the internal timer starts. Reset holds until V_{CC} exceeds the reset threshold for longer than the reset timeout period. After this time, reset releases.

Reset Threshold

The LM3700/LM3701 family is available with a reset voltage of 3.08V. Other reset thresholds in the 2.20V to 5.0V range, in steps of 10 mV, are available; contact National Semiconductor for details.

Low-Line Output ($\overline{\text{LLO}}$)

The low-line output comparator is typically used to provide a non-maskable interrupt to a μP when V_{CC} begins falling. $\overline{\text{LLO}}$ monitors V_{CC} and goes low when V_{CC} falls below V_{LLOT} (typically $1.02 \cdot V_{RST}$) with hysteresis of $0.0032 \cdot V_{RST}$.

Special Precautions for the micro SMD Package

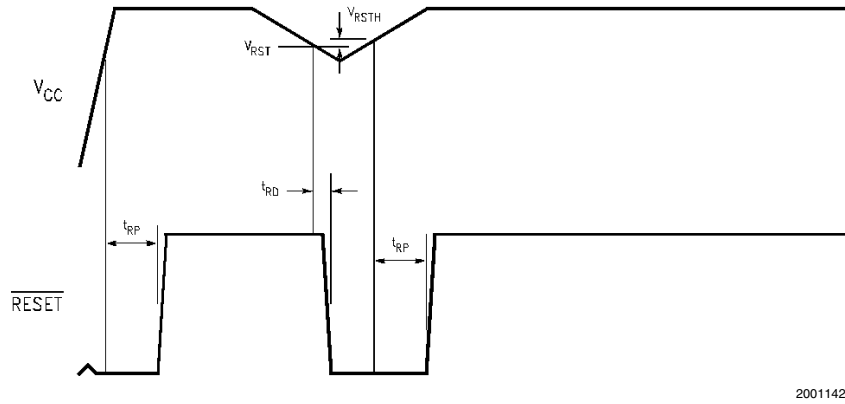
As with most integrated circuits, the LM3700 and LM3701 are sensitive to exposure from visible and infrared (IR) light radiation. Unlike a plastic encapsulated IC, the micro SMD package has very limited shielding from light, and some sensitivity to light reflected from the surface of the PC board or long wavelength IR entering the die from the side may be experienced. This light could have an unpredictable affect on the electrical performance of the IC. Care should be taken to shield the device from direct exposure to bright visible or IR light during operation.

Micro SMD Mounting

The micro SMD package requires specific mounting techniques which are detailed in National Semiconductor Application Note AN-1112. Referring to the section **Surface Mount Technology (SMT) Assembly Considerations**, it should be noted that the pad style which must be used with the 9-pin package is the NSMD (non-solder mask defined) type.

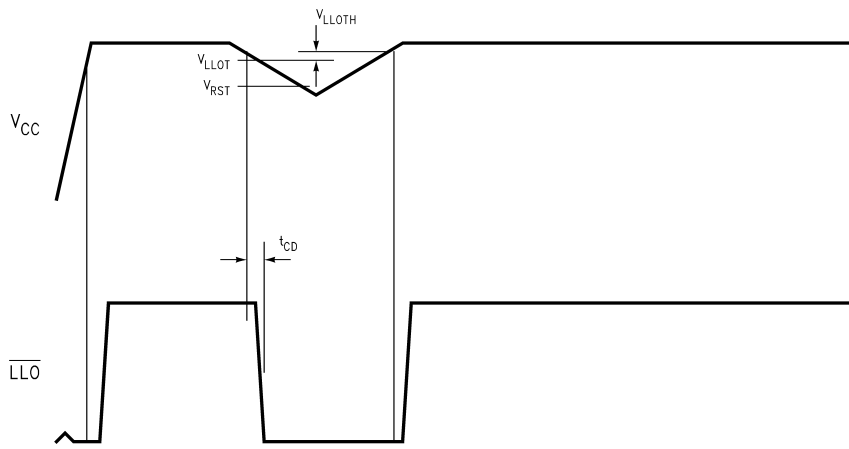
For best results during assembly, alignment ordinals on the PC board may be used to facilitate placement of the micro SMD device.

Timing Diagrams



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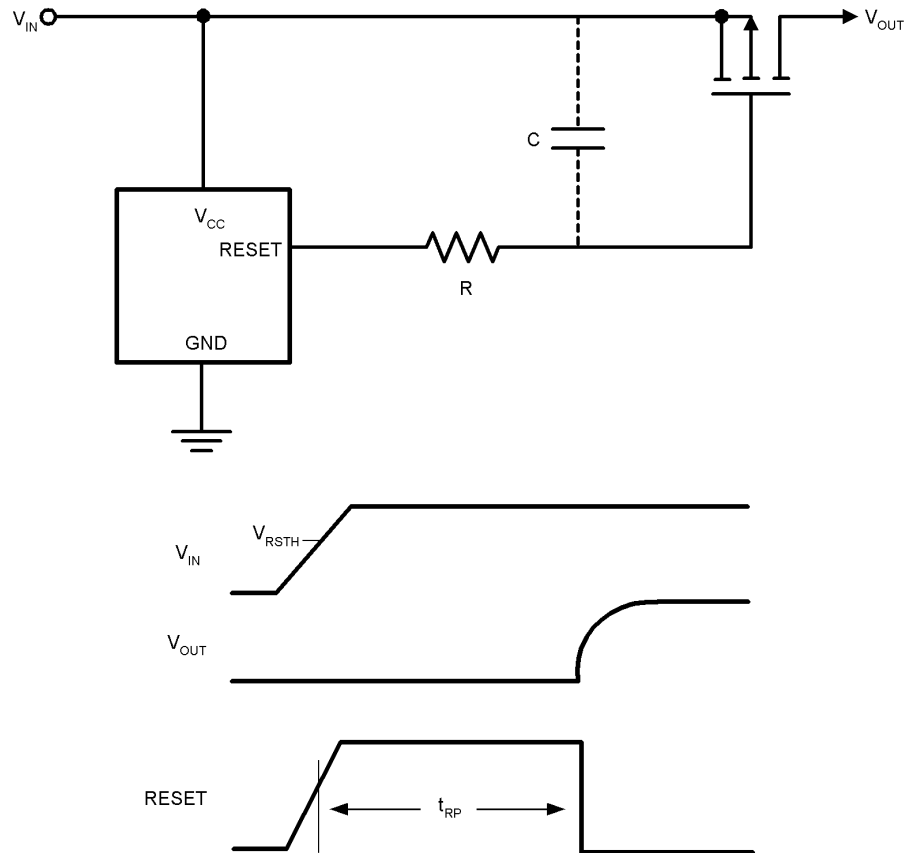
FIGURE 1. LM3700 Reset Time



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FIGURE 2. $\overline{\text{LLO}}$ Output

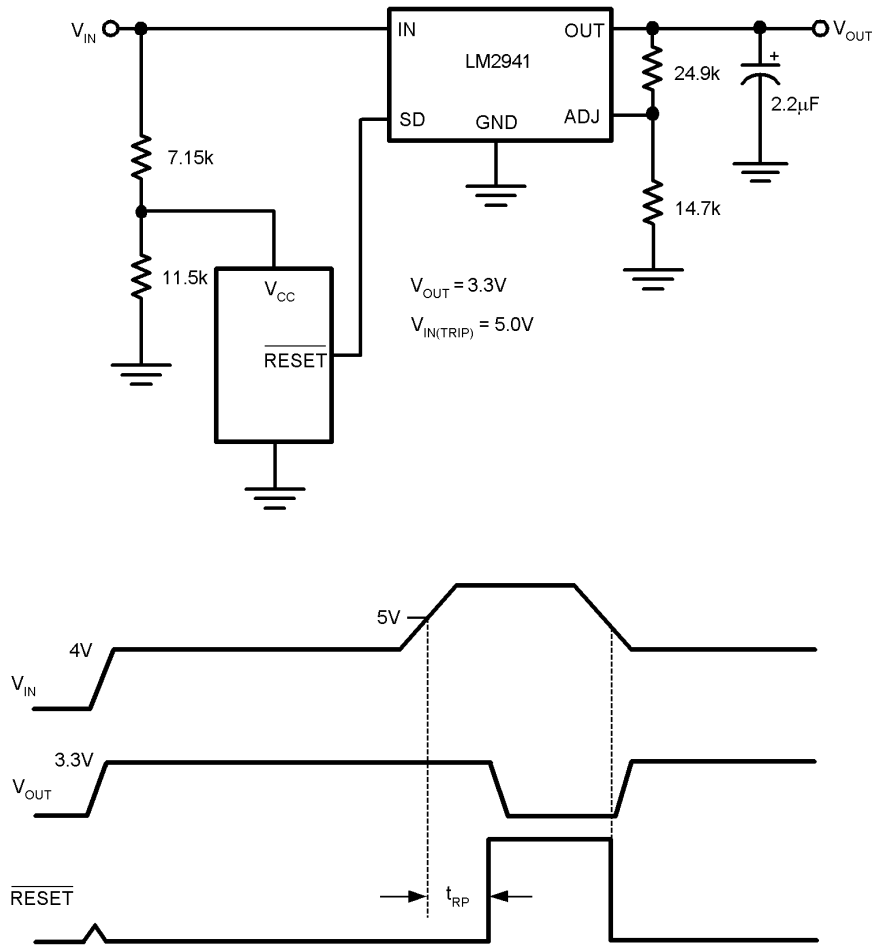
Typical Application Circuits



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FIGURE 3. LM3701 Power-On Delay

Typical Application Circuits (Continued)

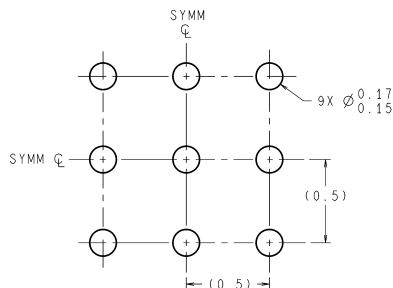


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FIGURE 4. Regulator/Switch with Long-Term Overvoltage Lockout Prevents Overdissipation in Linear Regulator

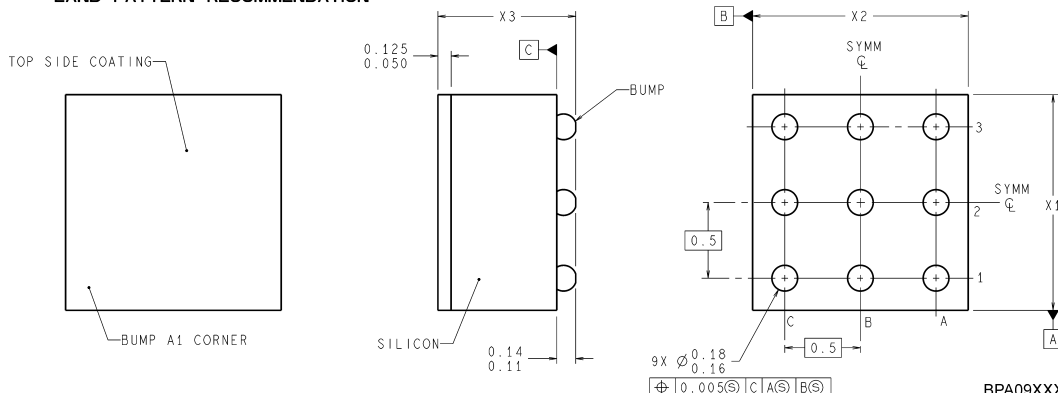
Physical Dimensions

inches (millimeters)
unless otherwise noted



DIMENSIONS ARE IN MILLIMETERS

LAND PATTERN RECOMMENDATION



BPA09XXX (Rev D)

NOTES: UNLESS OTHERWISE SPECIFIED

1. EPOXY COATING
2. 63Sn/37Pb EUTECTIC BUMP
3. RECOMMEND NON-SOLDER MASK DEFINED LANDING PAD.
4. PIN 1 IS ESTABLISHED BY LOWER LEFT CORNER WITH RESPECT TO TEXT ORIENTATION. REMAINING PINS ARE NUMBERED COUNTER CLOCKWISE.
5. XXX IN DRAWING NUMBER REPRESENTS PACKAGE SIZE VARIATION WHERE X1 IS PACKAGE WIDTH, X2 IS PACKAGE LENGTH AND X3 IS PACKAGE HEIGHT.
6. NO JEDEC REGISTRATION AS OF AUG. 1999.

9 bump micro SMD Package NS Package Number BPA09FFB

The dimensions of X1, X2 and X3 are given below

X1 = 1.412mm

X2 = 1.412mm

X3 = 0.850mm

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