

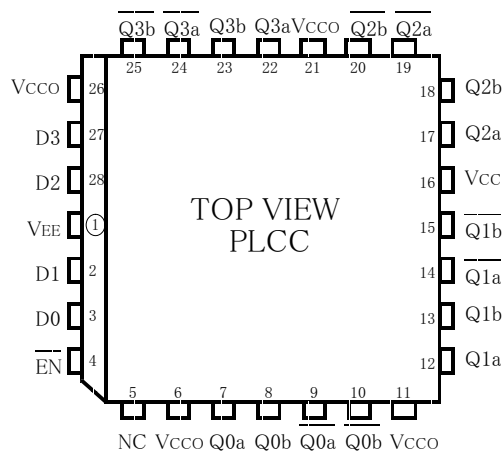
Description

The NB10EP112 is a quad driver with two pairs of OR/NOR outputs from each gate, and a common, buffered enable input.

Features

- 600ps max. propagation delay
- Common enable input
- 75k Ω internal pulldown resistors

Pin assignment

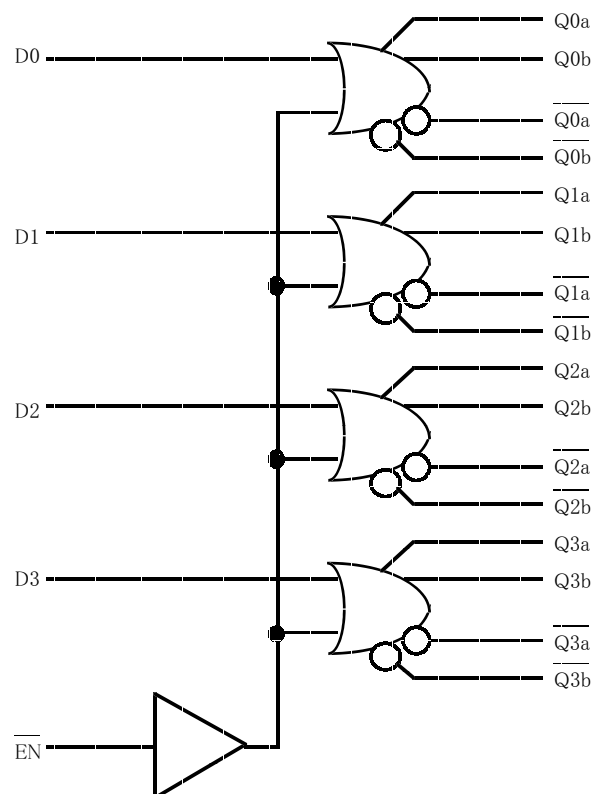


*All VCC and VCCO pins are tied together on the die.

Pin names

Pin	Function
D0-D3	Data inputs
EN	Enable input
Qna, Qnb	True outputs
Qna, Qnb	Inverting outputs

Block diagram



NB10EP112

DC Electrical Characteristics

$T_a=0\sim 85^{\circ}\text{C}$

$V_{CC}=0\text{V}; V_{EE}=-5.2\text{V} \pm 5\%$

Symbol	Characteristic	Min	Typ	Max	Unit
IIH	Input High Current D $\overline{\text{E}}$				μA
				200	
				200	
IEE	Power Supply Current		35	51	mA

AC Electrical Characteristics

$T_a=0\sim 85^{\circ}\text{C}$

$V_{CC}=0\text{V}; V_{EE}=-5.2\text{V} \pm 5\%$

Symbol	Characteristic	Min	Typ	Max	Unit
tPLH tPHL	Propagation Delay to Output D $\overline{\text{EN}}$	200 275	400 450	600 675	ps
tskew	Within-Device Skew Dn to Qn, $\overline{\text{Qn}}$ *1 Qna to Qnb *2		80 40		ps
tr tf	Rise/Fall Time 20 – 80%	275	425	700	ps

1. Within-device skew is defined as identical transitions on similar paths through a device.

2. Skew defined between common OR or common NOR outputs of a single gate.