

NCS2200 Series

Low Voltage Comparators

The NCS2200 Series is an industry first sub-one volt, low power comparator family. These devices consume only 10 μ A of supply current. They are guaranteed to operate at a low voltage of 0.85 V which allows them to be used in systems that require less than 1.0 V and are fully operational up to 6.0 V which makes them convenient for use in both 3.0 V and 5.0 V systems. Additional features include no output phase inversion with overdriven inputs, internal hysteresis, which allows for clean output switching, and rail-to-rail input and output performance. The NCS2200 Series is available in the tiny SOT23-5 and SOT23-6 package. There are eight options featuring two industry standard pinouts. (Table 1)

The NCS2201/3 Series in the SOT23-6 package features an enable function, which can be externally controlled. When the enable pin is pulled low (output tri-state mode), current consumption is typically 0.3 μ A. This allows the user to implement these devices in power sensitive applications such as portable electronics.

Features

- Operating Voltage of 0.85 V to 6.0 V
- Rail-to-Rail Input/Output Performance
- Low Supply Current of 10 μ A
- No Phase Inversion with Overdriven Input Signals
- Glitchless Transitioning in or out of Tri-State Mode
- Complementary or Open Drain Output Configuration
- Available with the Enable Function
- Internal Hysteresis
- Propagation Delay of 1.1 μ s

Typical Applications

- Single Cell NiCd/NiMH Battery Powered Applications
- Cellular Telephones
- Alarm and Security Systems
- Personal Digital Assistants

Table 1. Comparator Selector Guide

Output Type	Device	Package	Pinout Style
Complementary	NCS2200SN1T1	SOT23-5	1
Complementary	NCS2200SN2T1	SOT23-5	2
Complementary, Enable	NCS2201SN1T1	SOT23-6	1
Complementary, Enable	NCS2201SN2T1	SOT23-6	2
Open Drain	NCS2202SN1T1	SOT23-5	1
Open Drain	NCS2202SN2T1	SOT23-5	2
Open Drain, Enable	NCS2203SN1T1	SOT23-6	1
Open Drain, Enable	NCS2203SN2T1	SOT23-6	2



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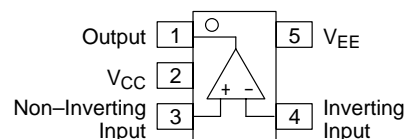


SOT23-5
(TSOP-5, SC59-5)
SN SUFFIX
CASE 483

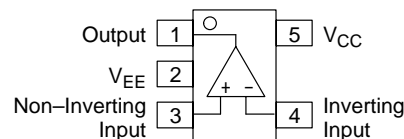


SOT23-6
(TSOP-6, SC59-6)
SN SUFFIX
CASE 318G

PIN CONNECTIONS

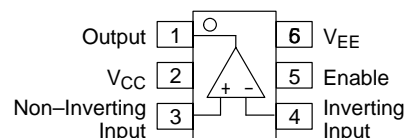


Style 1 Pinout (SN1T1)

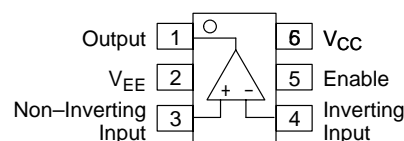


Style 2 Pinout (SN2T1)

SOT23-5



Style 1 Pinout (SN1T1)



Style 2 Pinout (SN2T1)

SOT23-6

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 11 of this data sheet.

DEVICE MARKING INFORMATION

See general marking information in the device marking section on page 11 of this data sheet.

NCS2200 Series

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Supply Voltage Range (V_{CC} to V_{EE})	V_S	6.0	V
Non-inverting/Inverting Input to V_{EE}	–	–0.2 to ($V_{CC} + 0.2$)	V
Thermal Resistance, Junction to Air	$R_{\theta JA}$	238	°C/W
Operating Junction Temperature	T_J	150	°C
Operating Ambient Temperature	T_A	–40 to +105	°C
Storage Temperature Range	T_{stg}	–65 to +150	°C
Output Short Circuit Duration Time (Note 1)	t_S	Indefinite	s
ESD Tolerance (Note 2) NCS2200/2201 Human Body Model Machine Model NCS2202/NCS2203 Human Body Model Machine Model	–	2000 200 1000 200	V

1. The maximum package power dissipation limit must not be exceeded.

$$P_D = \frac{T_{J(max)} - T_A}{R_{\theta JA}}$$

2. ESD data available upon request.

ELECTRICAL CHARACTERISTICS (For all values $V_{CC} = 0.85$ V to 6.0 V, $V_{EE} = 0$ V, $T_A = 25^\circ\text{C}$, unless otherwise noted.) (Note 3)

Characteristics	Symbol	Min	Typ	Max	Unit
Input Hysteresis $T_A = 25^\circ\text{C}$	V_{HYS}	2.0	8.0	20	mV
Input Offset Voltage $V_{CC} = 0.85$ V $T_A = 25^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to 105°C $V_{CC} = 3.0$ V $T_A = 25^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to 105°C $V_{CC} = 6.0$ V $T_A = 25^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to 105°C	V_{IO}	–10 –12 –6.0 –8.0 –5.0 –7.0	0.5 – 0.5 – 0.5 –	+10 +12 +6.0 +8.0 +5.0 +7.0	mV
Common Mode Voltage Range	V_{CM}	–	V_{EE} to V_{CC}	–	V
Output Leakage Current (NCS2202/NCS2203) $V_{CC} = 6.0$ V	I_{LEAK}	–	3.3	–	nA
Output Short-Circuit Sourcing or Sinking	I_{SC}	–	70	–	mA
Common Mode Rejection Ratio $V_{CM} = V_{CC}$	CMRR	53	65	–	dB
Input Bias Current	I_{IB}	–	1.0	–	pA
Power Supply Rejection Ratio $\Delta V_S = 2.575$ V	PSRR	45	55	–	dB
Supply Current $V_{CC} = 0.85$ V $T_A = 25^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to 105°C $V_{CC} = 3.0$ V $T_A = 25^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to 105°C $V_{CC} = 6.0$ V $T_A = 25^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to 105°C	I_{CC}	– – –	10 – 10 – 10 –	15 17 15 17 15 17	μA

3. The limits over the extended temperature range are guaranteed by design only.

NCS2200 Series

ELECTRICAL CHARACTERISTICS (continued) (For all values $V_{CC} = 0.85\text{ V}$ to 6.0 V , $V_{EE} = 0\text{ V}$, $T_A = 25^\circ\text{C}$, unless otherwise noted.) (Note 4)

Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage High (NCS2200/NCS2201) $V_{CC} = 0.85\text{ V}$, $I_{\text{source}} = 0.5\text{ mA}$ $T_A = 25^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to 105°C $V_{CC} = 3.0\text{ V}$, $I_{\text{source}} = 3.0\text{ mA}$ $T_A = 25^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to 105°C $V_{CC} = 6.0\text{ V}$, $I_{\text{source}} = 5.0\text{ mA}$ $T_A = 25^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to 105°C	V_{OH}	$V_{CC} - 0.2$ $V_{CC} - 0.225$ $V_{CC} - 0.2$ $V_{CC} - 0.25$ $V_{CC} - 0.2$ $V_{CC} - 0.25$	$V_{CC} - 0.10$ — $V_{CC} - 0.12$ — $V_{CC} - 0.12$ —	— — —	V
Output Voltage Low $V_{CC} = 0.85\text{ V}$, $I_{\text{sink}} = 0.5\text{ mA}$ $T_A = 25^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to 105°C $V_{CC} = 3.0\text{ V}$, $I_{\text{sink}} = 3.0\text{ mA}$ $T_A = 25^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to 105°C $V_{CC} = 6.0\text{ V}$, $I_{\text{sink}} = 5.0\text{ mA}$ $T_A = 25^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to 105°C	V_{OL}	— — —	$V_{EE} + 0.10$ — $V_{EE} + 0.12$ — $V_{EE} + 0.12$ —	$V_{EE} + 0.2$ $V_{EE} + 0.225$ $V_{EE} + 0.2$ $V_{EE} + 0.25$ $V_{EE} + 0.2$ $V_{EE} + 0.25$	V
Propagation Delay 20 mV Overdrive, $C_L = 15\text{ pF}$	t_{PHL} t_{PLH}	— —	0.7 1.1	— —	μs
Output Fall Time $V_{CC} = 6.0\text{ V}$, $C_L = 50\text{ pF}$	t_{FALL}	—	20	—	ns
Output Rise Time $V_{CC} = 6.0\text{ V}$, $C_L = 50\text{ pF}$	t_{RISE}	—	16	—	ns
Power-up Time	t_{PU}	—	35	—	μs

ENABLE FUNCTION ELECTRICAL CHARACTERISTICS (NCS2201/NCS2203 only)

(For all values $V_{CC} = 6.0\text{ V}$, $V_{EE} = 0\text{ V}$, $T_A = 25^\circ\text{C}$, unless otherwise noted.) (Note 4)

Enable Voltage Threshold Input Voltage Increasing, Device Enabled Input Voltage Decreasing, Device Disabled	$V_{EN(HIGH)}$ $V_{EN(LOW)}$	— 2.0	3.2 2.2	4.0 —	V
Enable Hysteresis	V_{ENHYS}	—	1.0	—	V
Enable Pull-up Current	I_{EN}	—	100	200	nA
Disable State Supply Current	I_{CCD}	—	300	600	nA
Enable Input to Output Propagation Delay Input Voltage Increasing, Device Enabled Input Voltage Decreasing, Device Disabled	$t_{EN(ON)}$ $t_{EN(OFF)}$	— —	82 0.5	— —	μs

4. The limits over the extended temperature range are guaranteed by design only.

NCS2200 Series

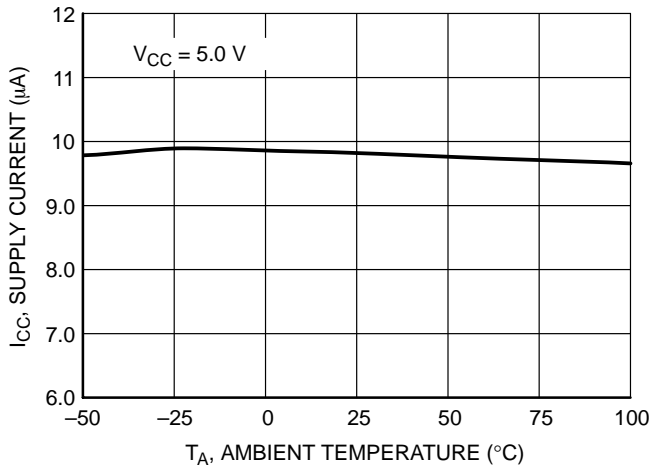


Figure 1. NCS2200 Series Supply Current versus Temperature

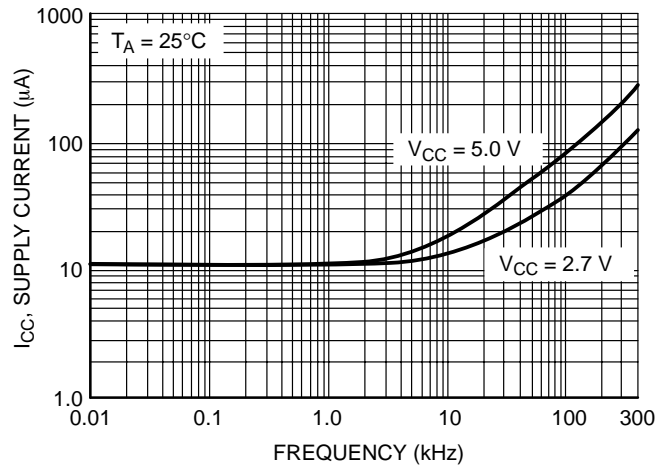


Figure 2. NCS2200 Series Supply Current versus Output Transition Frequency

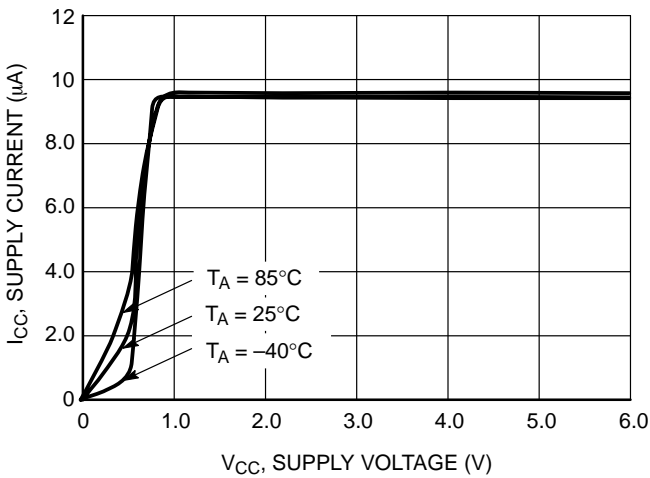


Figure 3. NCS2200 Series Supply Current versus Supply Voltage

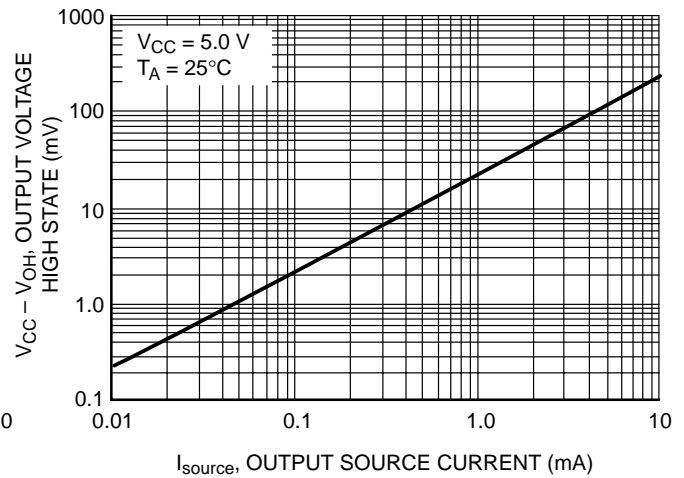


Figure 4. NCS2200/1 Output Voltage High State versus Output Source Current

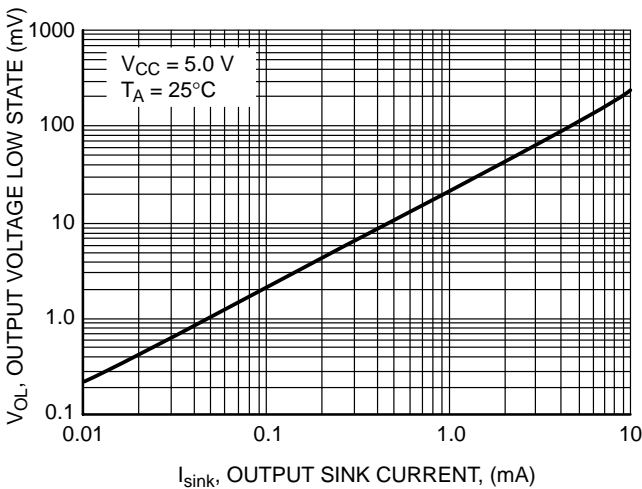


Figure 5. NCS2200 Series Output Voltage Low State versus Output Sink Current

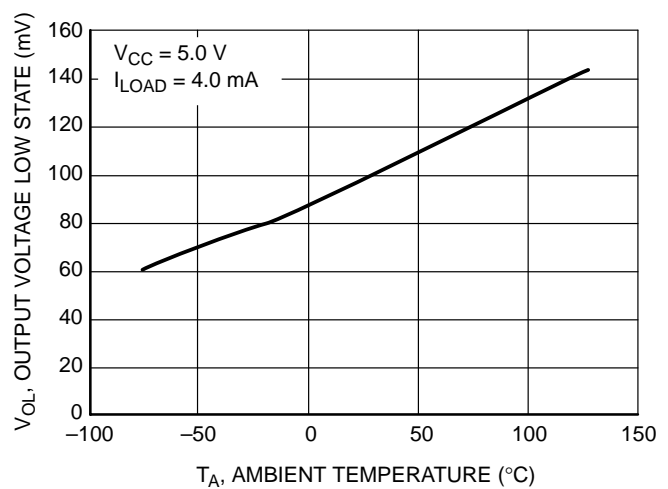


Figure 6. NCS2200 Series Output Voltage Low State versus Temperature

NCS2200 Series

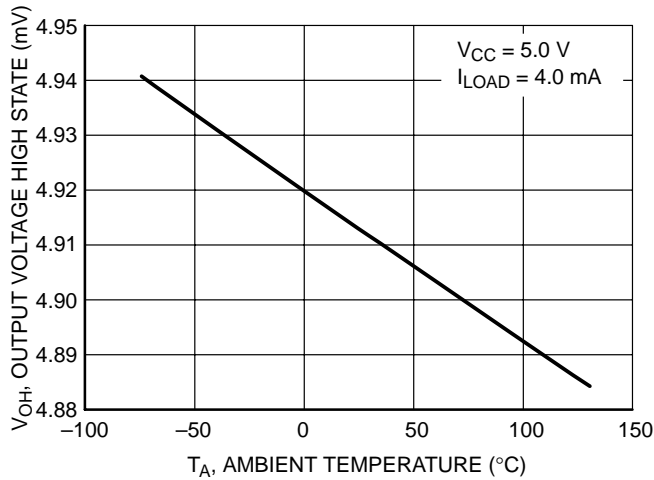


Figure 7. NCS2200/1 Series Output Voltage High State versus Temperature

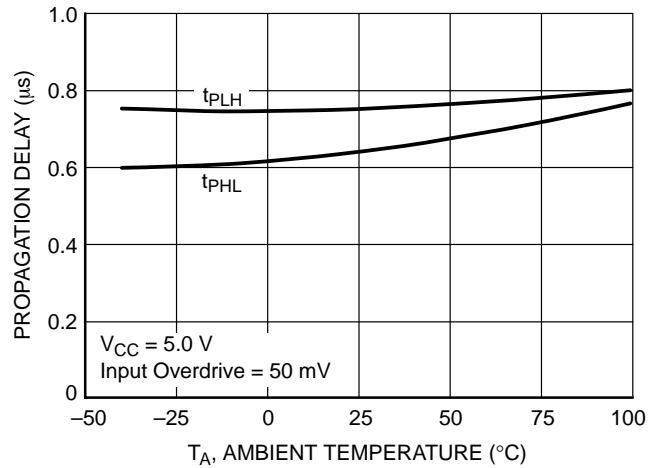


Figure 8. NCS2200 Series Propagation Delay versus Temperature

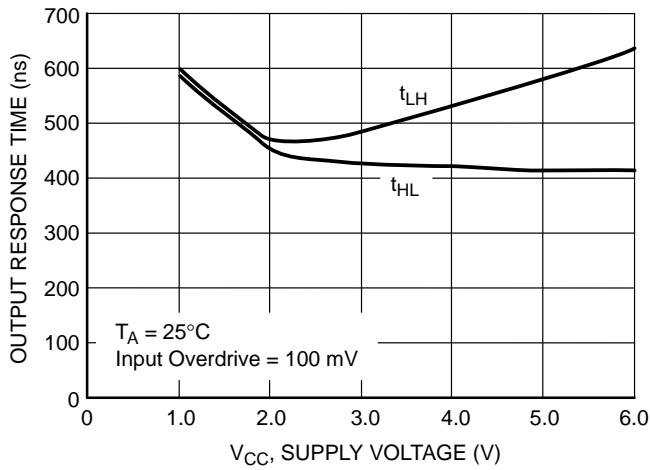


Figure 9. NCS2200 Series Output Response Time versus Supply Voltage

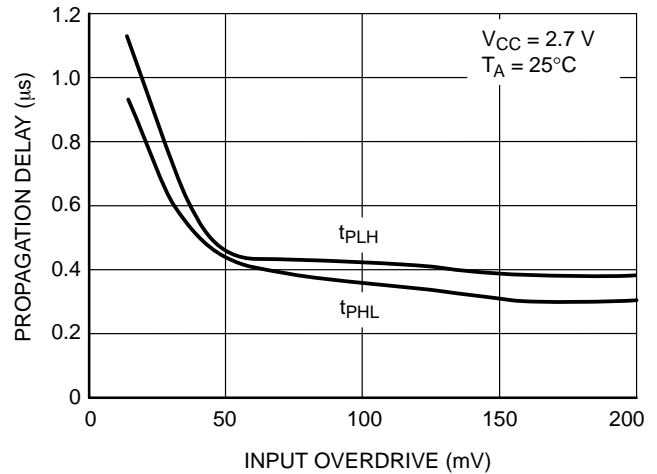


Figure 10. NCS2200 Series Propagation Delay versus Input Overdrive

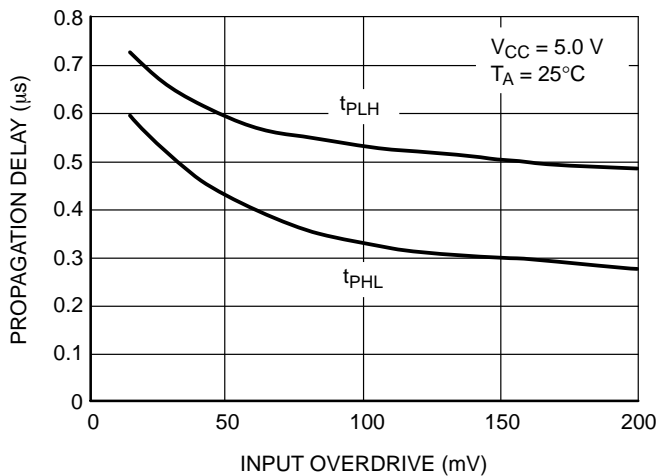


Figure 11. NCS2200 Series Propagation Delay versus Input Overdrive

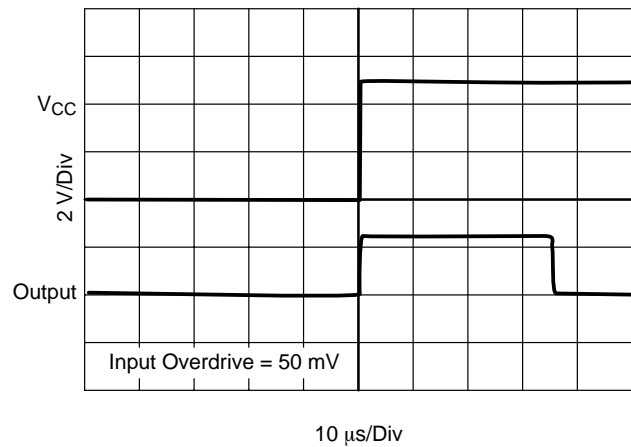


Figure 12. NCS2200 Series Power-Up Delay

NCS2200 Series

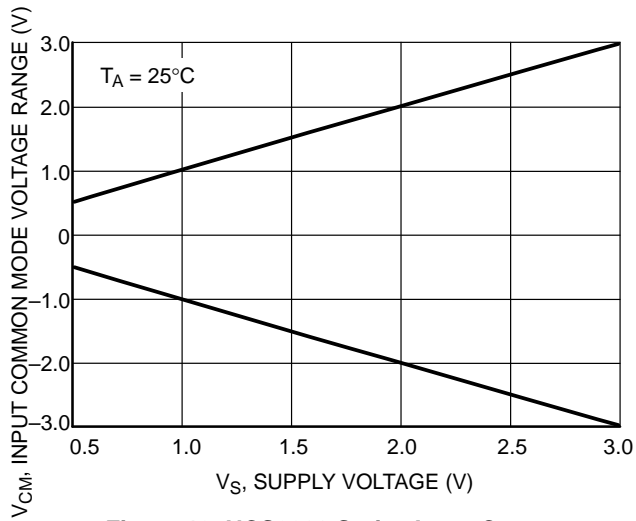


Figure 13. NCS2200 Series Input Common Mode Voltage Range versus Supply Voltage

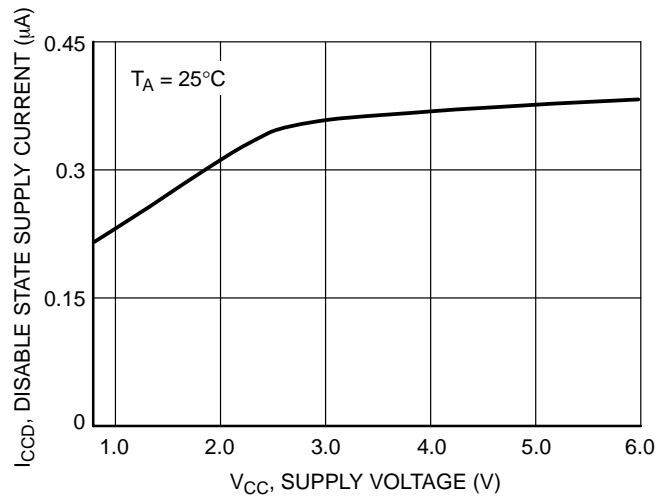


Figure 14. NCS2201/3 Series Disable State Supply Current versus Supply Voltage

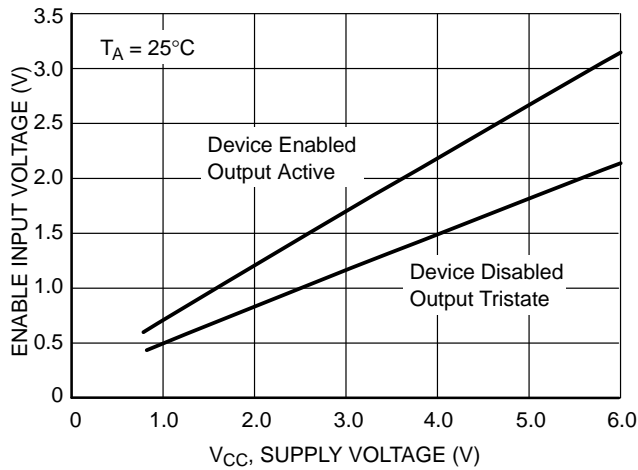


Figure 15. NCS2201/3 Enable Input Voltage versus Supply Voltage

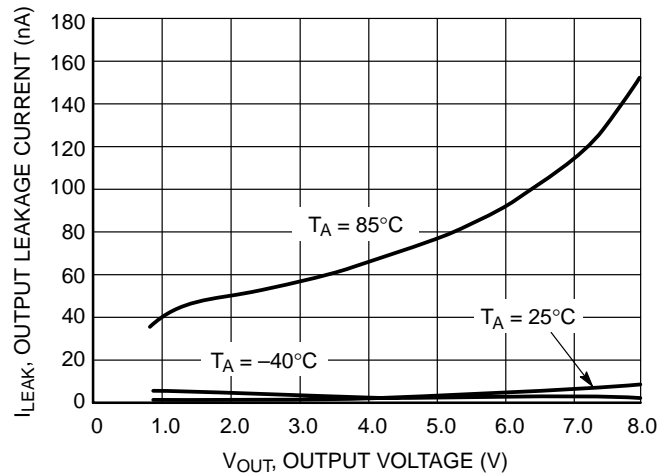


Figure 16. NCS2202/3 Output Leakage Current versus Output Voltage

OPERATING DESCRIPTION

The NCS2200 Series is an industry first sub-one volt, low power comparator family. This series is designed for rail-to-rail input and output performance. These devices consume only 10 μ A of supply current while achieving a typical propagation delay of 1.1 μ s at a 20 mV input overdrive. Figures 10 and 11 show propagation delay with various input overdrives. This comparator family is guaranteed to operate at a low voltage of 0.85 V up to 6.0 V. This is accomplished by the use of a modified analog CMOS process that implements depletion MOSFET devices. The common-mode input voltage range extends 0.1 V beyond the upper and lower rail without phase inversion or other adverse effects. This series is available in the SOT23-5 and SOT23-6 package.

The SOT23-6 features the enable function, which can be externally controlled. This feature allows significantly lower current consumption of 0.3 μ A. This makes the devices suitable for implementation in power sensitive applications such as portable electronics. The enable function is active high when connected to the V_{CC} pin.

When the enable pin is driven low (device disabled), output tri-state mode is activated. The device will remain in this mode and will not respond to any changes at the inputs of the comparator. In order to pull the device out of tri-state mode, the enable upper voltage threshold must be met. Figure 15 shows the enable input voltage required to either enable or disable the device, with a variance in supply voltage. In addition, these devices have a typical internal hysteresis of ± 8.0 mV. This allows for greater noise immunity and clean output switching.

Output Stage

The NCS2200/1 has a complementary P and N channel output stage that has capability of driving a rail-to-rail output swing with a load ranging up to 5.0 mA. It is designed such that shoot-through current is minimized while switching. This feature eliminates the need for bypass capacitors under most circumstances.

The NCS2202/3 has an open drain N-channel output stage that can be pulled up to 6.0 V (max) with an external resistor. This facilitates mixed voltage system applications.

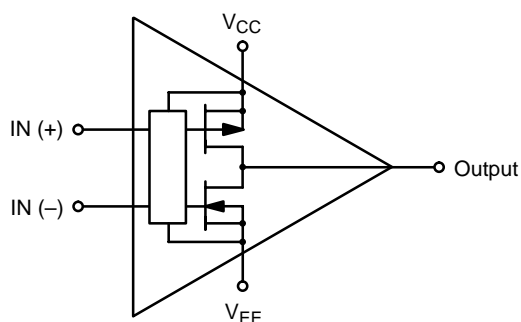


Figure 17. NCS2200/1SNxT1 Complementary Output Configuration

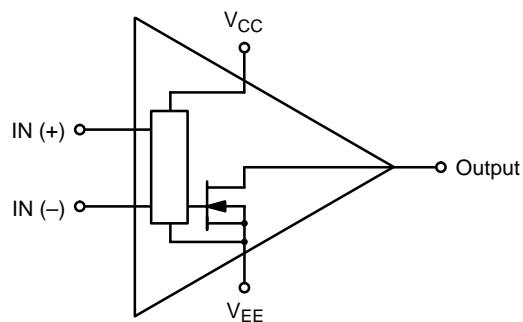
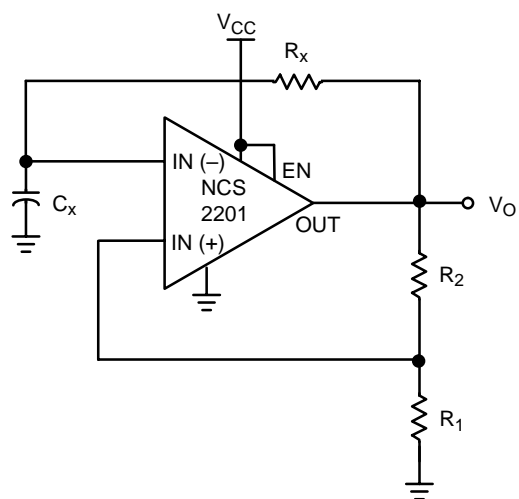


Figure 18. NCS2202/3SNxT1 Open Drain Output Configuration

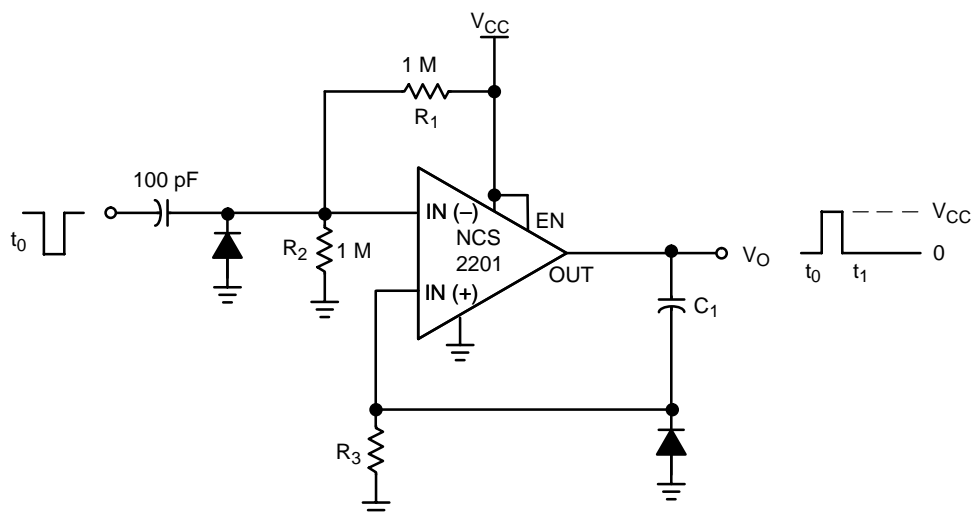
NCS2200 Series



The oscillation frequency can be programmed as follows:

$$f = \frac{1}{T} = \frac{1}{2.2 R_X C_X}$$

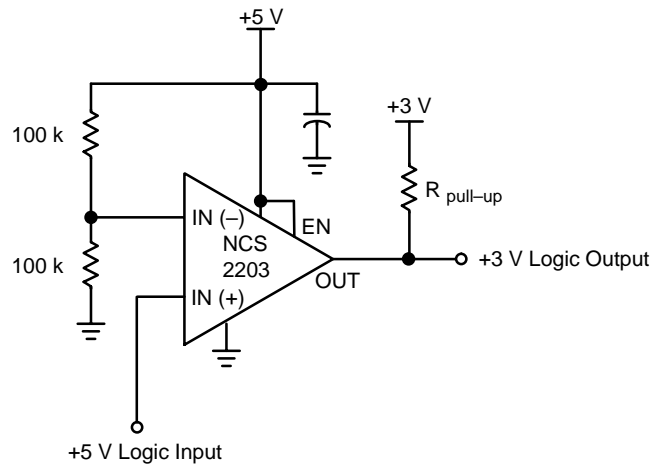
Figure 19. Schmitt Trigger Oscillator



The resistor divider R_1 and R_2 can be used to set the magnitude of the input pulse. The pulse width is set by adjusting C_1 and R_3 .

Figure 20. One-Shot Multivibrator

NCS2200 Series



This circuit converts 5 V logic to 3 V logic. Using the NCS2202/3 allows for full 5 V logic swing without creating overvoltage on the 3 V logic input.

Figure 21. Logic Level Translator

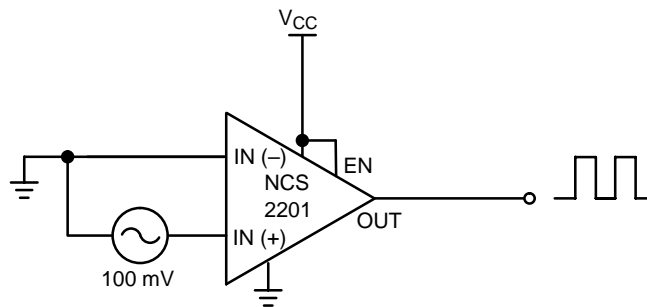


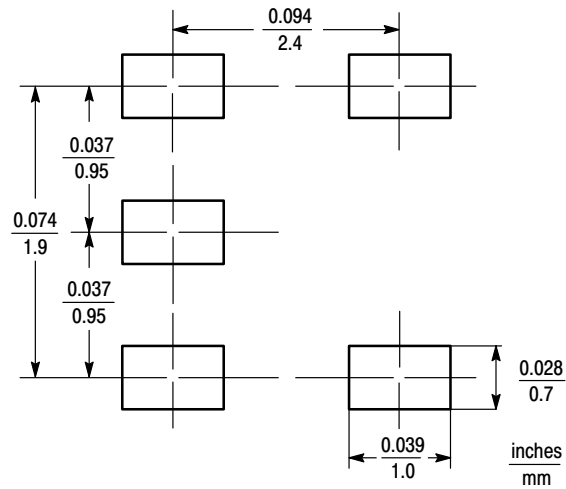
Figure 22. Zero-Crossing Detector

NCS2200 Series

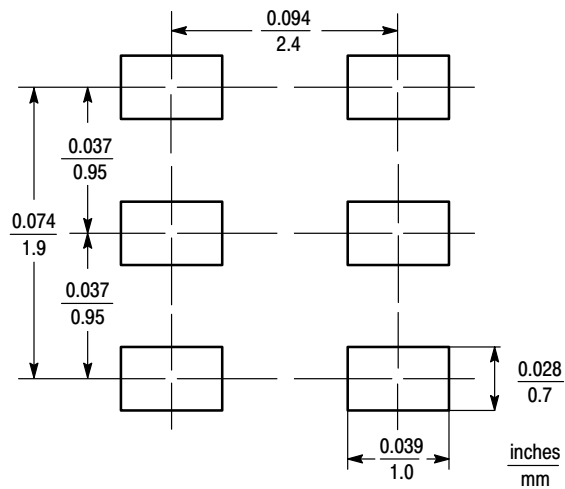
MINIMUM RECOMMENDED FOOTPRINT FOR SURFACE MOUNTED APPLICATIONS

Surface mount board layout is a critical portion of the total design. The footprint for the semiconductor packages must be the correct size to insure proper solder connection

interface between the board and the package. With the correct pad geometry, the packages will self align when subjected to a solder reflow process.



SOT23-5



SOT23-6

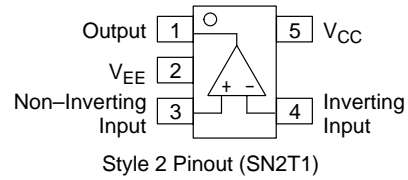
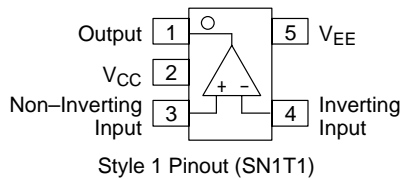
NCS2200 Series

ORDERING INFORMATION

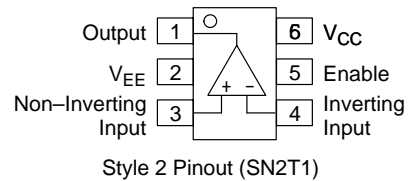
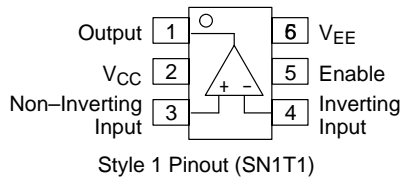
Device	Pinout Style	Output Type	Package	Shipping
NCS2200SN1T1	1	Complementary	SOT23–5	3000 Tape & Reel
NCS2200SN2T1	2	Complementary	SOT23–5	
NCS2201SN1T1	1	Complementary, Enable	SOT23–6	
NCS2201SN2T1	2	Complementary, Enable	SOT23–6	
NCS2202SN1T1	1	Open Drain	SOT23–5	
NCS2202SN2T1	2	Open Drain	SOT23–5	
NCS2203SN1T1	1	Open Drain, Enable	SOT23–6	
NCS2203SN2T1	2	Open Drain, Enable	SOT23–6	

This device contains 93 active transistors.

PIN CONNECTIONS



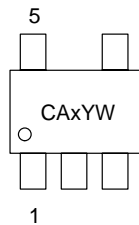
SOT23–5 (NCS2200, NCS2202)



SOT23–6 (NCS2201, NCS2203)

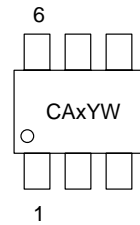
MARKING DIAGRAMS

**SOT23–5
SN SUFFIX
CASE 483**



x = I for NCS2200SN1T1
 J for NCS2200SN2T1
 M for NCS2202SN1T1
 N for NCS2202SN2T1
 Y = Year
 W = Work Week

**SOT23–6
SN SUFFIX
CASE 318G**

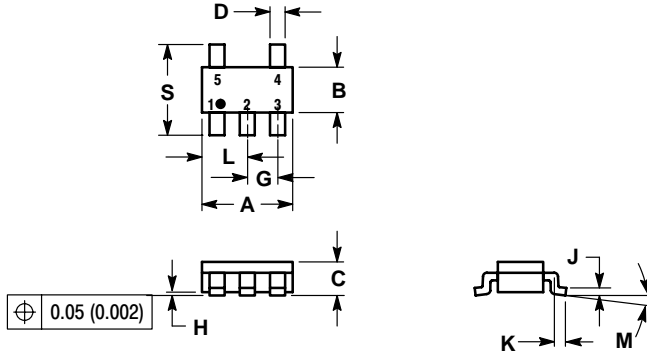


x = K for NCS2201SN1T1
 L for NCS2201SN2T1
 O for NCS2203SN1T1
 P for NCS2203SN2T1
 Y = Year
 W = Work Week

NCS2200 Series

PACKAGE DIMENSIONS

SOT23-5
(TSOP-5, SC59-5)
SN SUFFIX
PLASTIC PACKAGE
CASE 483-01
ISSUE B

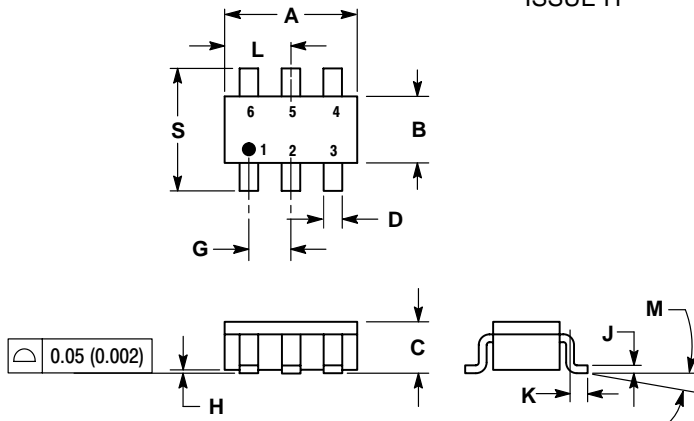


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	2.90	3.10	0.1142	0.1220
B	1.30	1.70	0.0512	0.0669
C	0.90	1.10	0.0354	0.0433
D	0.25	0.50	0.0098	0.0197
G	0.85	1.05	0.0335	0.0413
H	0.013	0.100	0.0005	0.0040
J	0.10	0.26	0.0040	0.0102
K	0.20	0.60	0.0079	0.0236
L	1.25	1.55	0.0493	0.0610
M	0°	10°	0°	10°
S	2.50	3.00	0.0985	0.1181


SOT23-6
(TSOP-6, SC59-6)
SN SUFFIX
PLASTIC PACKAGE
CASE 318G-02
ISSUE H



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	2.90	3.10	0.1142	0.1220
B	1.30	1.70	0.0512	0.0669
C	0.90	1.10	0.0354	0.0433
D	0.25	0.50	0.0098	0.0197
G	0.85	1.05	0.0335	0.0413
H	0.013	0.100	0.0005	0.0040
J	0.10	0.26	0.0040	0.0102
K	0.20	0.60	0.0079	0.0236
L	1.25	1.55	0.0493	0.0610
M	0°	10°	0°	10°
S	2.50	3.00	0.0985	0.1181

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