

RICOH

LOW VOLTAGE DETECTOR WITH OUTPUT DELAY

R3112xxx1A/C SERIES

OUTLINE

The R3112 Series are CMOS-based voltage detector ICs with high detector threshold accuracy and ultra-low supply current, which can be operated at an extremely low voltage and is used for system reset as an example.

Each of these ICs consists of a voltage reference unit, a comparator, resistor net for detector threshold setting, an output driver, a hysteresis circuit, and an output delay circuit. The detector threshold is fixed with high accuracy internally and does not require any adjustment. Two output types, Nch open drain type and CMOS type are available.

Three types of packages, SOT-23-5, small SC-82AB, and ultra-small SON1612-6(Under Development) can be selected so that high density mounting on boards is possible.

FEATURES

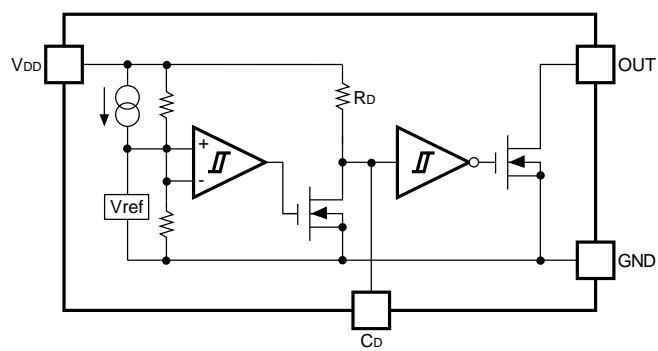
- Built-in Output Delay Circuit ..... Typ. 100ms with an external capacitor:  
0.022μF
- Ultra-low Supply Current..... Typ. 1.0μA (V<sub>DD</sub>=3.5V)
- Wide Range of Operating Voltage..... 0.7~6.0V(T<sub>opt</sub>=25°C)
- Detector Threshold ..... Stepwise setting with a step of 0.1V in  
the range of 0.9V to 5.0V is possible.
- High Accuracy Detector Threshold..... ±2.0%
- Low Temperature-Drift Coefficient of Detector Threshold ..... Typ. ±100ppm/°C
- Two Output Types..... Nch Open Drain and CMOS
- Three Types of Packages..... SOT-23-5 (Mini-mold), SC-82AB,  
SON1612-6(Under Development)

APPLICATIONS

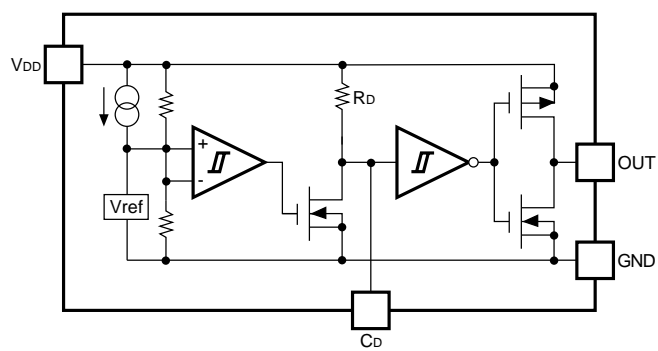
- CPU and Logic Circuit Reset
- Battery Checker
- Window Comparator
- Wave Shaping Circuit
- Battery Back-up Circuit
- Power Failure Detector

## BLOCK DIAGRAMS

R3112xxx1A



R3112xxx1C



## OPERATION

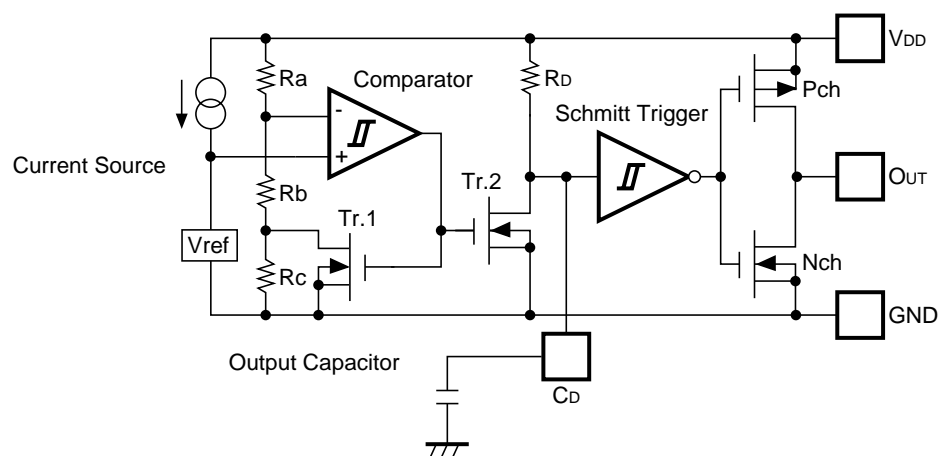


Fig. 1 Block Diagram with an external capacitor

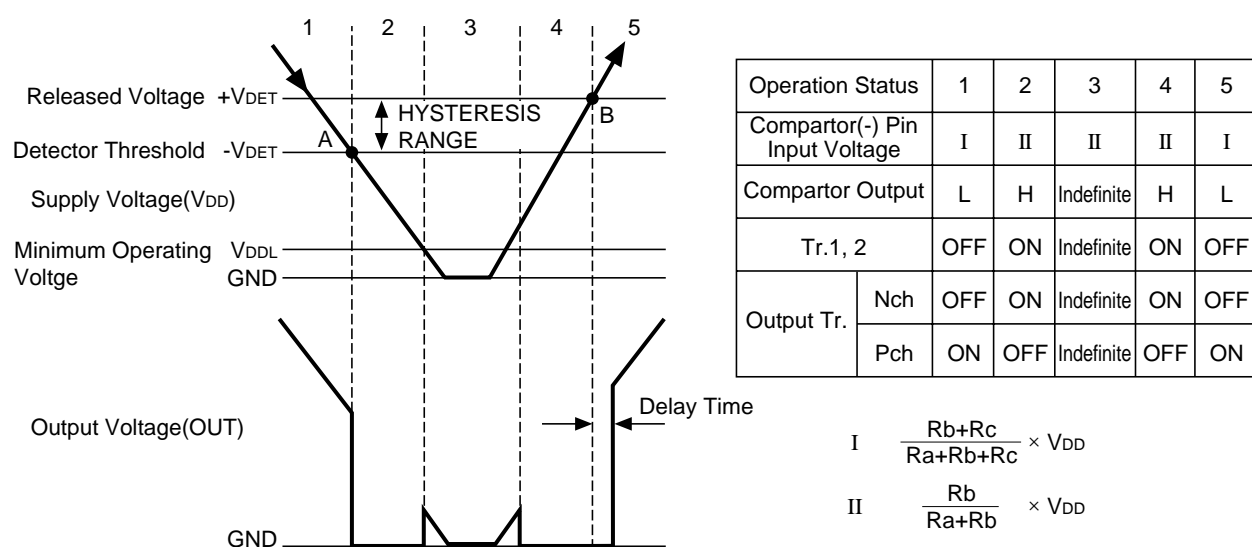


Fig. 2 Operation Diagram

1. Output voltage is equal to supply voltage. (As for Nch open drain type, equal to pull-up voltage.)
2. When the supply voltage is down to the detector threshold voltage level(Point A),  
 $V_{ref} \geq V_{DD} \times (R_b + R_c) / (R_a + R_b + R_c)$  is true, then output of the comparator is reversed from "L" to "H", therefore output voltage becomes GND level.
3. When the supply voltage is lower than minimum operating voltage, the operation of output transistor is indefinite. In the case of Nch open drain type, output voltage is equal to pull-up voltage.
4. Output Voltage becomes GND level.
5. When the supply voltage is higher than released voltage (Point B),  $V_{ref} \leq V_{DD} \times R_b / (R_a + R_b)$  is true, then

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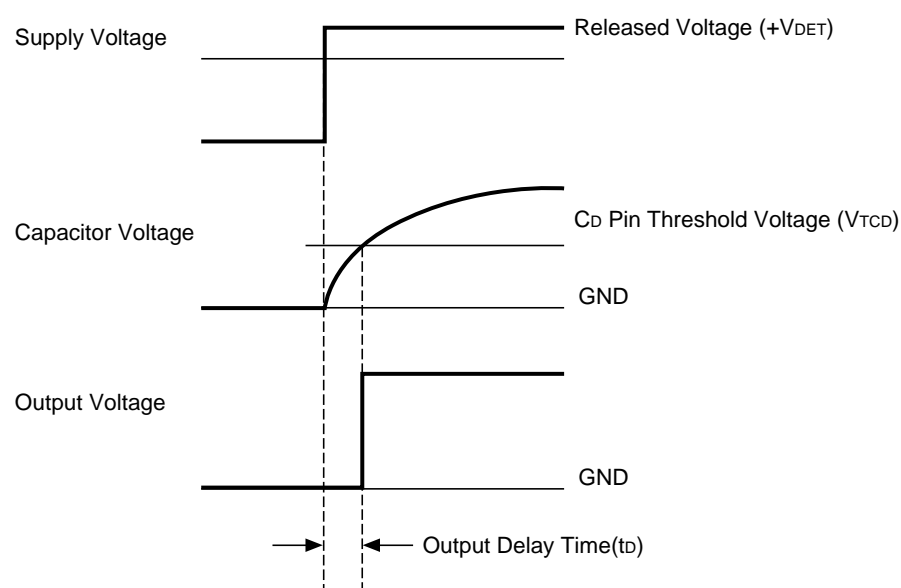
**R3112xxx1A/C**

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output of the comparator reaches the threshold level, and Output of Shmitt Trigger is reversed from “H” to “L”, then output voltage is equal to supply voltage. (As for Nch open drain type, equal to pull-up voltage.)

- \*) The difference between released voltage and detector threshold voltage means hysteresis range voltage.

- Operation of Output Delay



When the supply voltage which is higher than released voltage is forced to  $V_{DD}$  pin, charge to an external capacitor starts, then capacitor voltage increases. Until the capacitor voltage reaches to  $C_D$  Pin threshold voltage, output voltage maintains "L". When the capacitor voltage becomes higher than  $C_D$  pin threshold voltage, output voltage is reversed from "L" to "H". Where, the time interval between the rising edge of supply voltage and output voltage reverse point means output delay time.

- Output Delay Time

Output Delay Time ( $t_D$ ) can be calculated with the next formula.

$$t_D = 0.69 \times R_D \times C_D (\text{s})$$

$R_D$  is internal resistor and set at  $6.5\text{M}\Omega$ (Typ.) typically.  $C_D$ (F) describes the capacitance value of an external capacitor. Therefore,

$$t_D = 0.69 \times 6.5 \times 10^6 \times C_D (\text{s})$$

SELECTION GUIDE

The package type, the detector threshold, the output type, and the taping type of R3112 Series can be designated at the users' request by specifying the part number as follows;

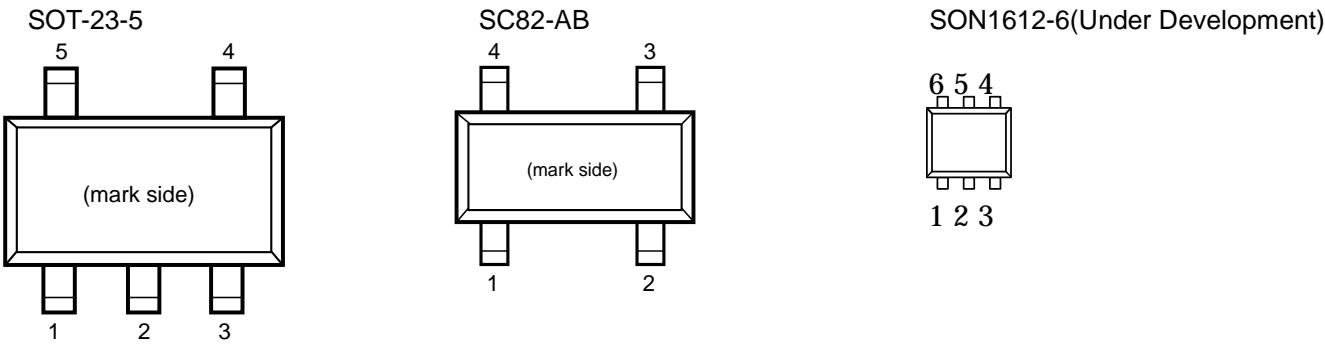
R3112xxx1x-xx ← Part Number

↑↑ ↑↑

ab c d

Code	Contents
a	Designation of Package Type; Q:SC-82AB            N:SOT-23-5            D:SON1612-6(Under Development)
b	Setting Detector Threshold (-V <sub>DET</sub> ); Stepwise setting with a step of 0.1V in the range of 0.9V to 5.0V is possible.
c	Designation of Output Type; A: Nch Open Drain C: CMOS
d	Designation of Packing or Taping Type ; Ex. SOT-23-5, SC-82AB, SON1612-6(Under Development): TR is prescribed as standard directions. (Refer to Taping Specifications)

PIN CONFIGURATION



## PIN DESCRIPTION

### SOT-23-5

Pin No.	Symbol	Description
1	OUT	Output Pin(Output “L” at detector threshold, Output “H” at released voltage)
2	V <sub>DD</sub>	Voltage Supply Pin
3	GND	Ground Pin
4	NC	No Connection
5	C <sub>D</sub>	Pin for External Capacitor (for setting output delay)

### SC-82AB

Pin No.	Symbol	Description
1	V <sub>DD</sub>	Voltage Supply Pin
2	GND	Ground Pin
3	C <sub>D</sub>	Pin for External Capacitor (for setting output delay)
4	OUT	Output Pin(Output “L” at detector threshold, Output “H” at released voltage)

### SON1612-6(Under Development)

Pin No.	Symbol	Description
1	OUT	Output Pin(Output “L” at detector threshold, Output “H” at released voltage)
2	GND	Ground Pin
3	C <sub>D</sub>	Pin for External Capacitor (for setting output delay)
4	NC	No Connection
5	GND	Ground Pin
6	V <sub>DD</sub>	Voltage Supply Pin

ABSOLUTE MAXIMUM RATINGS

Symbol	Item	Rating	Unit
V <sub>DD</sub>	Supply Voltage	6.5	V
V <sub>OUT1</sub>	Output Voltage (CMOS)	V <sub>SS</sub> -0.3~V <sub>DD</sub> +0.3	V
V <sub>OUT2</sub>	Output Voltage (Nch)	V <sub>SS</sub> -0.3~6.5	V
I <sub>OUT</sub>	Output Current	20	mA
P <sub>D</sub>	Power Dissipation (SOT23-5)	250	mW
	Power Dissipation (SC82-AB)	150	
	Power Dissipation (SON1612-6)-(Under Development)*Note1	500	
T <sub>opt</sub>	Operating Temperature Range	-40~85	°C
T <sub>stg</sub>	Storage Temperature Range	-55~125	°C
T <sub>solder</sub>	Lead temperature (Soldering)	260°C, 10s	

\*Note 1: This specification is at mounted on board.

PD depends on conditions of mounting on board. This specification is based on the measurement at the condition below:

\*Measurement Conditions

Environment: Mounted on board (Wind velocity 0m/s)

Board Material: FR-4 (2-layer)

Board dimensions : 40mm x 40mm x t1.6mm

Copper Area : 50%

ABSOLUTE MAXIMUM RATINGS
Absolute Maximum ratings are threshold limit values that must not be exceeded ever for an instant under any conditions. Moreover, such values for any two items must not be reached simultaneously. Operation above these absolute maximum ratings may cause degradation or permanent damage to the device. These are stress ratings only and do not necessarily imply functional operation below these limits.



## • R3112x091A/C

T<sub>opt</sub>=25°C

Symbol	Item	Conditions	Min.	Typ.	Max.	Unit
-V <sub>DET</sub>	Detector Threshold		0.882	0.900	0.918	V
V <sub>HYS</sub>	Detector Threshold Hysteresis		0.027	0.045	0.063	V
I <sub>SS</sub>	Supply Current	V <sub>DD</sub> =0.80V		0.6	2.0	μA
		V <sub>DD</sub> =1.90V		0.5	2.0	
V <sub>DDH</sub>	Maximum Operating Voltage				6.0	V
V <sub>DDL</sub>	Minimum Operating Voltage*Note1	T <sub>opt</sub> =25°C			0.70	V
		-40°C ≤ T <sub>opt</sub> ≤ 85°C			0.80	
I <sub>OUT</sub>	Output Current (Driver Output Pin)	Nch V <sub>DS</sub> =0.05V, V <sub>DD</sub> =0.70V	10	120		μA
		V <sub>DS</sub> =0.50V, V <sub>DD</sub> =0.85V	0.05	0.90		mA
		Pch V <sub>DS</sub> =-2.1V, V <sub>DD</sub> =4.5V	1.0	3.5		mA
V <sub>TCD</sub>	CD pin Threshold Voltage	V <sub>DD</sub> =0.99V	0.297	0.495	0.693	V
I <sub>CD</sub>	CD pin Output Current	V <sub>DS</sub> =0.10V, V <sub>DD</sub> =0.70V	2	70		μA
		V <sub>DS</sub> =0.50V, V <sub>DD</sub> =0.85V	10	400		
R <sub>D</sub>	Output Delay Resistance		3.25	6.50	13.00	MΩ
Δ-V <sub>DET</sub> / ΔT	Detector Threshold Temperature Coefficient	-40°C ≤ T <sub>opt</sub> ≤ 85°C		±100		ppm/ °C

## • R3112x271A/C

T<sub>opt</sub>=25°C

Symbol	Item	Conditions	Min.	Typ.	Max.	Unit
-V <sub>DET</sub>	Detector Threshold		2.646	2.700	2.754	V
V <sub>HYS</sub>	Detector Threshold Hysteresis		0.081		0.189	V
I <sub>SS</sub>	Supply Current	V <sub>DD</sub> =2.60V		1.0	3.0	μA
		V <sub>DD</sub> =3.70V		0.5	2.5	
V <sub>DDH</sub>	Maximum Operating Voltage				6.0	V
V <sub>DDL</sub>	Minimum Operating Voltage*Note1	T <sub>opt</sub> =25°C			0.7	V
		-40°C ≤ T <sub>opt</sub> ≤ 85°C			0.8	
I <sub>OUT</sub>	Output Current (Driver Output Pin)	Nch V <sub>DS</sub> =0.05V, V <sub>DD</sub> =0.70V	10	120		μA
		V <sub>DS</sub> =0.50V, V <sub>DD</sub> =1.50V	1.0	3.0		mA
		Pch V <sub>DS</sub> =-2.1V, V <sub>DD</sub> =4.5V	1.0	3.5		mA
V <sub>TCD</sub>	CD pin Threshold Voltage	V <sub>DD</sub> =2.97V	0.891	1.485	2.079	V
I <sub>CD</sub>	CD pin Output Current	V <sub>DS</sub> =0.1V, V <sub>DD</sub> =0.7V	2	70		μA
		V <sub>DS</sub> =0.5V, V <sub>DD</sub> =1.5V	200	500		
R <sub>D</sub>	Output Delay Resistance		3.25	6.50	13.00	MΩ
Δ-V <sub>DET</sub> / ΔT	Detector Threshold Temperature Coefficient	-40°C ≤ T <sub>opt</sub> ≤ 85°C		±100		ppm/ °C

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**R3112xxx1A/C**


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**• R3112x501A/C**
 $T_{opt}=25^{\circ}\text{C}$ 

Symbol	Item	Conditions	Min.	Typ.	Max.	Unit
$-V_{DET}$	Detector Threshold		4.900	5.000	5.100	V
$V_{HYS}$	Detector Threshold Hysteresis		0.150	0.250	0.350	V
$I_{SS}$	Supply Current	$V_{DD}=4.9\text{V}$		1.5	3.0	$\mu\text{A}$
		$V_{DD}=6.0\text{V}$		0.6	2.5	
$V_{DDH}$	Maximum Operating Voltage				6.0	V
$V_{DDL}$	Minimum Operating Voltage*Note1	$T_{opt}=25^{\circ}\text{C}$			0.7	V
		$-40^{\circ}\text{C} \leq T_{opt} \leq 85^{\circ}\text{C}$			0.8	
$I_{OUT}$	Output Current (Driver Output Pin)	Nch $V_{DS}=0.05\text{V}, V_{DD}=0.70\text{V}$	10	120		$\mu\text{A}$
		$V_{DS}=0.50\text{V}, V_{DD}=1.50\text{V}$	1.0	3.0		mA
		Pch $V_{DS}=-2.1\text{V}, V_{DD}=6.0\text{V}$	1.5	4.5		mA
$V_{TCD}$	CD pin Threshold Voltage	$V_{DD}=5.50\text{V}$	1.650	2.750	3.850	V
$I_{CD}$	CD pin Output Current	$V_{DS}=0.1\text{V}, V_{DD}=0.7\text{V}$	2	70		$\mu\text{A}$
		$V_{DS}=0.5\text{V}, V_{DD}=1.5\text{V}$	200	500		
$R_D$	Output Delay Resistance		3.25	6.50	13.00	$\text{M}\Omega$
$\Delta V_{DET}/\Delta T$	Detector Threshold Temperature Coefficient	$-40^{\circ}\text{C} \leq T_{opt} \leq 85^{\circ}\text{C}$		$\pm 100$		ppm/ $^{\circ}\text{C}$

\*Note1: Minimum Operating Voltage means the value of input voltage when output voltage maintains 0.1V or less. (In the case of Nch Open Drain Type, Output pin is pulled up with a resistance of 470k $\Omega$  to 5.0V.)

## ELECTRICAL CHARACTERISTICS BY DETECTOR THRESHOLD

Product Code	Detector Threshold			Hysteresis Range			Supply Current 1			Supply Current 2			Output Current 1			Output Current 2								
	-V <sub>DET</sub> [V]			V <sub>HYS</sub> [V]			I <sub>SS1</sub> [μA]			I <sub>SS2</sub> [μA]			I <sub>OUT1</sub> [mA]			I <sub>OUT2</sub> [mA]								
	Min.	Typ.	Max.	Min.	Typ.	Max.	Condi- tions	Typ.	Max.	Condi- tions	Typ.	Max.	Condi- tions	Min.	Typ.	Conditions	Min.	Typ						
R3112x091A /C	0.88 2	0.90 0	0.91 8	0.02 7	0.04 5	0.06 3	V <sub>DD</sub> = -V <sub>DET</sub> -0.1V	0.6	2.0	V <sub>DD</sub> = -V <sub>DET</sub> +0.1 V	0.5	2.0	V <sub>DS</sub> = 0.05 V V <sub>DD</sub> = 0.7V	0.01	0.12	Nch	0.05	0.9						
R3112x101A /C	0.98 0	1.00 0	1.02 0	0.03 0	0.05 0	0.07 0																		
R3112x111A /C	1.07 8	1.10 0	1.12 2	0.03 3	0.05 5	0.07 7																		
R3112x121A /C	1.17 6	1.20 0	1.22 4	0.03 6	0.06 0	0.08 4																		
R3112x131A /C	1.27 4	1.30 0	1.32 6	0.03 9	0.06 5	0.09 1																		
R3112x141A /C	1.37 2	1.40 0	1.42 8	0.04 2	0.07 0	0.09 8																		
R3112x151A /C	1.47 0	1.50 0	1.53 0	0.04 5	0.07 5	0.10 5																		
R3112x161A /C	1.56 8	1.60 0	1.63 2	0.04 8	0.08 0	0.11 2																		
R3112x171A /C	1.66 6	1.70 0	1.73 4	0.05 1	0.08 5	0.11 9																		
R3112x181A /C	1.76 4	1.80 0	1.83 6	0.05 4	0.09 0	0.12 6																		
R3112x191A /C	1.86 2	1.90 0	1.93 8	0.05 7	0.09 5	0.13 3	1.0	3.0	0.5	2.5					V <sub>DS</sub> = 0.5V V <sub>DD</sub> = 1.5V	1.0	3.0							
R3112x201A /C	1.96 0	2.00 0	2.04 0	0.06 0	0.10 0	0.14 0																		
R3112x211A /C	2.05 8	2.10 0	2.14 2	0.06 3	0.10 5	0.14 7																		
R3112x221A /C	2.15 6	2.20 0	2.24 4	0.06 6	0.11 0	0.15 4																		
R3112x231A /C	2.25 4	2.30 0	2.34 6	0.06 9	0.11 5	0.16 1																		
R3112x241A /C	2.35 2	2.40 0	2.44 8	0.07 2	0.12 0	0.16 8																		
R3112x251A /C	2.45 0	2.50 0	2.55 0	0.07 5	0.12 5	0.17 5																		
R3112x261A /C	2.54 8	2.60 0	2.65 2	0.07 8	0.13 0	0.18 2																		
R3112x271A /C	2.64 6	2.70 0	2.75 4	0.08 1	0.13 5	0.18 9																		
R3112x281A /C	2.74 4	2.80 0	2.85 6	0.08 4	0.14 0	0.19 6																		
R3112x291A /C	2.84 2	2.90 0	2.95 8	0.08 7	0.14 5	0.20 3	2.94 0	3.00 0	3.06 0	0.09 0	0.15 0							0.21 0						
R3112x301A /C	2.94 0	3.00 0	3.06 0	0.09 0	0.15 0	0.21 0																		

R3112xxx1A/C

R3112x311A	3.03	3.10	3.16	0.09	0.15	0.21													
/C	8	0	2	3	5	7													
R3112x321A	3.13	3.20	3.26	0.09	0.16	0.22													
/C	6	0	4	6	0	4													
R3112x331A	3.23	3.30	3.36	0.09	0.16	0.23													
/C	4	0	6	9	5	1													
R3112x341A	3.33	3.40	3.46	0.10	0.17	0.23													
/C	2	0	8	2	0	8													
R3112x351A	3.43	3.50	3.57	0.10	0.17	0.24													
/C	0	0	0	5	5	5													
R3112x361A	3.52	3.60	3.67	0.10	0.18	0.25		1.2	3.0		0.6	2.5							
/C	8	0	2	8	0	2													
R3112x371A	3.62	3.70	3.77	0.11	0.18	0.25													
/C	6	0	4	1	5	9													
R3112x381A	3.72	3.80	3.87	0.11	0.19	0.26													
/C	4	0	6	4	0	6													
R3112x391A	3.82	3.90	3.97	0.11	0.19	0.27													
/C	2	0	8	7	5	3													
R3112x401A	3.92	4.00	4.08	0.12	0.20	0.28													
/C	0	0	0	0	0	0													
R3112x411A	4.01	4.10	4.18	0.12	0.20	0.28													
/C	8	0	2	3	5	7													
R3112x421A	4.11	4.20	4.28	0.12	0.21	0.29													
/C	6	0	4	6	0	4													
R3112x431A	4.21	4.30	4.38	0.12	0.21	0.30													
/C	4	0	6	9	5	1													
R3112x441A	4.31	4.40	4.48	0.13	0.22	0.30													
/C	2	0	8	2	0	8													
R3112x451A	4.41	4.50	4.59	0.13	0.22	0.31													
/C	0	0	0	5	5	5													
R3112x461A	4.50	4.60	4.69	0.13	0.23	0.32		1.5	3.0		0.6	2.5							
/C	8	0	2	8	0	2													
R3112x471A	4.60	4.70	4.79	0.14	0.23	0.32													
/C	6	0	4	1	5	9													
R3112x481A	4.70	4.80	4.89	0.14	0.24	0.33													
/C	4	0	6	4	0	6													
R3112x491A	4.80	4.90	4.99	0.14	0.24	0.34													
/C	2	0	8	7	5	3													
R3112x501A	4.90	5.00	5.10	0.15	0.25	0.35													
/C	0	0	0	0	0	0													

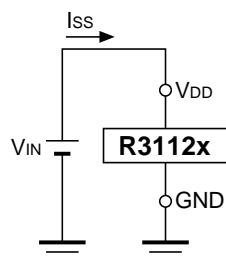
Output Current 3				Minimum Operating Voltage				CD pin Threshold Voltage				CD pin Output Current 1			CD pin Output Current 2			Resistance for Output Delay			Detector Threshold Temperature	
I <sub>OUT3</sub> [mA]				V <sub>DDL</sub> [V]				V <sub>TCD</sub> [V]				I <sub>CD1</sub> [μA]			I <sub>CD2</sub> [μA]			R <sub>D</sub> [MΩ]			ΔV <sub>DET</sub> /ΔT <sub>opt</sub> [ppm/°C]	
Condition	Min.	Typ.		Condition	Max.	Condition	Max.	Condition	Min.	Typ.	Max.	Condition	Min.	Typ.	Condition	Min.	Typ.	Min.	Typ.	Max.	Condition	Typ.
Pch	V <sub>DS</sub> = -21V V <sub>DD</sub> = 4.5V	1.5	3.5	T <sub>opt</sub> = 25°C	0.7	-40°C ≤T <sub>opt</sub> ≤85°C	0.8	V <sub>DD</sub> = (V <sub>DET</sub> ) ×1.1V	0.29	0.49	0.69	V <sub>DS</sub> = 0.1V V <sub>DD</sub> = 0.7V	20	70	V <sub>DS</sub> = 0.5V V <sub>DD</sub> = 0.85V	0.01	400	3.25	6.5	13.0	-40°C ≤T <sub>opt</sub> ≤85°C	±100
									7	5	3											
									0.33	0.55	0.77											
									0	0	0											
									0.36	0.60	0.84											
									3	5	7											
									0.39	0.66	0.92											
									6	0	4											
									0.42	0.71	1.00											
									9	5	1											
									0.46	0.77	1.07											
									2	0	8											
									0.49	0.82	1.15											
									5	5	5											
									0.52	0.88	1.23											
									8	0	2											
									0.56	0.93	1.30											
									1	5	9											
									0.59	0.99	1.38											
									4	0	6											
									0.62	1.04	1.46											
									7	5	3											
									0.66	1.10	1.54											
									0	0	0											
									0.69	1.15	1.61											
									3	5	7											
									0.72	1.21	1.69											
									6	0	4											
									0.75	1.26	1.77											
									9	5	1											
									0.79	1.32	1.84											
									2	0	8											
									0.82	1.37	1.92											
									5	5	5											
									0.85	1.43	2.00											
									8	0	2											
									0.89	1.48	2.07											
									1	5	9											
									0.92	1.54	2.15											
									4	0	6											
									0.95	1.59	2.23											
									7	5	3											
									0.99	1.65	2.31											
									0	0	0											

R3112xxx1A/C

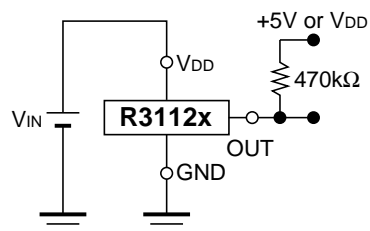
									1.02 3	1.70 5	2.38 7									
									1.05 6	1.76 0	2.46 4									
									1.08 9	1.81 5	2.54 1									
									1.12 2	1.87 0	2.61 8									
									1.15 5	1.92 5	2.69 5									
									1.18 8	1.98 0	2.77 2									
									1.22 1	2.03 5	2.84 9									
									1.25 4	2.09 0	2.92 6									
									1.28 7	2.14 5	3.00 3									
									1.32 0	2.20 0	3.08 0									
									1.35 3	2.25 5	3.15 7									
									1.38 6	2.31 0	3.23 4									
									1.41 9	2.36 5	3.31 1									
									1.45 2	2.42 0	3.38 8									
									1.48 5	2.47 5	3.46 5									
									1.51 8	2.53 0	3.54 2									
									1.55 1	2.58 5	3.61 9									
									1.58 4	2.64 0	3.69 6									
									1.61 7	2.69 5	3.77 3									
									1.65 0	2.75 0	3.85 0									

## TEST CIRCUITS

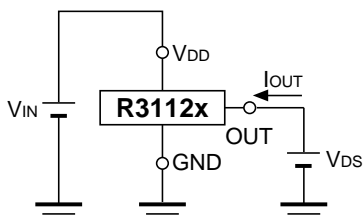
\*Pull-up circuit is not necessary for CMOS Output type, or R3112xxxxC.



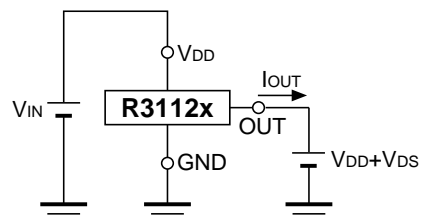
Supply Current Test Circuit



Detector Threshold Test Circuit

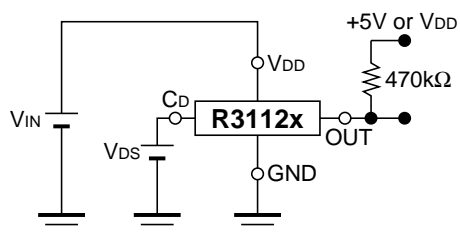


Nch Driver Output Current Test Circuit

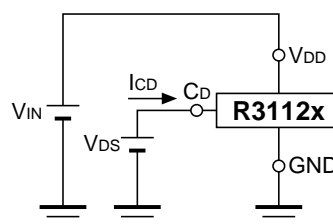


Pch Driver Output Current Test Circuit

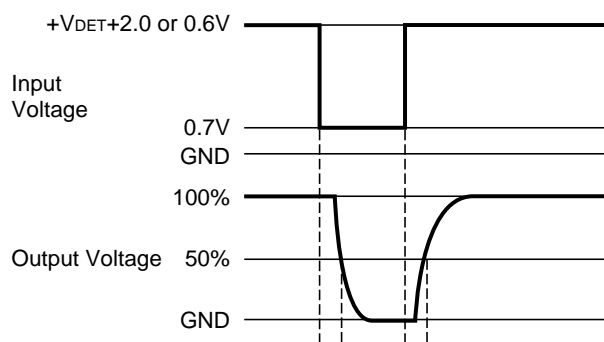
\*Apply only to CMOS



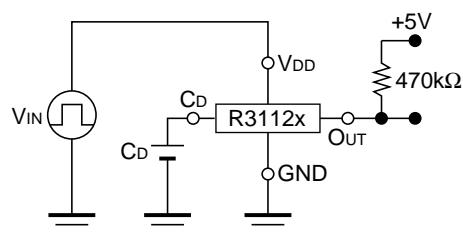
CD Pin Threshold Test Circuit



CD Pin Output Current Test Circuit



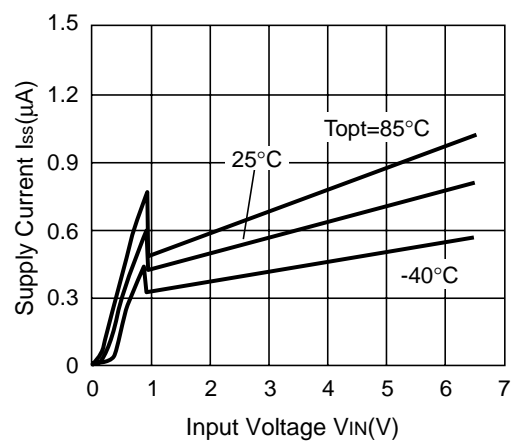
Output Delay Time Test Circuit



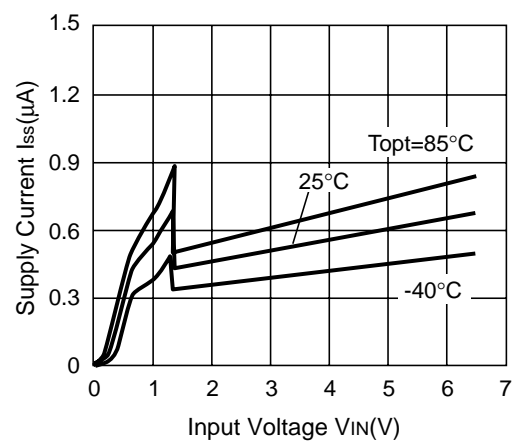
## TYPICAL CHARACTERISTICS

### 1) Supply Current vs. Input Voltage

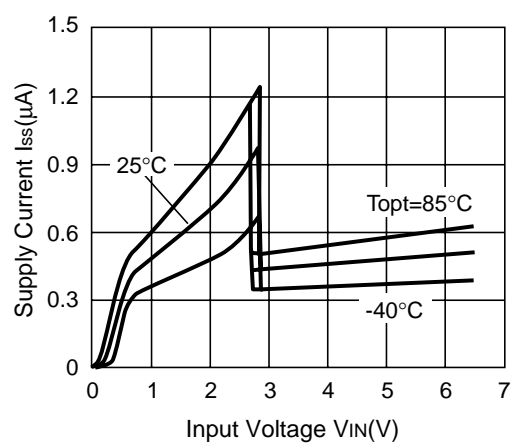
R3112x091x



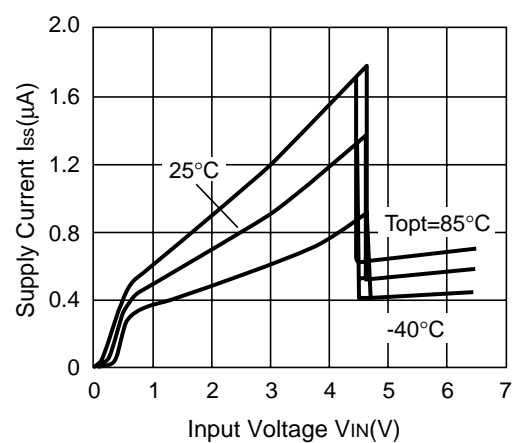
R3112x131x



R3112x271x



R3112x451x

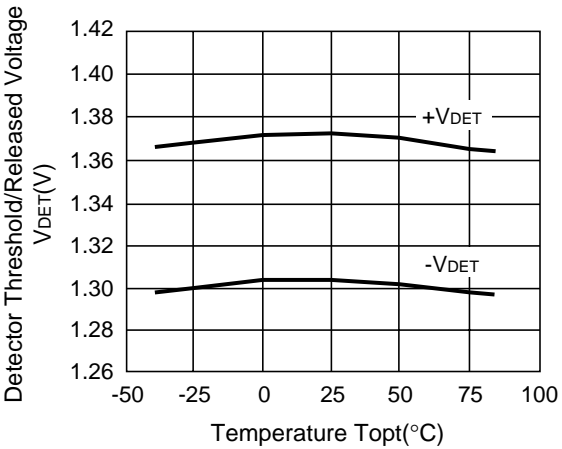
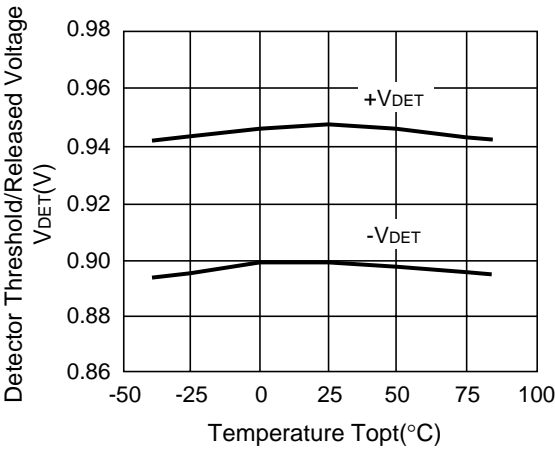


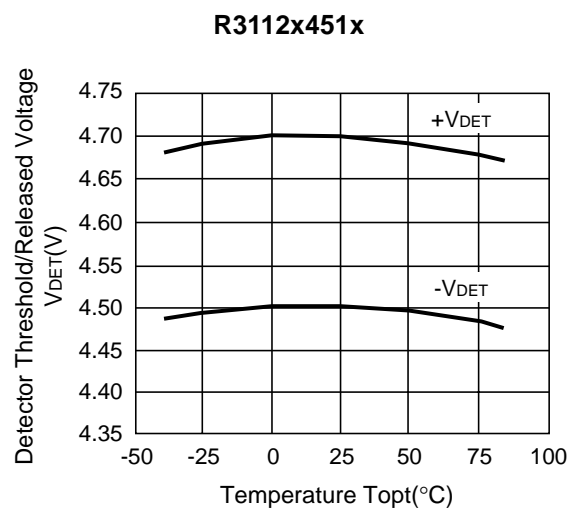
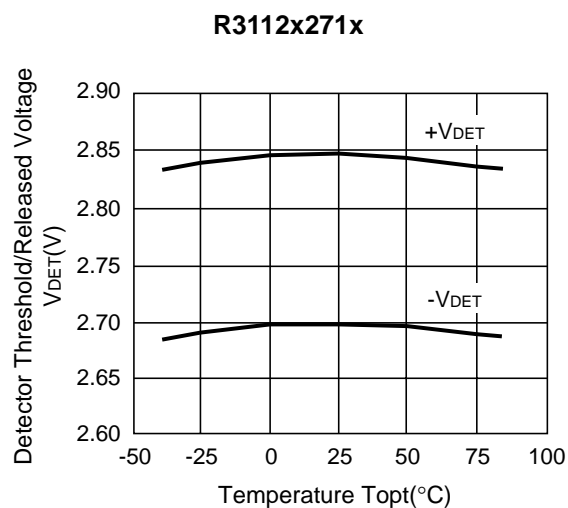
### 2) Detector Threshold vs. Temperature

R3112x091x

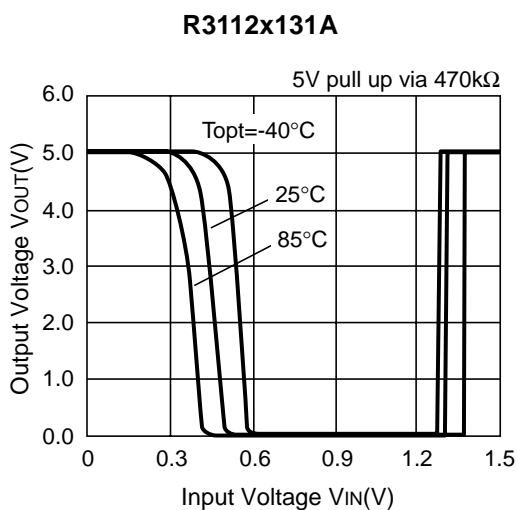
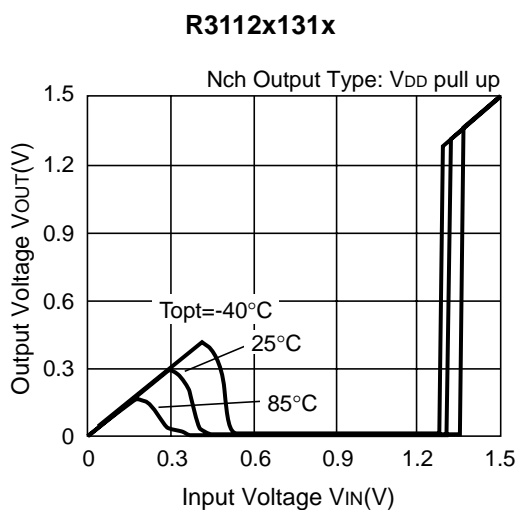
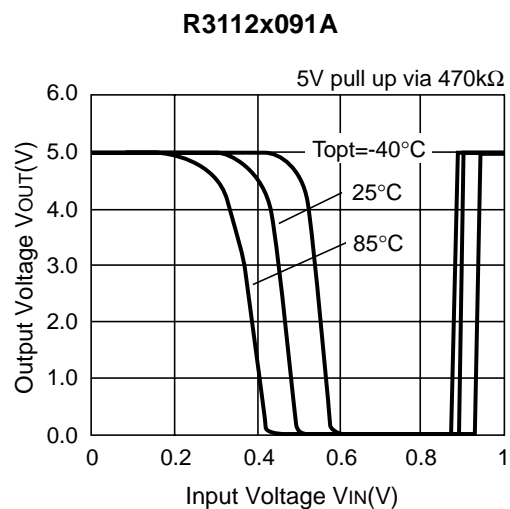
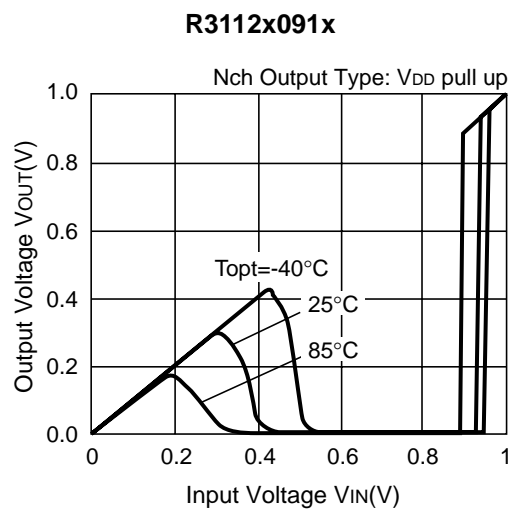
R3112x131x



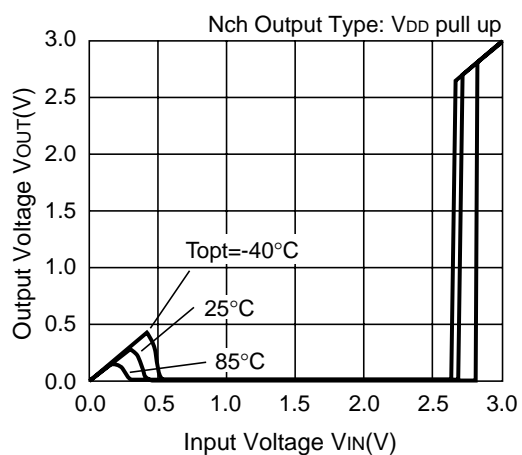




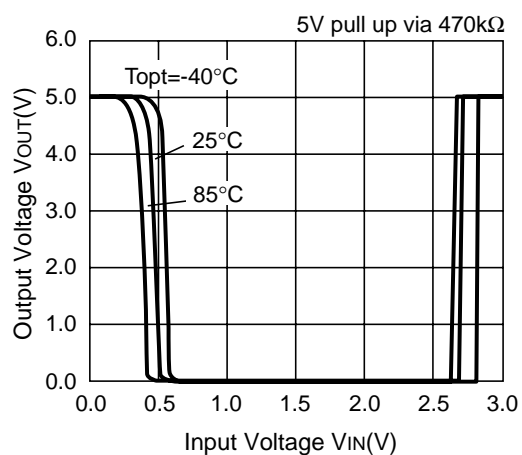
3) Output Voltage vs. Input Voltage



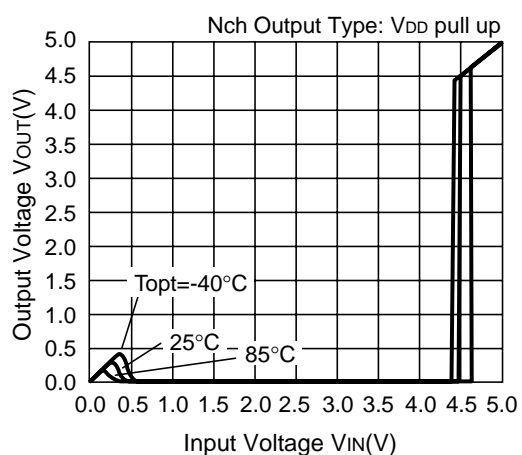
**R3112x271x**



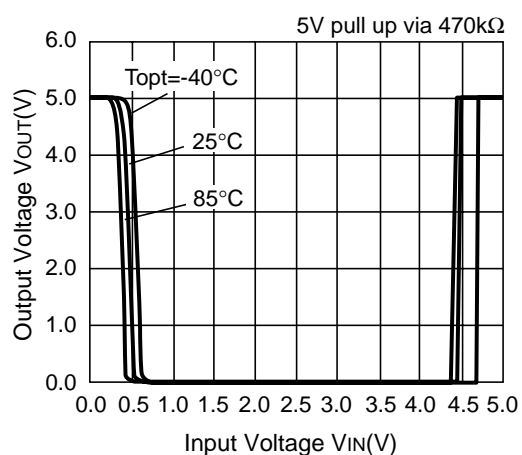
**R3112x271A**



**R3112x451x**

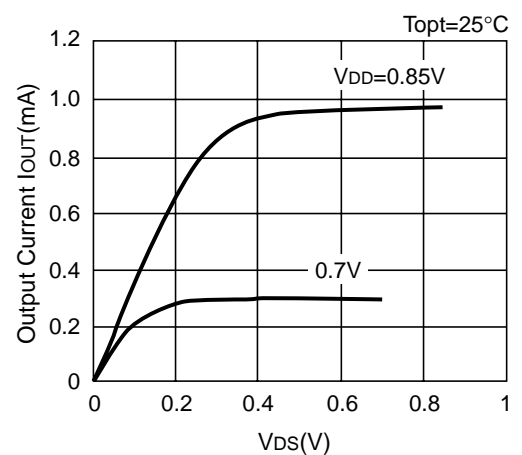


**R3112x451A**

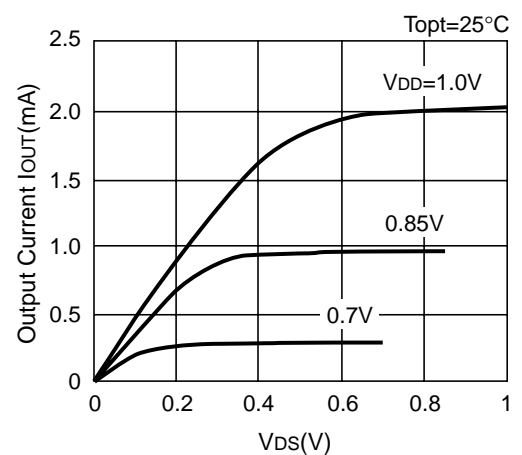


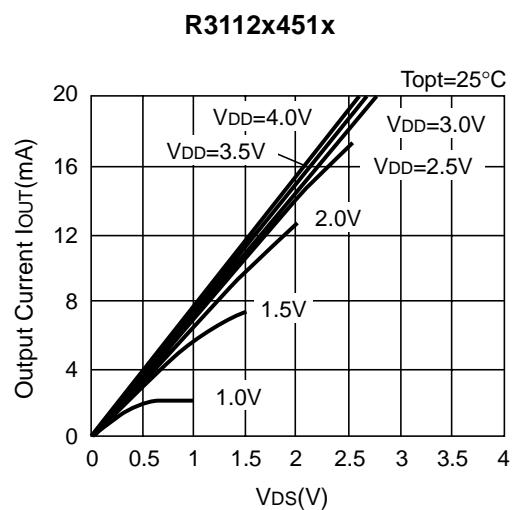
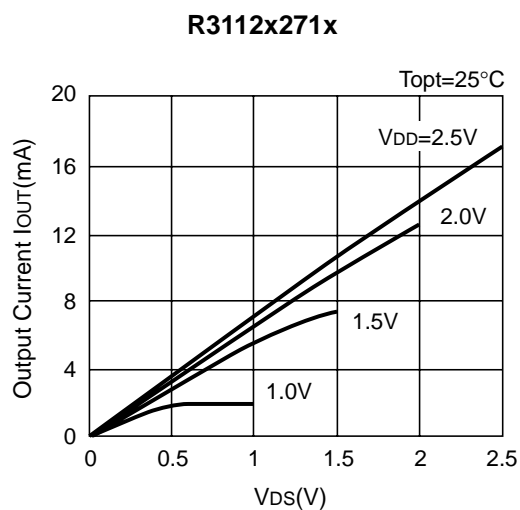
4) Nch Driver Output Current vs. V<sub>DS</sub>

**R3112x091x**

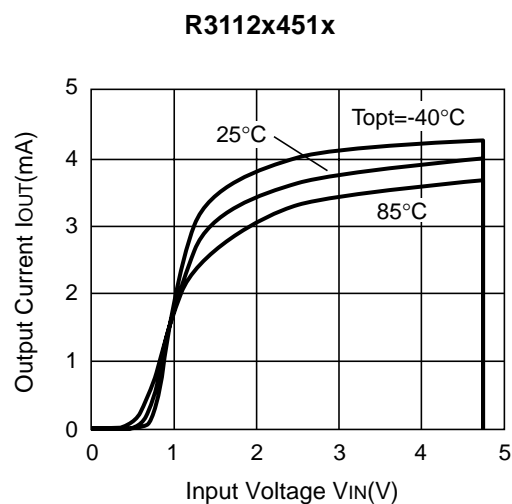
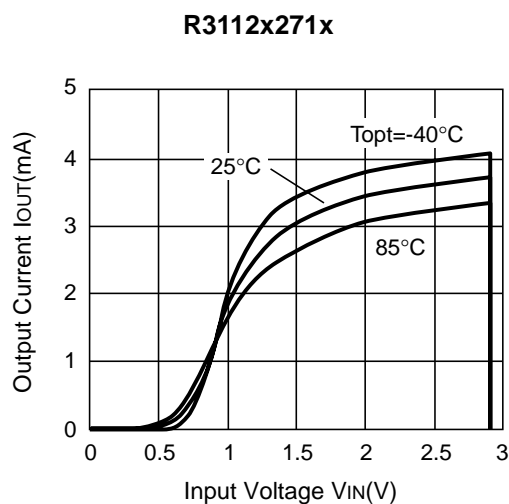
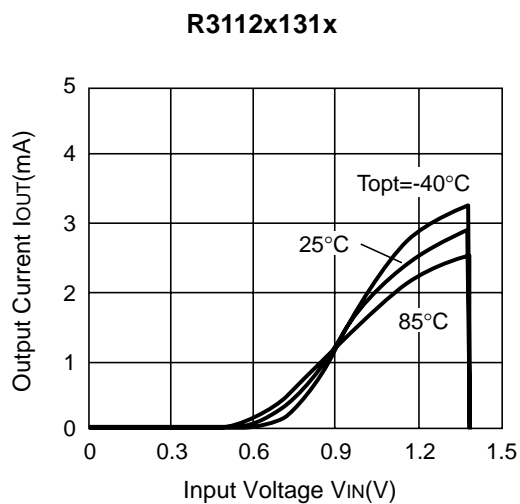
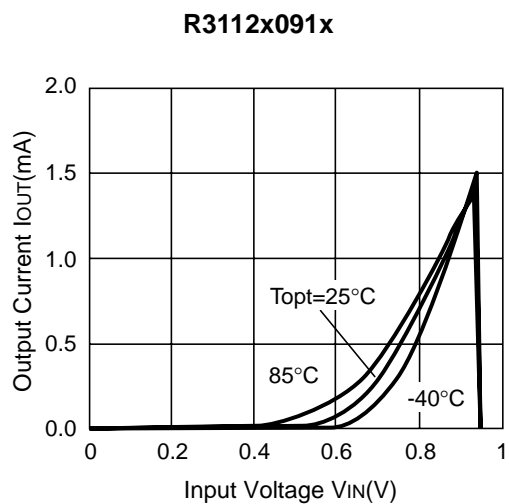


**R3112x131x**



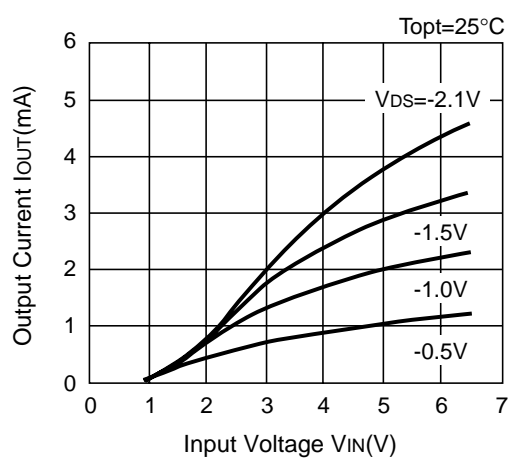


5) Nch Driver Output Current vs. Input Voltage

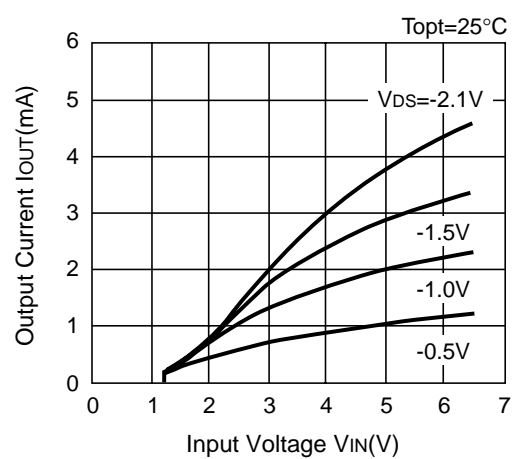


## 6) Pch Driver Output Current vs. Input Voltage

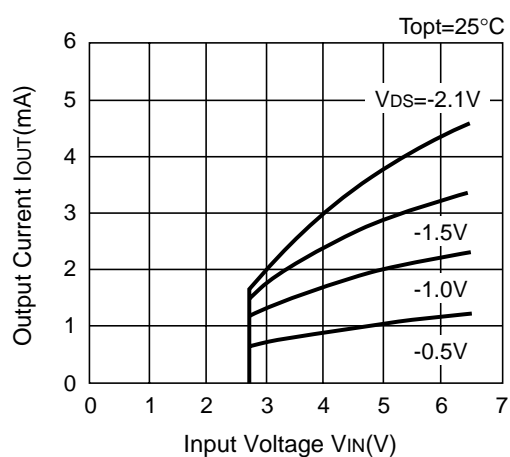
R3112x091C



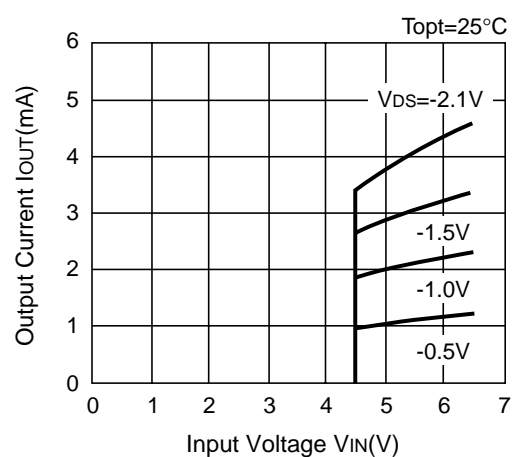
R3112x131C



R3112x271C



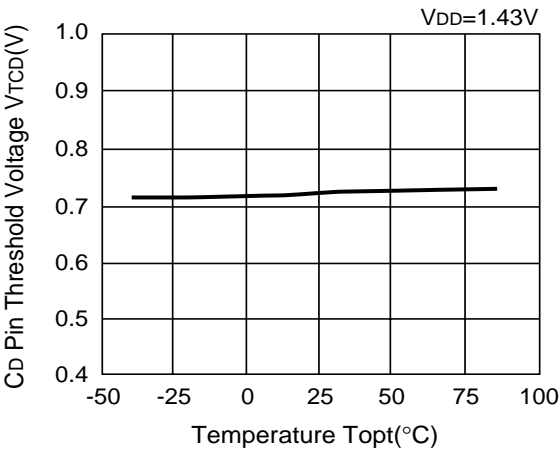
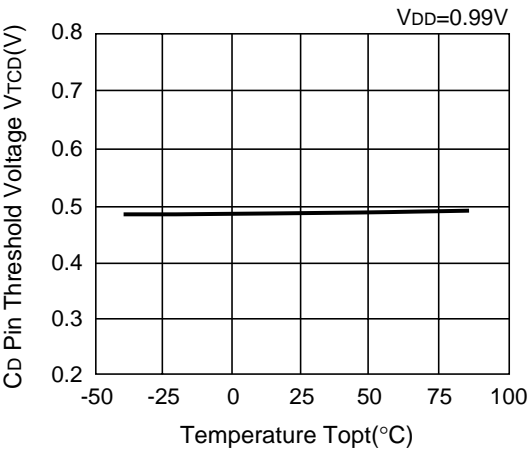
R3112x451C

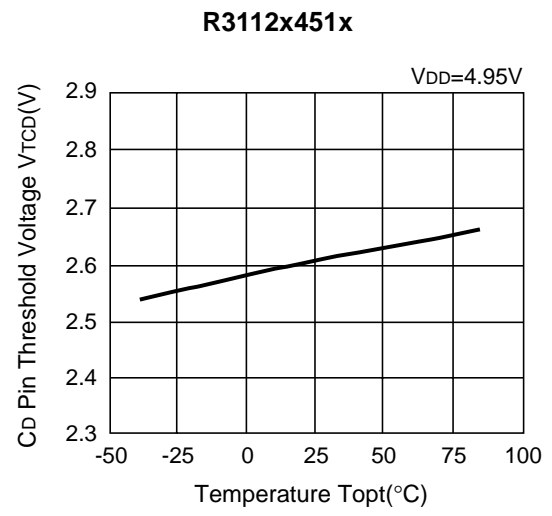
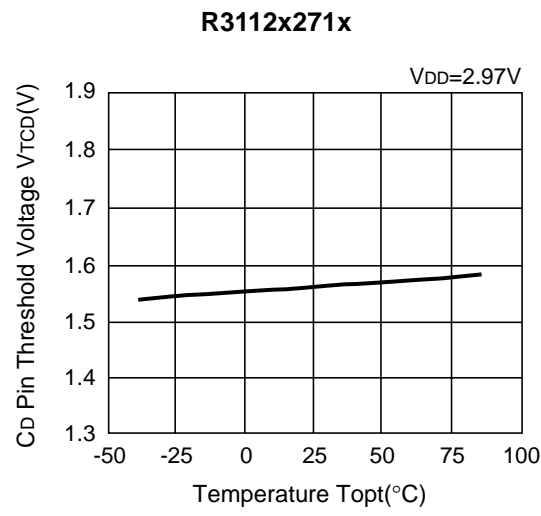


## 7) Cd Pin Threshold Voltage vs. Temperature

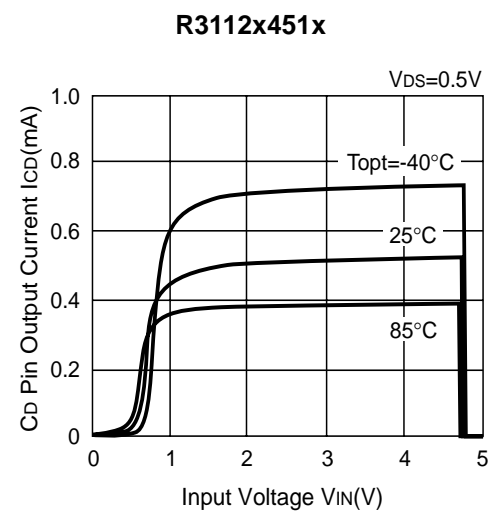
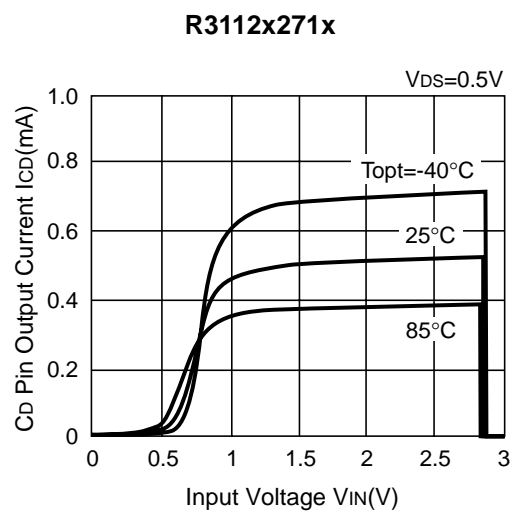
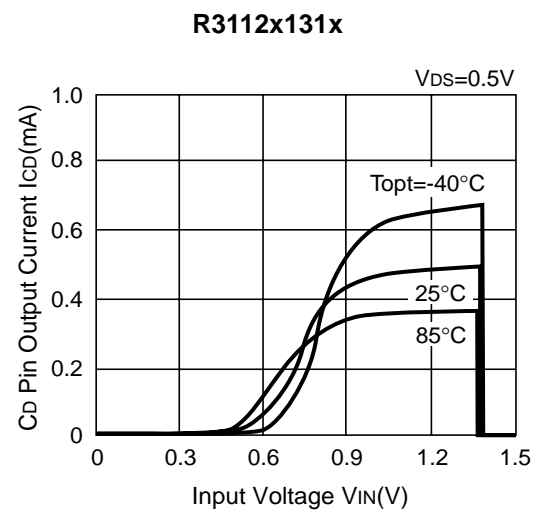
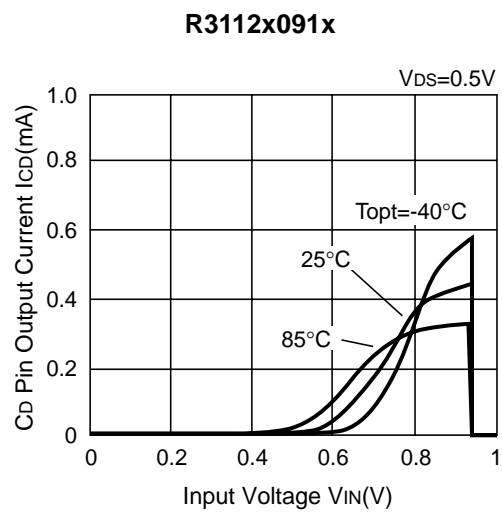
R3112x091x

R3112x131x





8) Cd Pin Output Current vs. Input Voltage



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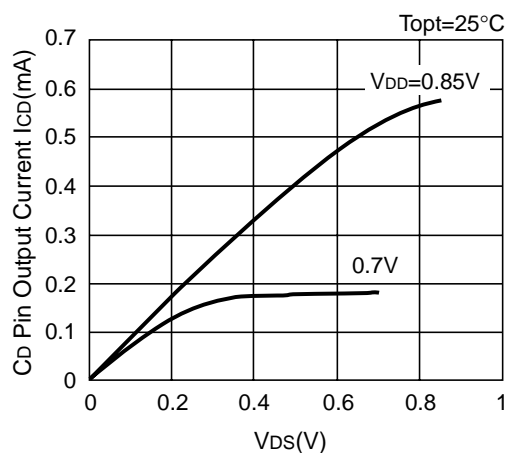
R3112xxx1A/C

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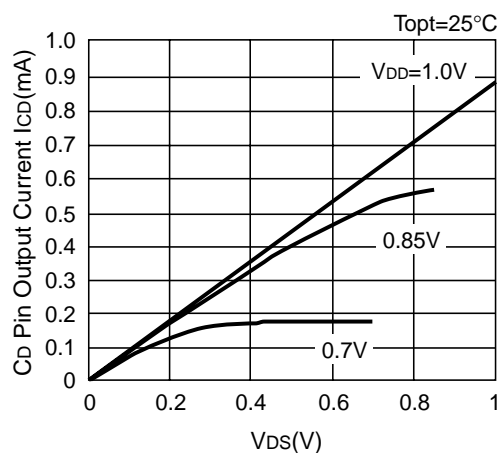


9) Cd Pin Output Current vs.  $V_{DS}$  ( $T_{opt}=25^{\circ}\text{C}$ )

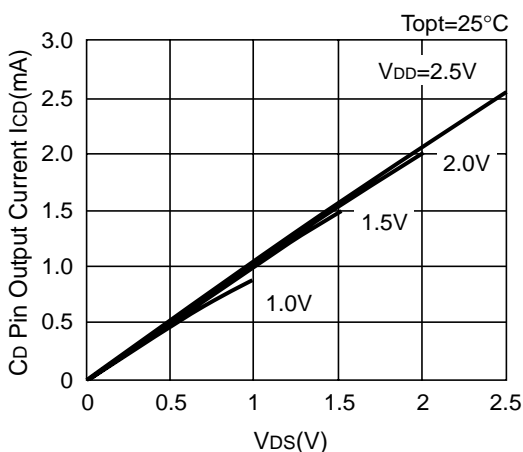
**R3112x091x**



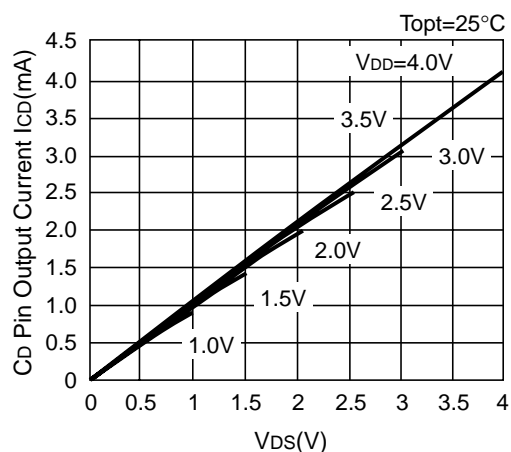
**R3112x131x**



**R3112x271x**



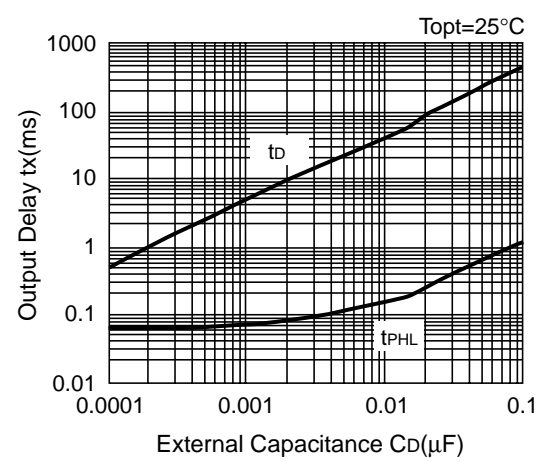
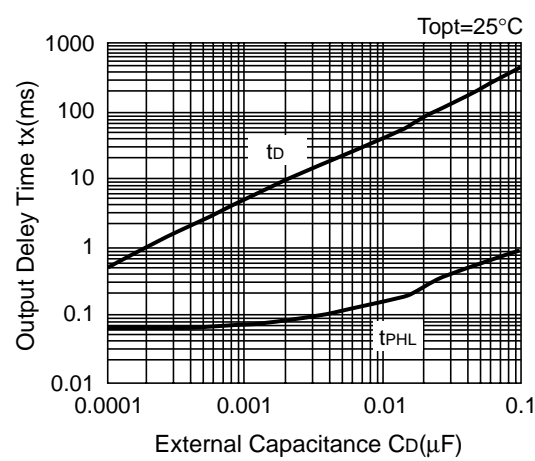
**R3112x451x**

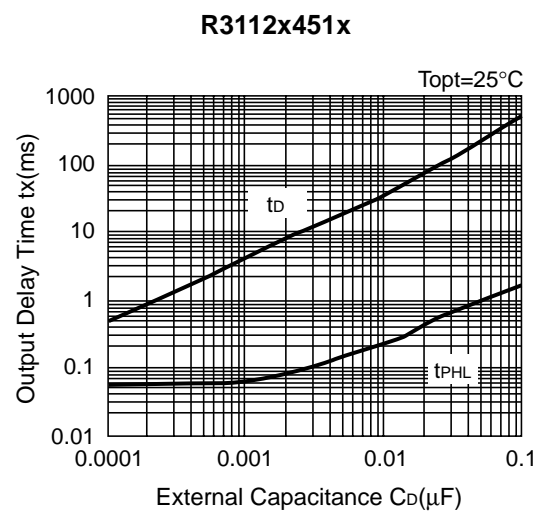
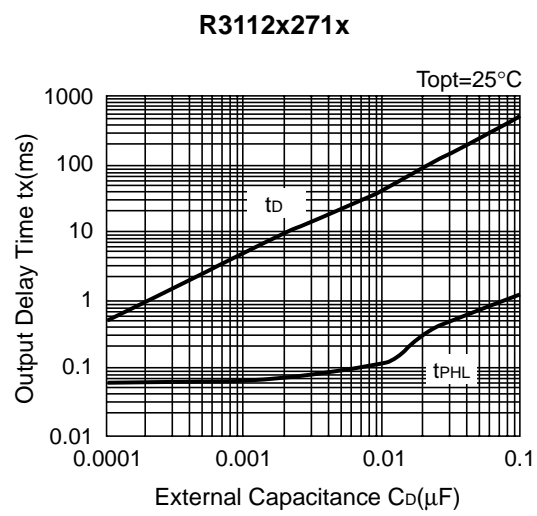


10) Output Delay Time vs. External Capacitance ( $T_{opt}=25^{\circ}\text{C}$ )

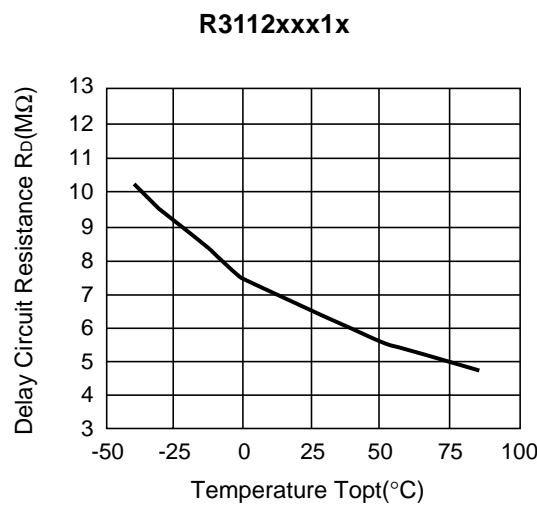
**R3112x091x**

**R3112x131x**





11) Delay Circuit Resistance vs. Temperature



## TECHNICAL NOTES

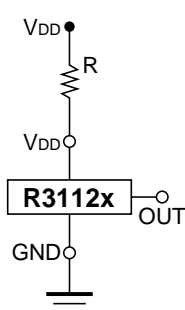


Figure A

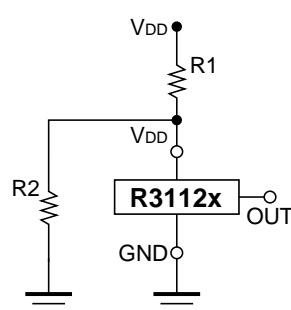


Figure B

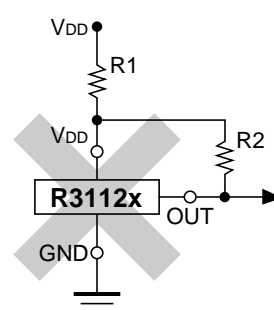


Figure C

When R3112xxx1A (Nch open drain output type) is used in Figure A or Figure B, if impedance of voltage supply pIn,  $V_{DD}$  and  $V_{DD}$  of this IC is large, detector threshold level would shift by voltage dropdown caused by the consumption current of the IC itself. Released voltage may also shift and delay time for start-up might be generated by this usage.

When R3112xxx1C (CMOS output type) is used in Figure A or Figure B, Output level could be unstable by cross conduction current which is generated at detector threshold level or at released voltage level, therefore, do not use R3112xxx1C with the connection in Figure A or Figure B.

The connection in Figure C may cause the oscillation in both R3112xxx1C (CMOS Output) and R3112xxx1A (Nch Open Drain Output), therefore do not use R3112xxx1x Series with the connection in Figure C.