Ordering number: EN 1868C

Monolithic Linear IC

SANYO No. 1868C

LA3401

VCO Non-Adjusting PLL FM MPX Stereo Demodulator with FM Accessories

The LA3401 is a multifunctional MPX demodulator IC designed for FM stereo electronic tuning. It features the VCO non-adjusting function that eliminates the need to adjust free-running frequency of VCO and the accessory functions such as FM/AM input, FM/AM input changeover, muting.

Applications

Home stereos, portable hi-fi sets

Functions

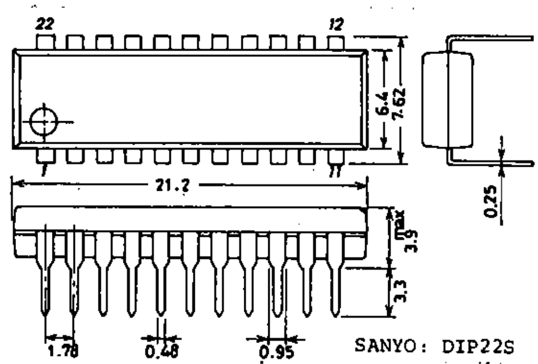
- VCO non-adjusting function
- Gain variable type post amp
- Muting at the FM-AM changeover mode (changeover mute)
- Muting function
- VCO stop function
- Muting at the V_{CC}-ON mode
- PLL MPX stereo demodulator
- FM-AM changeover
- Drive pin for external muting
- Separation adjust function

Features

- Non-adjusting VCO: Eliminates the need to adjust free-running frequency.
- Good temperature characteristic of VCO: ±0.1% typ. for ±50°C change.
- Less high frequency distortion of stereo main signal (0.07% typ. at f=10kHz) (Non-adjusting PLL makes it possible to make the capture range narrower, providing less high frequency beat distortion of stereo main signal.)
- Low distortion: Mono 0.01% typ.
  Main 0.025% typ.
- High S/N: 91dB typ./mono 300mV input, LPF
  94dB typ./mono 400mV input, LPF
- High voltage gain: Approximately 13dB (Common to FM, AM at standard constants)
  This gain can be varied by external constants.
- Wide dynamic range: Distortion 1.0%/mono 800mV, 1kHz input
  (Post amp gain: Approximately 13dB)
- The semifixed resistor (pin 4) for separation adjust can be changed to a fixed resistor or can be removed.
- High ripple rejection: 34dB typ.

Package Dimensions 3059

(unit: mm)



Maximum Ratings at Ta=25°C

				unit
Maximum Supply Voltage	$V_{CC}max$		16.0	V
Lamp Drive Current	$I_L max$		30.0	mA
Allowble Power Dissipation	Pdmax	Ta≦45°C	620	mW
Operating Temperature	Topr		-20 to +70	°C
Storage Temperature	Tstg		-40 to +125	°C

Operating Conditions at Ta=25°C

			unit
Recommended Supply Voltage	V_{CC}	13.0	V
Recommended Input Signal Voltage	V_i	300 to 400	mV
Operating Voltage Range	V_{CC} op	6.5 to 14.0	V

Operating Characteristics at Ta=25°C, V_{CC}=13V, f=1kHz, input 400mV, L+R=90%, pilot=10%

			min	typ	max	unit
Quiescent Current	Icco	Quiescent		25	35	mA
Input Resistance	ri	FM,AM input	14	20		kohm
Ripple Rejection of Power Supply				34		dB
Channel Separation	Sep	f=100Hz		45		dB
		f=1kHz	40	55		dB
		f=10kHz		50		dB
Total Harmonic Distortion	THD	Mono		0.01	0.08	%
		Stereo main		0.025	0.1	%
		Stereo sub		0.02	0.1	%
		AM		0.01	0.08	%
Allowable Input Level	Vinmax	THD=1%(FM mono,AM)	800			mV
S/N		Mono,300mV,Rg=5.1kohm,LPF		91		dB
		Mono,400mV,Rg=5.1kohm,LPF	80	94		dB
Output Voltage (*1)	Vo	Mono,AM,Input 300mV	802	1162	1545	mV
		Mono,AM,Input 400mV	1070	1550	2060	mV
Channel Balance	CB	Mono,AM			1	dB
Muting Attenuation	Attmute	External mute OFF	70	79		dB
Crosstalk	CT	AM→FM	65	72		dB
		FM→AM	65	72		dB
Mute-ON Voltage	Vmton	Pin 15 voltage	3.5		Vcc-3	V
Mute-OFF Voltage	Vmtoff	Pin 15 voltage			0.3	V
FM/AM Changeover Voltage	V_{FM-AM}	Pin 10 voltage,AM→FM			0.5	V
		Pin 10 voltage,FM→AM	4.3		10	V
					Vcc-2	V
VCO Stop Voltage		Pin 17 voltage	5.0		Vcc-2	V
19kHz Carrier Leak	CL19	De-emphasis		33		dB
38kHz Carrier Leak	CL38	De-emphasis		46		dB
Variation in DC Output Voltage (External mute OFF)		Mono-stereo		35	140	mV
		Mono-mute		15	110	mV
		Stereo-mute		35	140	mV
		AM-mute		15	110	mV
Lamp Lighting Level		Pilot	4	8	17	mV
Lamp Hysteresis				3		dB
Capture Range		Pilot 30mV		±1.2		%

(Note) *1: The signal voltage after separation adjust is measured.

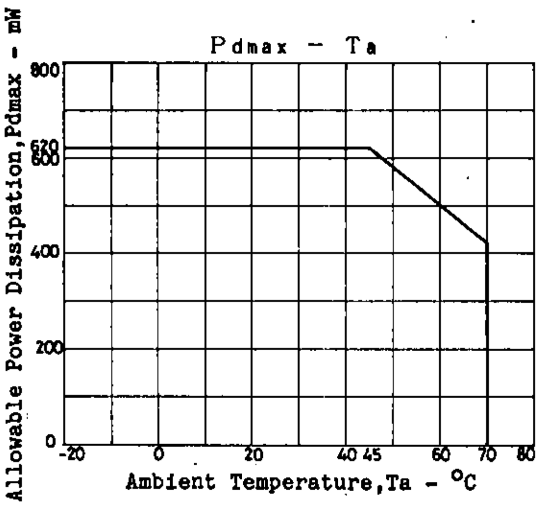
*2: The maximum voltage applied to pin 10 (FM/AM changeover voltage) is set to V_{CC}-2V (not exceeding 10V).

*3: Capture range is defined by :

$$\text{Capture range} = \left(\frac{F0-F1}{F1} - \frac{F0-456}{456}\right) \times 100\ [\%]$$

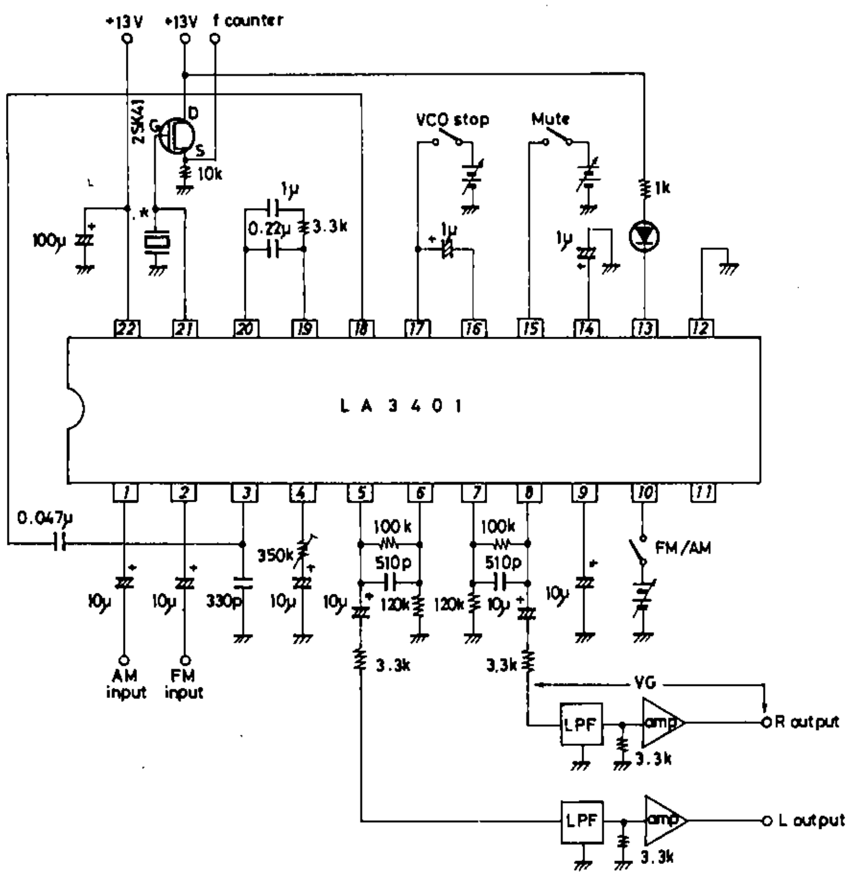
where F0: Free-running frequency

F1: Capture frequency when input frequency is changed.



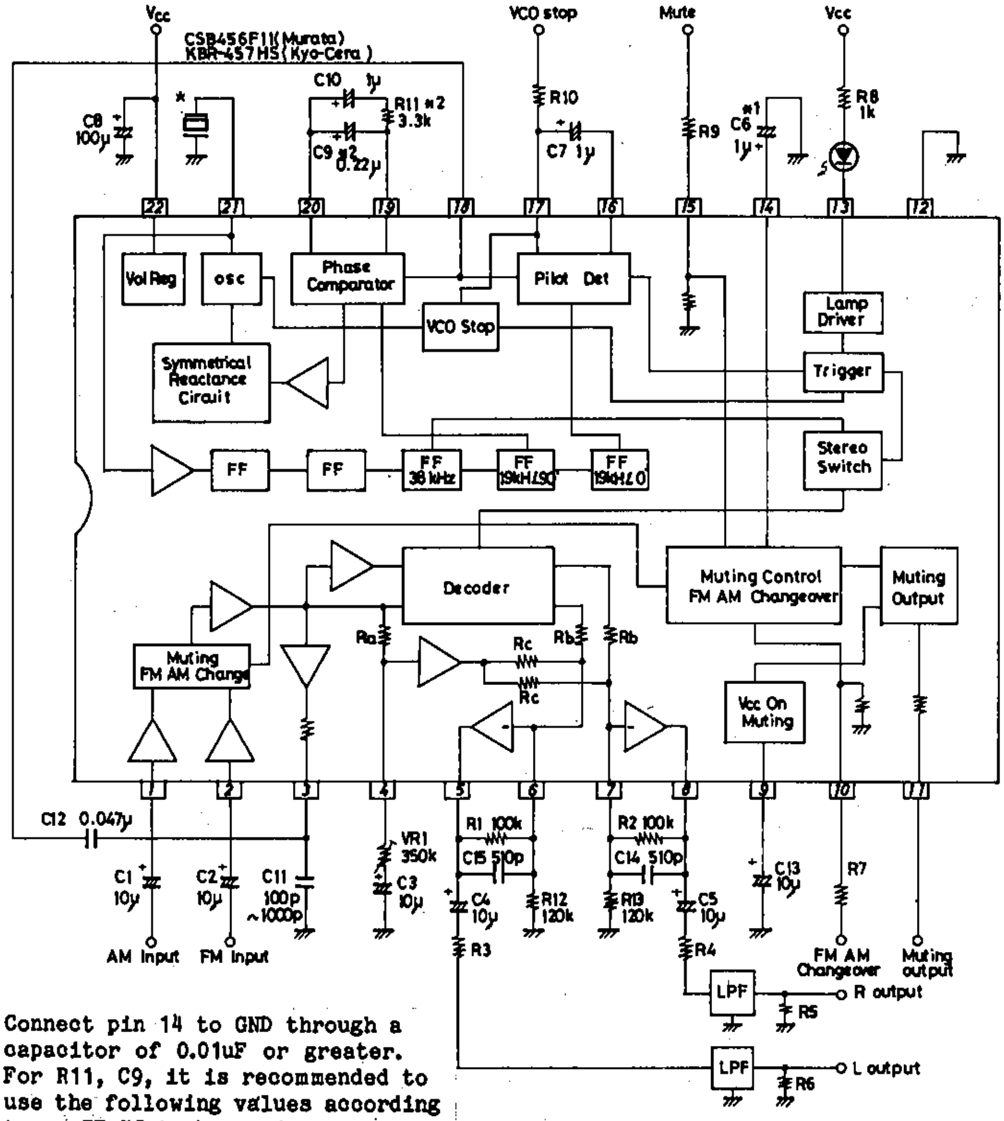
Test Circuit

Unit (resistance: Ω, capacitance: F)



*: CSB456F11typ(Murata)
LPF:BL-13(Korin Giken)
amp:THE=0.005%max,V_{NI}=1uVmax,
band width: 100kHz min,ri=330kohms max.
VG: S/N,muting attenuation,crosstalk measurement=50dBmin,
Other measurements than above=0dB

Sample Application Circuit



(Note 1) Connect pin 14 to GND through a capacitor of 0.01uF or greater.

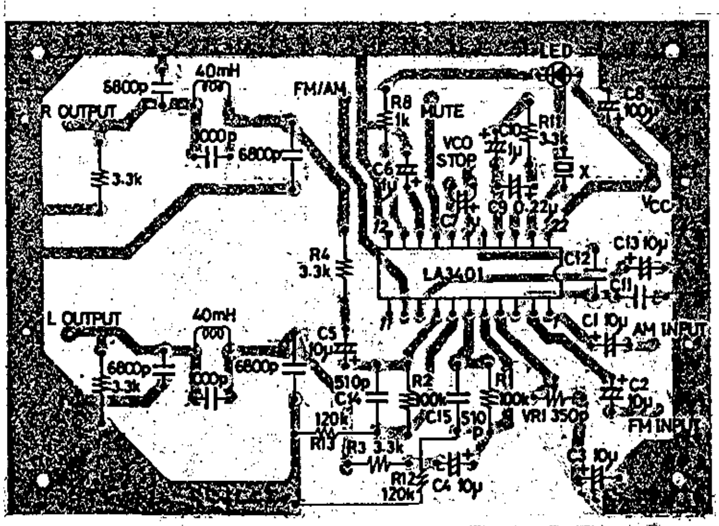
(Note 2) For R11, C9, it is recommended to use the following values according to an IF IC to be used.

IF IC	R11	C9
LA1235	3.3k	0.22μ
LA1265,1230,1231N	5.6k	0.22μ
LA1260	10k	0.1μ

*: CSB456F11(Murata)
KBR-457HS(Kyocera)

Unit (resistance: Ω, capacitance: F)

Sample Printed Circuit Pattern



(Cu-foiled area 110 x 75 mm^2)

External Parts

Part No.	Description	Remarks
C1	DC cut	
C2	"	Decreasing the value worsens separation at low frequencies.
C3	"	Decreasing the value worsens separation at low frequencies.
C4,5	"	
C6	Time constant for muting at changeover mode	Even when no FM/AM changeover muting is provided, a capacitor of 0.01μF or greater is connected.
C7	Sync detect filter	
C8	Power supply ripple filter	
C9	PLL loop filter	A capacitor value from 0.1 to 0.22uF is selected according to demodulation output of FM IF.(Note 1)
C10	PLL loop filter	Decreasing the value widens capture range; increasing the value delays stereo operation start timing after release of VCO stop.
C11	Improvement in low frequency stereo distortion	(L-R) signal and decoder 38kHz switching signal are phased with each other by a capacitor of 100 to 1000pF (differs with each audio set) connected.
C12	DC cut	
C13	Time constant for muting at V_{CC}-ON mode	Output signal is muted for a certain time after application of power.
C14,15	De-emphasis constant	The values of C14, C15 are determined so that R1・C15=R2・C14=50us(75μs) is yielded.
R1,2	Post amp feedback resistor de-emphasis constant	R1・C15=R2・C14=50us(75μs)
R3,4	LPF input resistor	3.3kohms or greater (If less than this, the maximum outut voltage cannot be obtained.) Wiring between pin 5 and R3 and between pin 8 and R4 must be made as short as possible.
R5,6	LPF output resistor	
R7	Limiting resistor	The value of R7 is determined so that voltage applied to pin 10 becomes a value from 4.3V to V_{CC}-2V (not exceeding 10V).
R8	Limiting resistor	Current flowing into pin 13 must not exceed 30mA.
R9	Limiting resistor	The value of R9 is determined so that voltage applied to pin 15 becomes a value from 3.5V to V_{CC}-3V.
R10	Limiting resistor	The value of R10 is determined so that voltage applied to pin 17 becomes a value from 5V to V_{CC}-2V. For how to obtain R10, refer to VCO stop application mentioned later.

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Part No.	Description	Remarks
R11	Loop filter	A resistor value from 3.3 to 10kohms is selected according to demodulation output of FM IF (Note 1). Increasing the value widens capture range, but delays stereo operation start timing after release of VCO stop (Note 2).
R12,13	Output DC voltage setting	Post amp output DC voltage 3.3(1+R_1/R12) or 3.3(1+R2/R13), extension in output dynamic range .
VR1	Separation adjust	Separation is adjusted by changing (L+R) signal level with VR1.
X	Free-running frequency setting	CSB456F11(Murata),KBR-457HS(Kyocera)

Note 1 : For C9, R11 setting, refer to Sample Application Circuit (Note 2) and Note 2 for Using IC.

Note 2 : To advance stereo operation start timing, the value of C10 is decreased. Decreasing the value of C10 narrows capture range. This narrowing also depends on the value of C9. It is recommended to use C10 of 0.47uF or greater.

Pin Voltage, Name, Remarks

Pin No.	Voltage[V]	Pin Name	Remarks
1	3.3	AM input	Input resistor 20kohms
2	3.3	FM input	Input resistor 20kohms
3	3.3	Composite amp output	Output resistor 1kohm
4	3.3	Separation adjust	
5	3.3	Post amp output	L output
6	3.3	Post amp input	Minus input
7	3.3	Post amp input	Minus input
8	3.3	Post amp output	R output
9	3.3	V_{CC}-ON muting	
10	-	FM/AM changeover	Input resistor 80kohms
11	-	Muting output	
12	0	GND	
13	-	Stereo indicator	Open collector
14	0 or 4.9	Changeover mute	Gnd through a capacitor of 0.01µF or greater
15	-	Muting	Input resistor 80kohms
16	2.7	Pilot sync detect filter	
17	2.7	Pilot sync detect filter, VCO stop	
18	2.7	PLL input	
19	2.7	Loop filter	
20	2.7	Loop filter	
21	-	OSC	-4.2V -2.5V
22	Vcc	Power supply	

Note for Using IC

1. Ceramic resonator
   (1) Shown below are ceramic resonators recommended for use in the LA3401.

Type No.	Supplier
CSB456F11	Murata
KBR-457HS	Kyocera

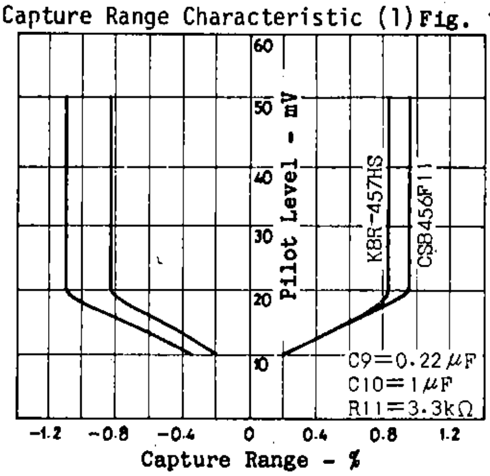
(2) By externally connecting a capacitor in parallel with a ceramic resonator, ceramic resonators shown below can be also used.

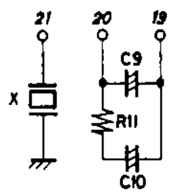
Ceramic resonator	Parallel external capacitor
CSB456F10 (Murata)	20pF
KBR-457HS1 (Kyocera)	15pF

2. Capture range and PLL loop filter constants

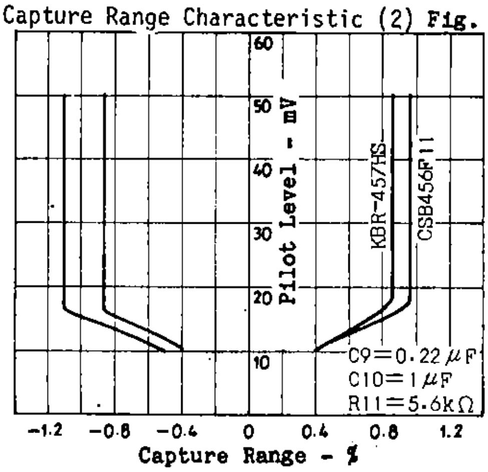
(1) It is desirable that the capture range, which is related to the stereo distortion, should be set in the range where the capture range does not depend on the pilot level. For example, when the PLL loop filter constants are C9=0.22uF, C10=1uF, R11=3.3kohms, the capture range characteristic becomes as shown in Fig. 1. For these loop filter constants, it is desirable that the input pilot level should be approximately 20mV or greater where the capture range does not depend on the pilot level. Figs. 2, 3 shows how the capture range characteristic changes with the loop filter constants.

Capture Range Characteristic (1) Fig. 1

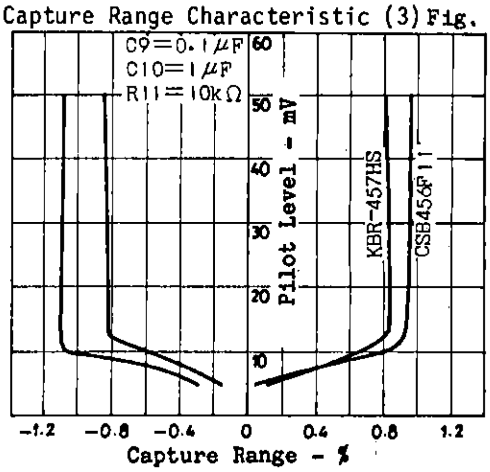




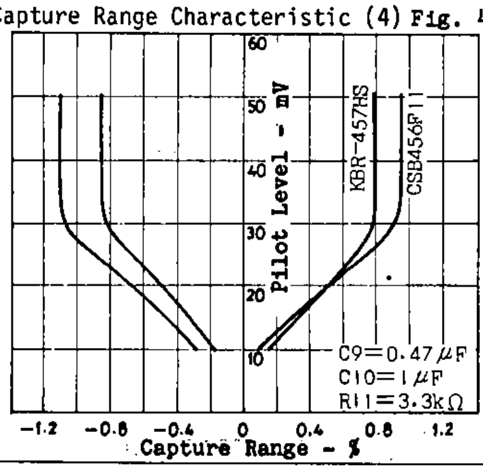
Capture Range Characteristic (2) Fig. 2



Capture Range Characteristic (3) Fig. 3

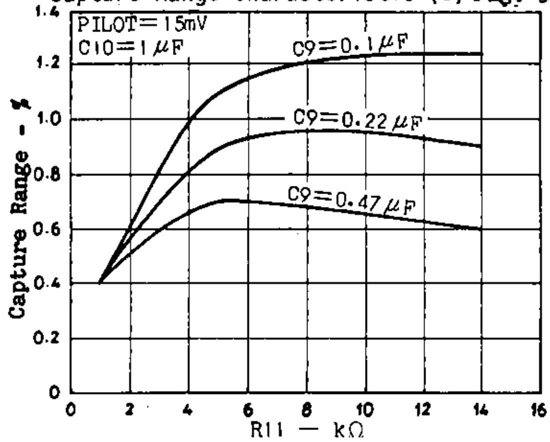


Capture Range Characteristic (4) Fig. 4



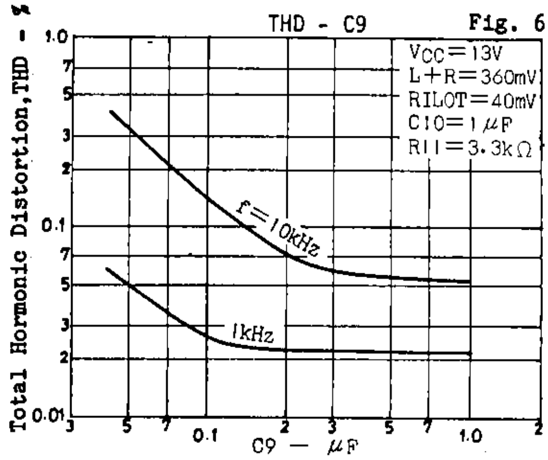
(2) Fig. 5 shows how the capture range changes with loop filter constant R11.

Capture Range Characteristic (5) Fig. 5



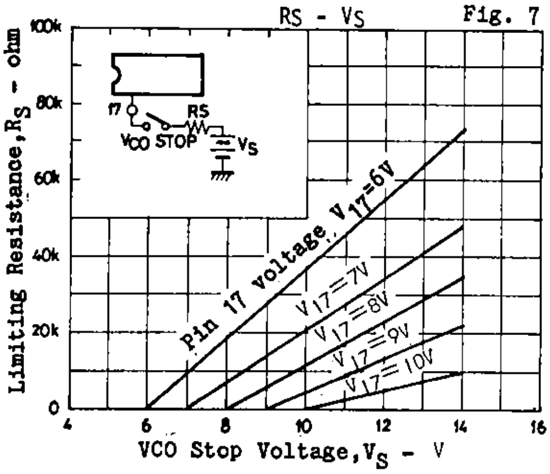
(3) Fig. 6 shows how the distortion of stereo main (L + R) changes with loop filter C9.

THD - C9 Fig. 6



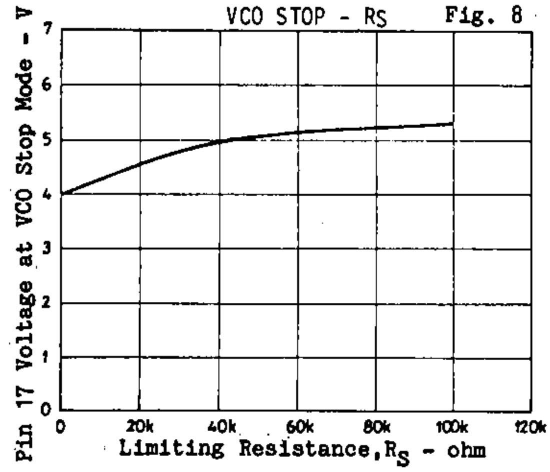
3. VCO stop method
The relation between VCO stop supply V_S and limiting resistor R_S is shown in Fig. 7. R_S must be set so that the voltage on pin 17 is within the specified range when V_S is applied. For example, it is seen from Fig.7 that the value of R_S is approximately 33kohms when the voltage on pin 17 is set to 7V at V_S=12V. The relation between R_S and the voltage on pin 17 at the VCO stop mode is shown in Fig. 8. The voltage on pin 17 at the VCO stop mode increases with increasing R_S. The lower value on pin 17 is set by adding an increase in the voltage to the minimum value specified.

RS - VS Fig. 7



4. Forced monaural mode
To provide the forced monaural mode, pin 16 is connected to GND through a resistor of 10kohms. In this case, VCO oscillation does not stop.

VCO STOP - RS Fig. 8



FM/AM mode changeover

(1) How to changeover

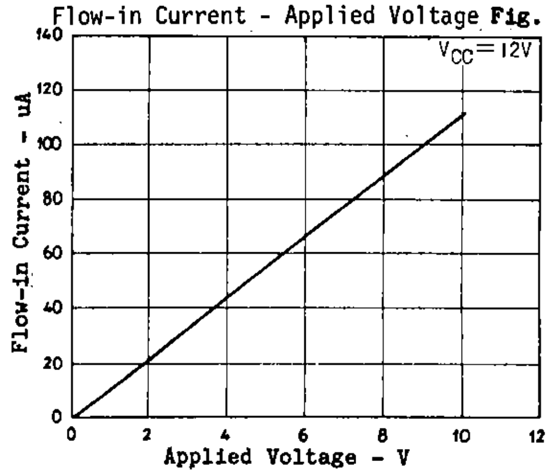
Changeover is performed by externally applying voltage to pin 10.

FM→AM changeover: Apply a voltage of 4.3V to V_{CC}-2V (not exceeding 10V) to pin.

AM→FM changeover: Apply a voltage of 0.5V or less to pin 10.

Fig. 9 shows the relation between the voltage on pin 10 and the flow-in current.

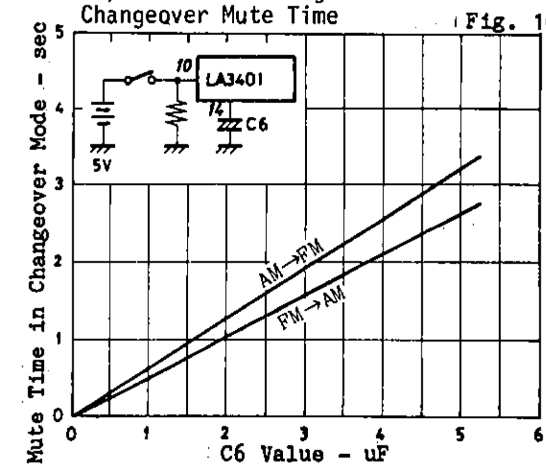
Flow-in Current - Applied Voltage Fig. 9



(2) Muting in the changeover mode

Muting is turned ON for a certain period of time fixed by external capacitor C6 in the FM→AM or AM→FM changeover mode (muting in the changeover mode). Fig. 10 shows the relation between the muting time in the changeover mode and C6.

Capacitance in Chageover Mode - Changeover Mute Time Fig. 10



(3) VCO oscillation stop in the AM mode

By externally applying a specified voltage to pin 10 to select the AM mode, VCO oscillation stops automatically and the monaural mode is forced to be entered.

Muting function

(1) How to turn ON/OFF muting

Muting is turned ON/OFF by externally applying voltage to pin 15.

Muting ON: Apply a voltage of 3.5V to V_{CC}-3V to pin 15.

Muting OFF: Apply a voltage of 0.3V or less to pin 15.

Fig. 9 shows the relation between the voltage on pin 15 and the flow-in current.

(2) Hysteresis characteristic

Muting ON/OFF is allowed a hysteresis of approximately 6dB to prevent malfunction attributable to ripple included in the IF meter output, muting drive output.

(3) Forced monaural in the muting mode

By externally applying a specified voltage to pin 15 to select the muting mode, the forced monaural mode is automatically entered.

Muting output

Since the muting signal is delivered at the muting output (pin 11) in the following mode, external transistors can be used to provide external muting.

① AM→FM changeover mode (muting in the changeover mode)

② Muting mode

③ V_{CC}-ON/OFF mode

Fig. 11 shows a sample application of external muting.

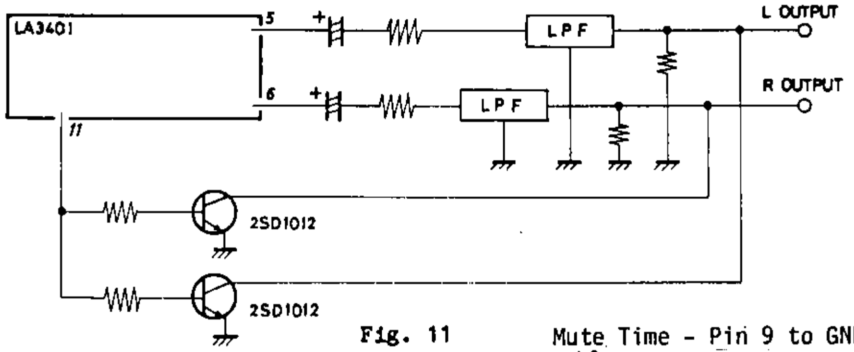
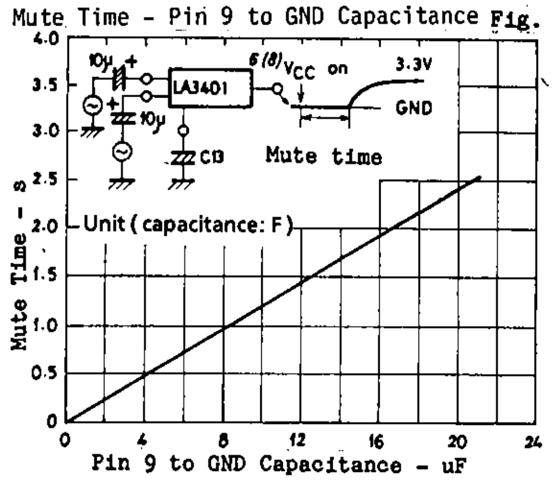


Fig. 11

Mute Time - Pin 9 to GND Capacitance Fig. 12



Muting in the V_{CC}-ON mode

1. Muting time

   Muting is turned ON for a certain period of time fixed by external capacitor C13. Fig. 12 shows the relation between the muting time and C13.

2. Values of AM/FM input coupling capacitors (C1, C2) and value of C13

   If muting is released before the DC voltage on the AM input (pin 1) or FM input (pin 2) is stabilized after V_{CC} is turned ON, pop noise is generated. Therefore, the value of C13 must be determined by the input coupling capacitor value. The adequate value of C13 for C1, C2 of 10uF is 10uF or thereabouts. If the value of C1, C2 is increased, the value of C13 is also increased accordingly.

Feedback resistance of post amp and total gain, de-emphasis constant values

Table 1 shows the feedback resistance of the post amp and the total gain, de-emphasis.

Table 1. Feedback resistance of post amp and total gain, de-emphasis

R1(R2)	Total	C13(C14)50μs	C13(C14)50μs
33kΩ	3.0dB	1500pF	2200pF
39kΩ	4.5dB	1200pF	2000pF
51kΩ	6.5dB	1000pF	1500pF
62kΩ	8.5dB	750pF	1200pF
82kΩ	11.0dB	620pF	910pF
100kΩ	13.0dB	510pF	750pF
130kΩ	15.0dB	390pF	560pF
150kΩ	16.0dB	330pF	510pF
180kΩ	17.5dB	270pF	390pF

Total gain: Value in monaural mode

R1·C15=R2·C14=50us,75us

How to extend the dynamic range of the post amp

In the Sample Application Circuit of the LA3401 the dynamic range of the post

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amp is extended by connecting resistors R_{12}, R_{13} across the virtual GND points (pins 6,7) of the post amp and GND as shown in Fig. 13 to set the output (pins 5,8) DC voltages to an adequate value.

The DC voltages on pins 5, 8 are obtained as follows:

$$3.3\left(\frac{R_B+R_1}{R_B}\right)=3.3\left(1+\frac{R_1}{R_B}\right)$$

$$3.3\left(\frac{R_B+R_2}{R_B}\right)=3.3\left(1+\frac{R_2}{R_B}\right)$$

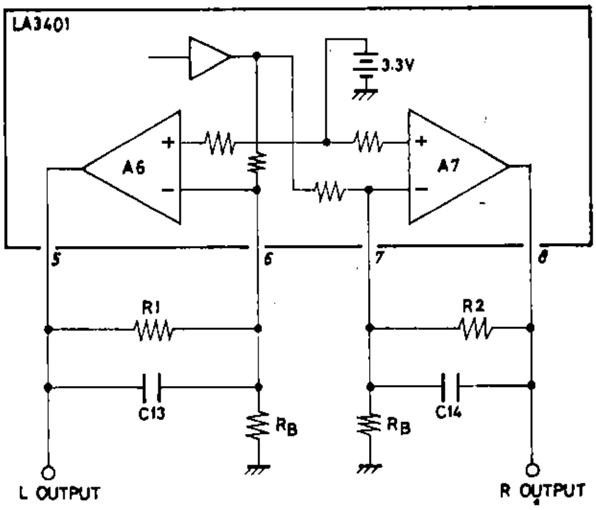
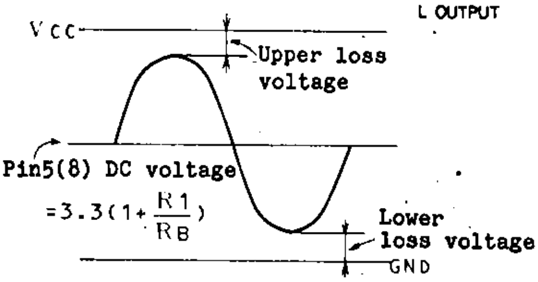


Fig. 13



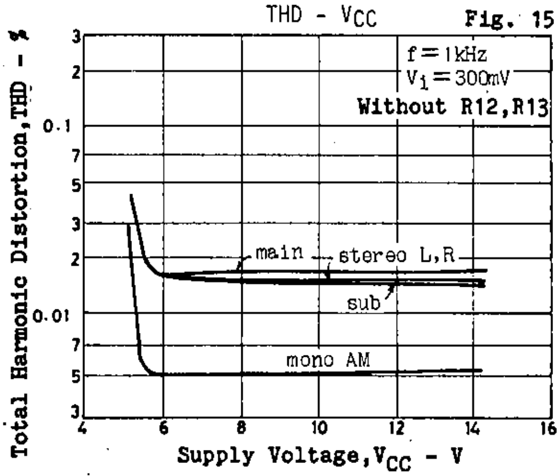
Upper, lower loss voltage of post amp
Fig. 14

The upper and lower loss voltages of the post amp output are approximately 2V and 0.5V respectively as shown in Fig. 14. With these loss voltages considered, the voltages on pins 5, 8 are set.

In the Sample Application Circuit the voltages on pins 5, 8 are set to 6V and the maximum output voltage is obtained at V_{CC}=13V.

The Sample Application Circuit provides the reduced voltage characteristic at approximately 9V. If the reduced voltage characteristic at approximately 6V is required, remove R_{12}, R_{13} shown in the Sample Application Circuit. Then, the output (pins 5, 8) DC voltages becomes approximately 3.3V and the reduced voltage characteristic becomes as shown in Fig. 15. Fig. 15 shows the THD vs. V_{CC} characteristic, but other characteristics such as separation are also available at V_{CC}=6V by removing R_{12}, R_{13}.

THD - V_{CC} Fig. 15



Low-pass filter

Fig. 16 shows a sample circuit configuration where an LC filter is used as the low-pass filter and Fig. 17 shows a sample characteristic of this filter. As compared with the LPF(BL-13) in the Sample Application Circuit, the use of this filter makes the attenuation less at 19kHz, 38kHz; therefore, carrier

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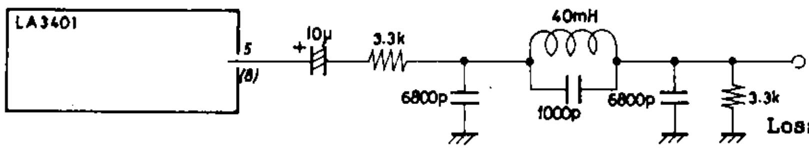
leak at the LPF output causes the stereo distortion and separation characteristic to get worse than specified in the Operating Characteristics. For the stereo distortion, the BL-13 provides approximately 0.02%, while the LC filter provides approximately 0.5%.

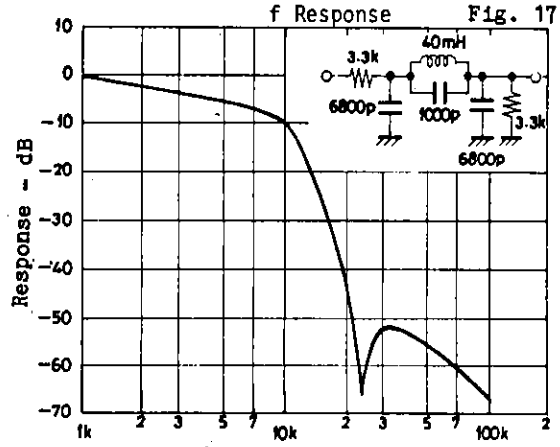
Unit (resistance: Ω, capacitance: F)



Loss of LPF: Approx. -6dB

Fig. 16 Sample LC filter circuit (including de-emphasis circuit)

f Response Fig. 17



Decoder circuit (Refer to the Block Diagram in the Sample Application Circuit.)

The LA3401 adopts a decoder circuit of chopper type. The sub signal sync-detected by this decoder is applied to the post amp minus input through Rb as shown in the Sample Application Circuit. This signal is matrixed with the main signal coming out of amp A5 and passing through R_C.

The gain for the sub signal is:

$$V_S \frac{R1}{Rb} \cdot \frac{2}{\pi} \text{ or } V_S \frac{R2}{Rb} \cdot \frac{2}{\pi}$$

The gain for the main signal is:

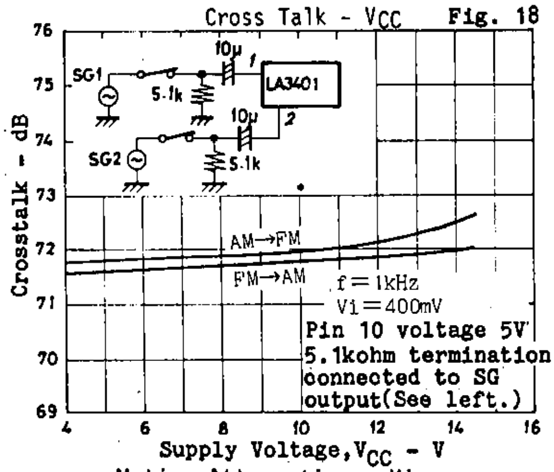
$$V_M \frac{VR1}{Ra+VR1} \cdot \frac{R1}{Rc} \text{ or } V_M \frac{VR1}{Ra+VR1} \cdot \frac{R2}{Rc}$$

R1,R2: Post amp feedback resistor
V_S: Peak value of input sub signal
VR1: Semifixed resistor for separation adjust
V_M: Peak value of input main signal

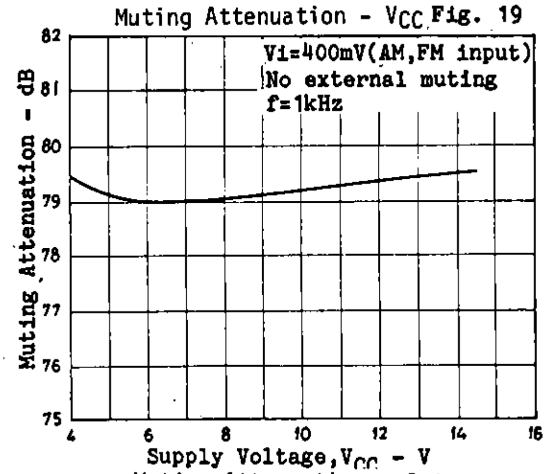
In the LA3401, the gain of the main signal is varied with VR1 to adjust the separation. Since the IF output is generally such that the sub signal level is lower than the main signal level, the separation can be adjusted by attenuating the main signal level with VR1. The use of an antibirdie filter across the IF output and the FM input of the LA3401 may cause the sub signal level to be raised, and when the sub signal level is higher than the main signal level the separation cannot be adjusted with VR1. In this case, the sub signal level is attenuated to be less than the main signal level and applied to the LA3401 and the separation is adjusted with VR1.

Unit (resistance: Ω, capacitance: F)

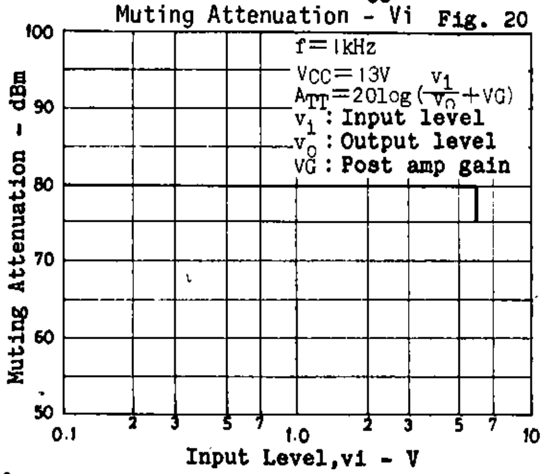
Cross Talk - V_{CC} Fig. 18



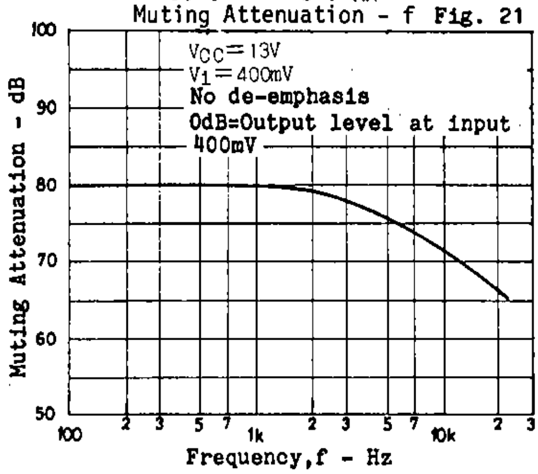
Muting Attenuation - V_{CC} Fig. 19



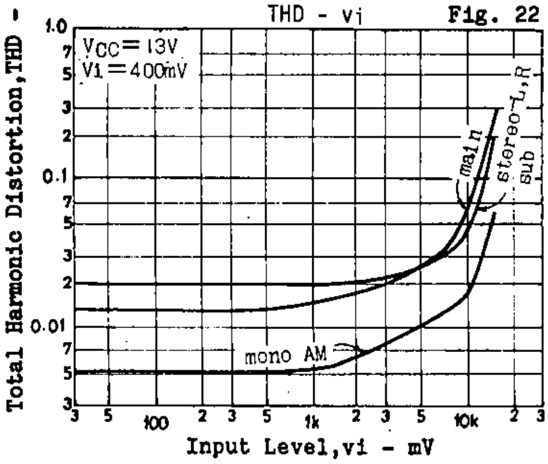
Muting Attenuation - Vi Fig. 20



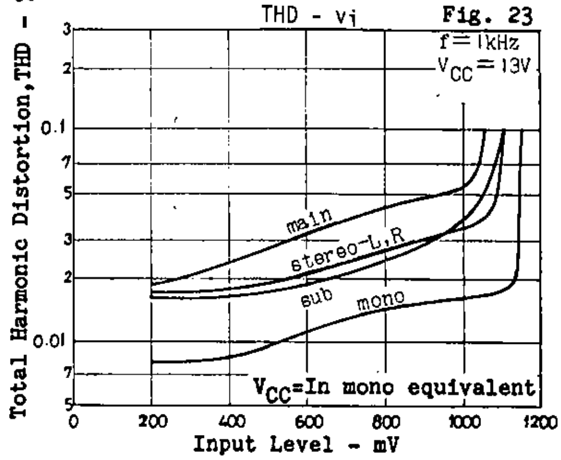
Muting Attenuation - f Fig. 21



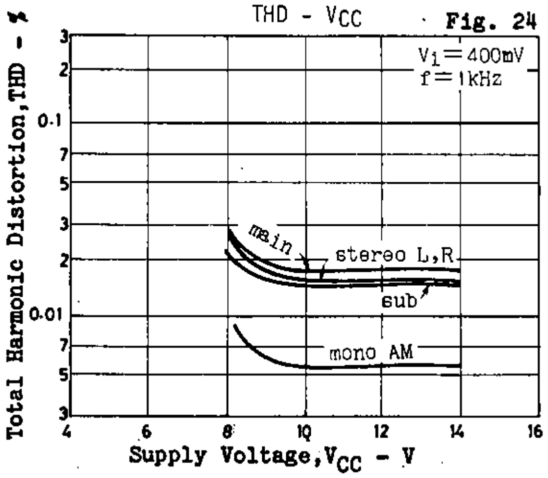
THD - v_i Fig. 22



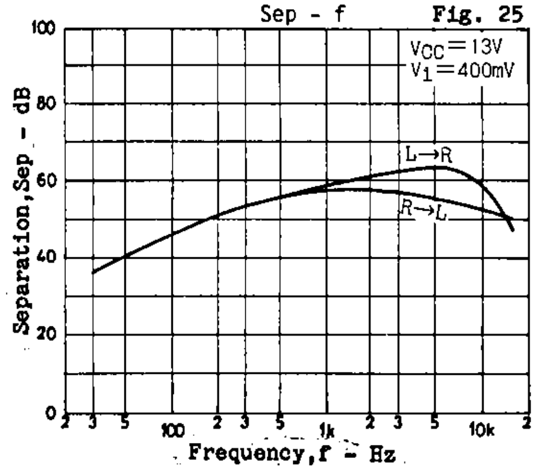
THD - v_i Fig. 23



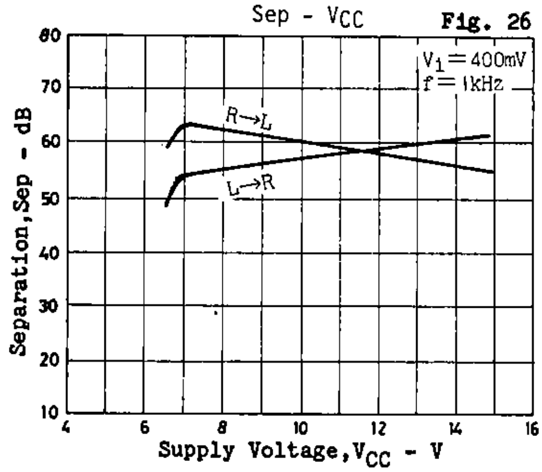
THD - V_{CC} Fig. 24



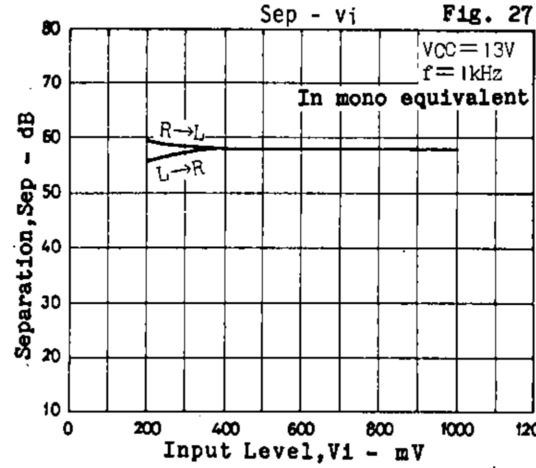
Sep - f Fig. 25



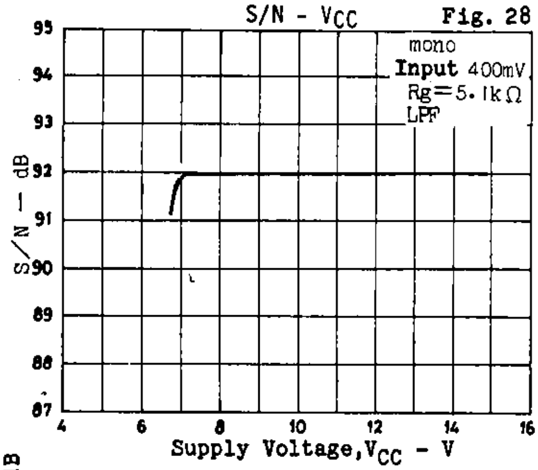
Sep - VCC **Fig. 26**



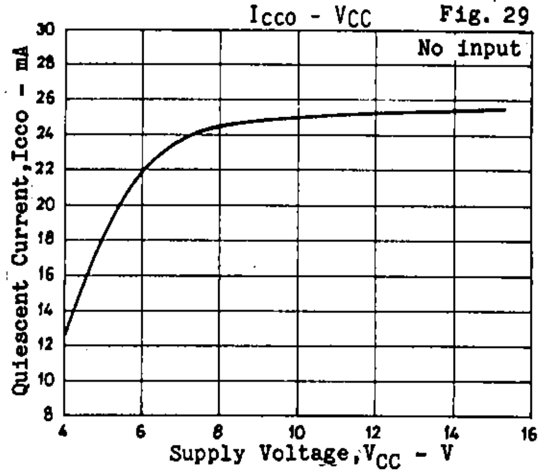
Sep - vi **Fig. 27**



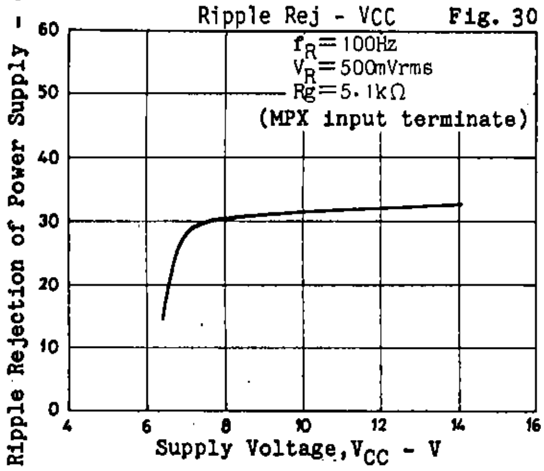
S/N - VCC **Fig. 28**



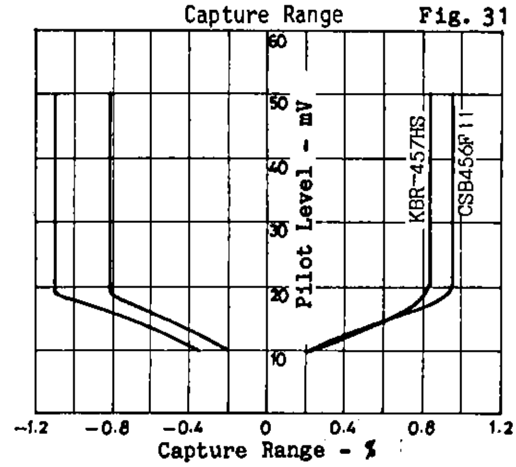
Icco - VCC **Fig. 29**



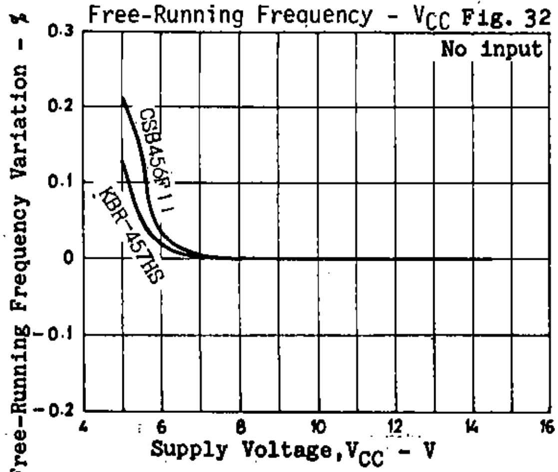
Ripple Rej - VCC **Fig. 30**



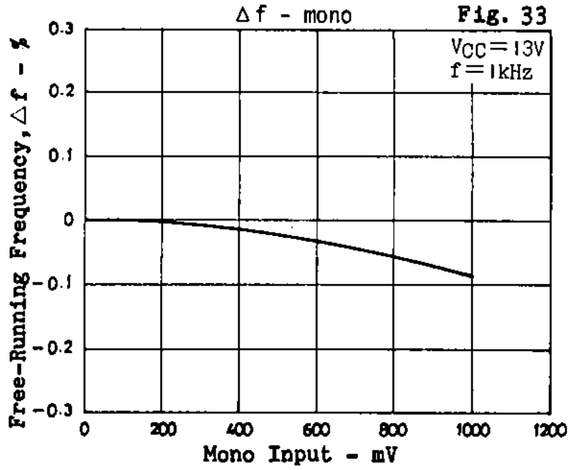
Capture Range **Fig. 31**



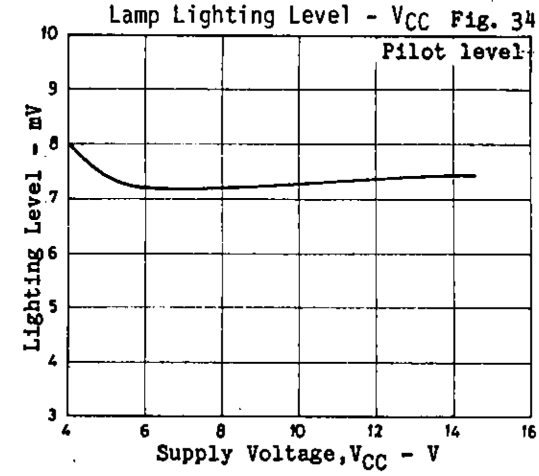
Free-Running Frequency - VCC **Fig. 32**



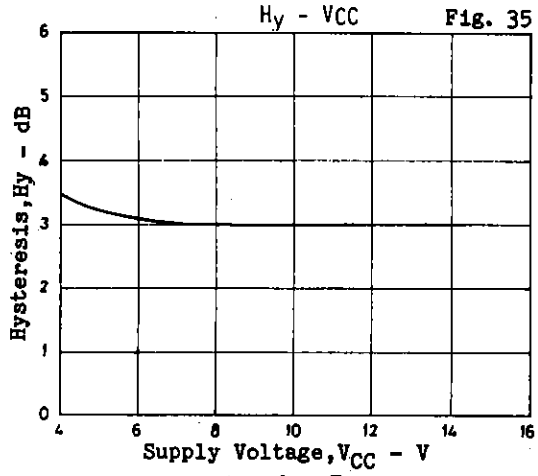
Δf - mono **Fig. 33**



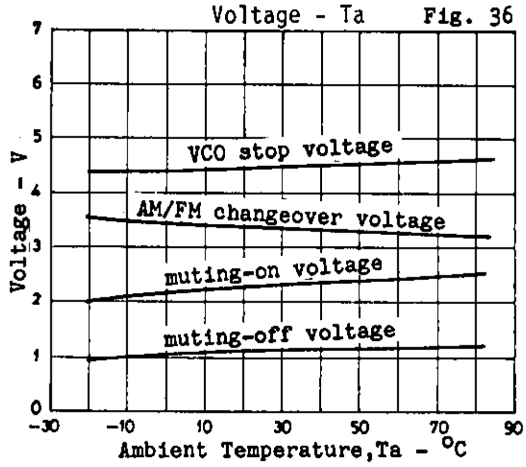
Lamp Lighting Level - V_{CC} Fig. 34



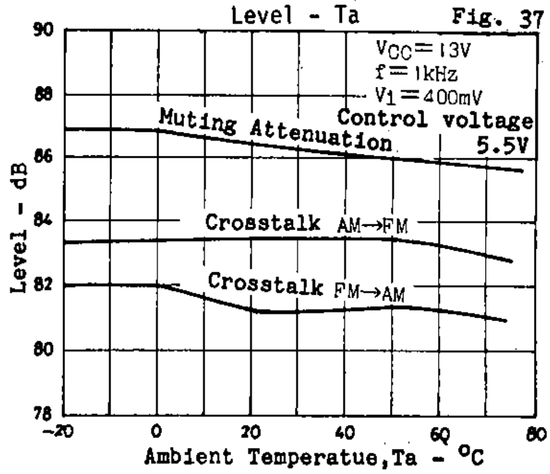
H_y - VCC Fig. 35



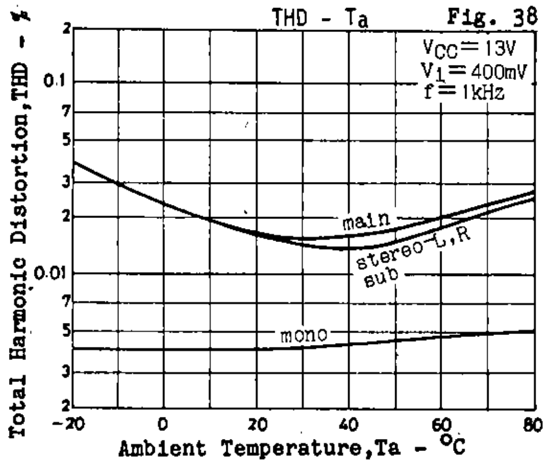
Voltage - Ta Fig. 36



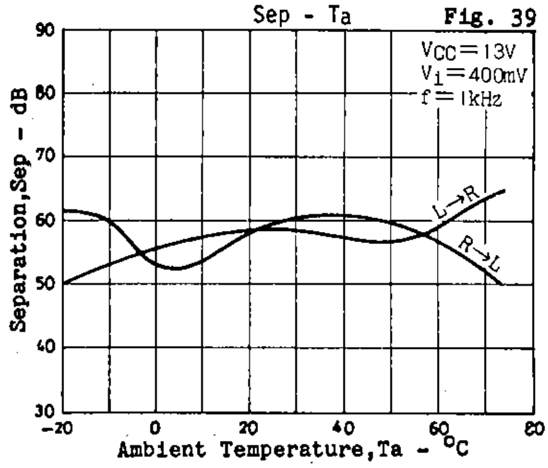
Level - Ta Fig. 37



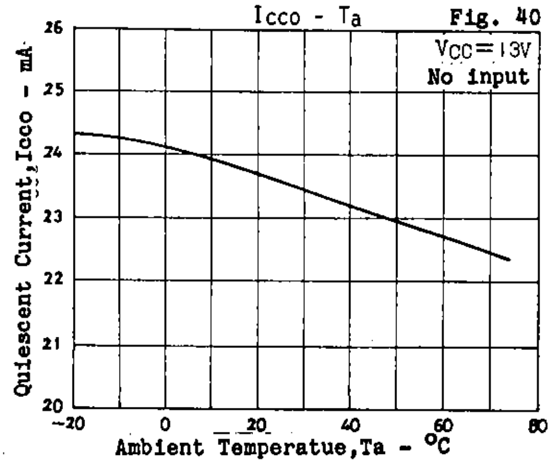
THD - Ta Fig. 38



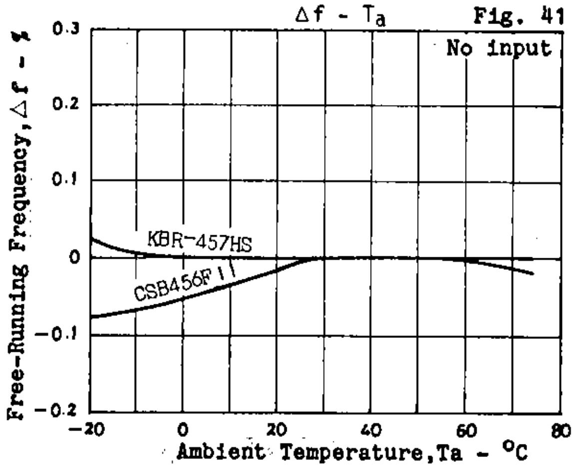
Sep - Ta Fig. 39

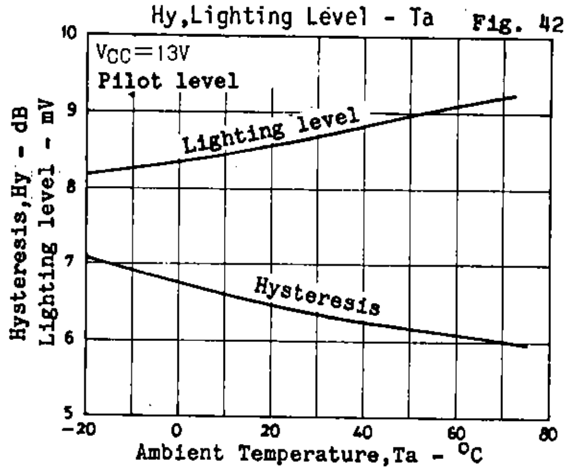


Icco - Ta Fig. 40



Δf - Ta Fig. 41




Hy,Lighting Level - Ta
Fig. 42
VCC=13V
Pilot level
Lighting level
Hysteresis
Hysteresis,Hy - dB
Lighting level - mV
10
9
8
7
6
5
-20
0
20
40
60
80
Ambient Temperature,Ta - °C