



SANYO Semiconductors

## DATA SHEET

# 2SC6014 — NPN Epitaxial Planar Silicon Transistor

## DC / DC Converter Applications

### Applications

- Relay drivers, lamp drivers, motor drivers, flash.

### Features

- Adoption of MBIT process.
- Large current capacitance.
- Low collector-to-emitter saturation voltage.
- High-speed switching.
- Narrow  $h_{FE}$  range.
- High allowable power dissipation.

### Specifications

#### Absolute Maximum Ratings at $T_a=25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	$V_{CBO}$		40	V
Collector-to-Emitter Voltage	$V_{CEO}$		30	V
Emitter-to-Base Voltage	$V_{EBO}$		6	V
Collector Current	$I_C$		5	A
Collector Current (Pulse)	$I_{CP}$		8	A
Base Current	$I_B$		600	mA
Collector Dissipation	$P_C$	Mounted on a ceramic board (250mm <sup>2</sup> ×0.8mm)	1.3	W
		$T_c=25^\circ\text{C}$	3.5	W
Junction Temperature	$T_J$		150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$		-55 to +150	$^\circ\text{C}$

#### Electrical Characteristics at $T_a=25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	$I_{CBO}$	$V_{CB}=30\text{V}, I_E=0\text{A}$			0.1	$\mu\text{A}$
Emitter Cutoff Current	$I_{EBO}$	$V_{EB}=4\text{V}, I_C=0\text{A}$			0.1	$\mu\text{A}$
DC Current Gain	$h_{FE}$	$V_{CE}=2\text{V}, I_C=500\text{mA}$	250		400	

Marking : QB

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## 2SC6014

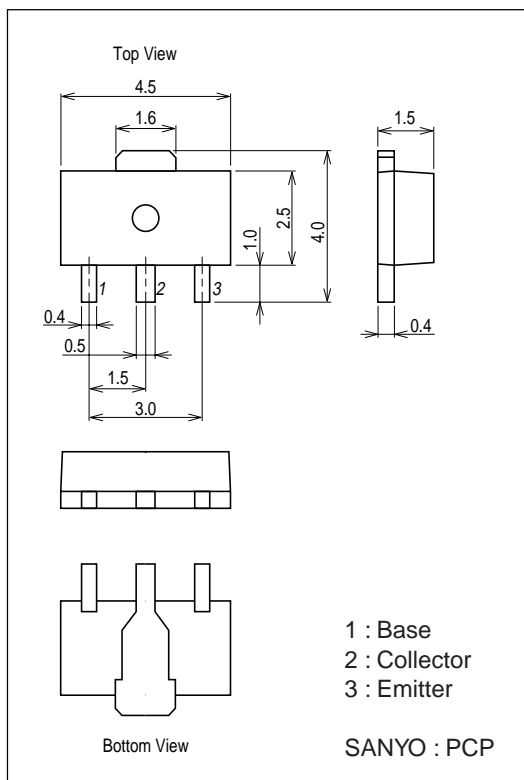
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Gain-Bandwidth Product	$f_T$	$V_{CE}=10V, I_C=500mA$		420		MHz
Output Capacitance	$C_{ob}$	$V_{CB}=10V, f=1MHz$		20		pF
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C=1.5A, I_B=30mA$		95	140	mV
		$I_C=2.5A, I_B=125mA$		135	200	mV
Base-to-Emitter Saturation Voltage	$V_{BE(sat)}$	$I_C=1.5A, I_B=30mA$		0.83	1.2	V
Collector-to-Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C=10\mu A, I_E=0A$	40			V
Collector-to-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C=1mA, R_{BE}=\infty$	30			V
Emitter-to-Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E=10\mu A, I_C=0A$	6			V
Turn-ON Time	$t_{on}$	See specified Test Circuit.		30		ns
Storage Time	$t_{stg}$	See specified Test Circuit.		300		ns
Fall Time	$t_f$	See specified Test Circuit.		15		ns

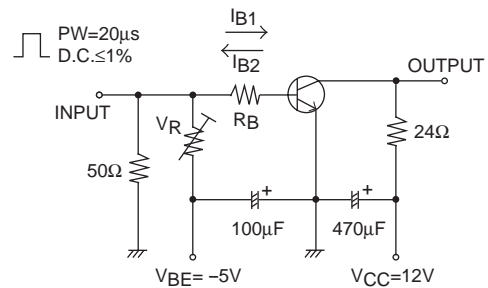
### Package Dimensions

unit : mm

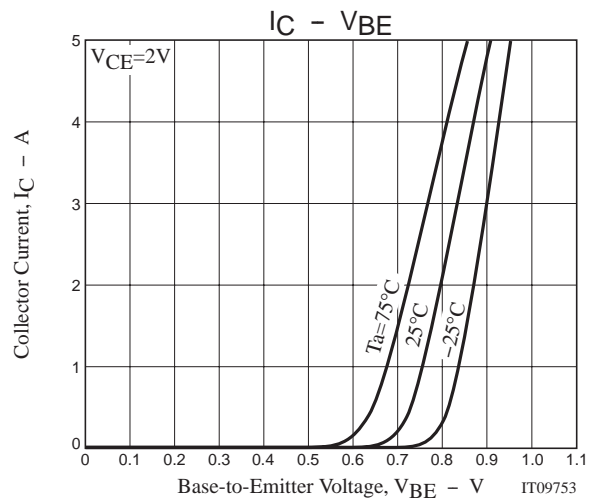
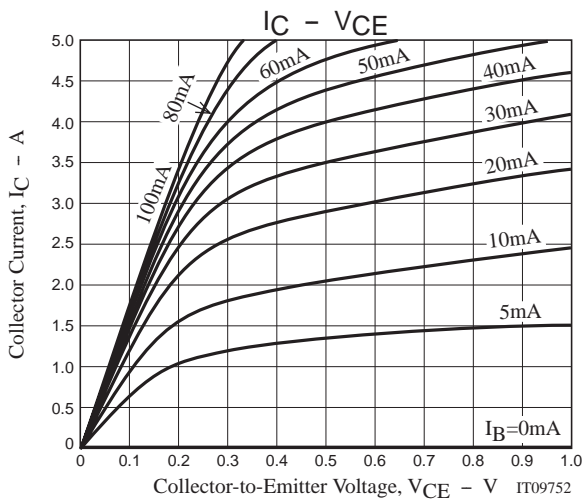
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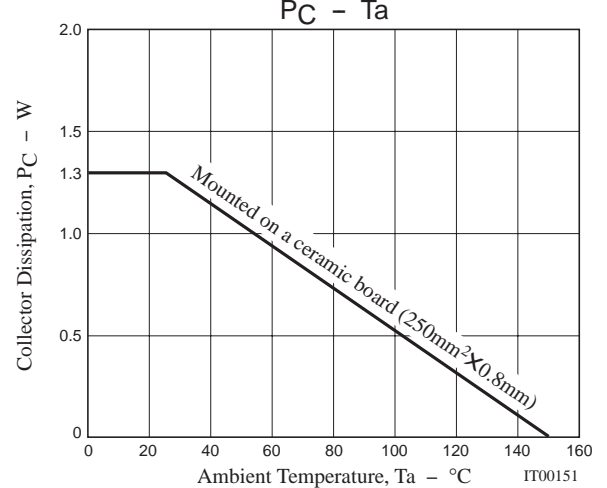
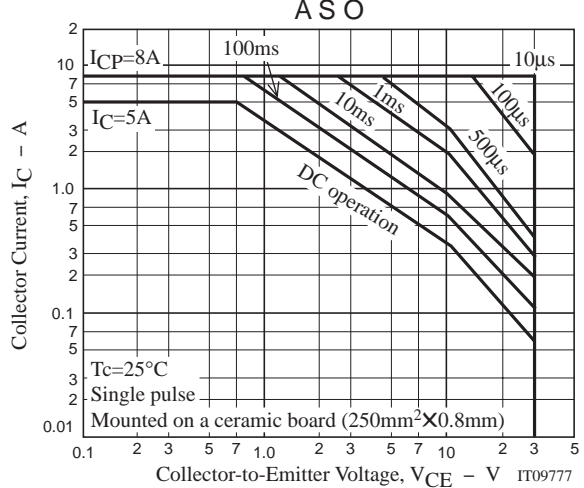
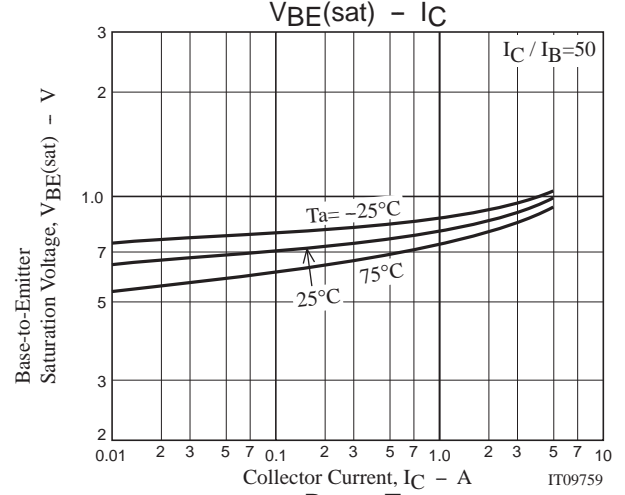
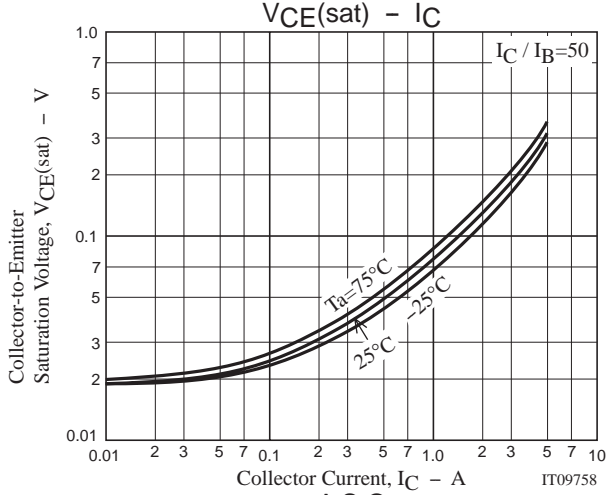
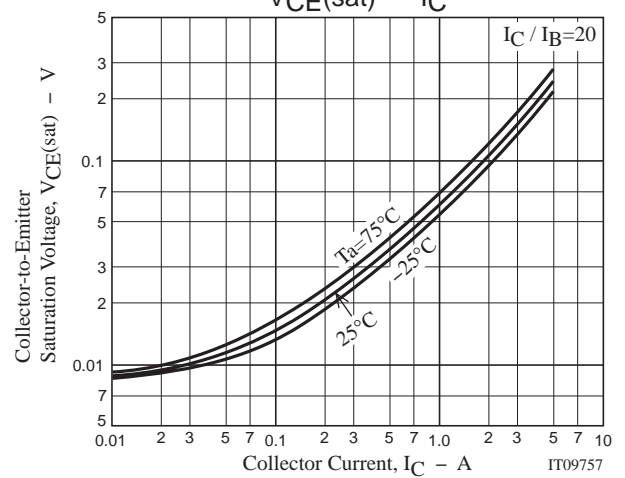
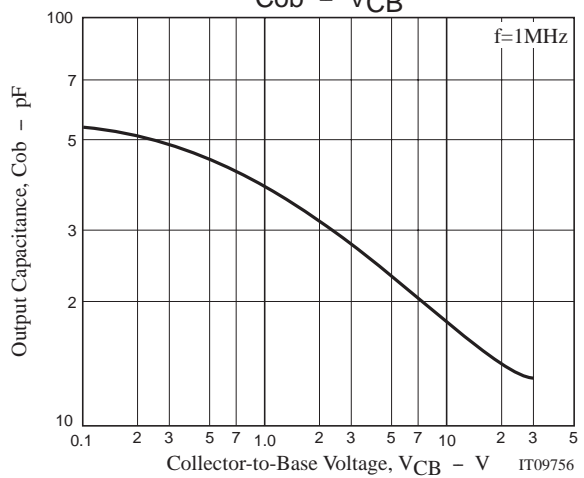
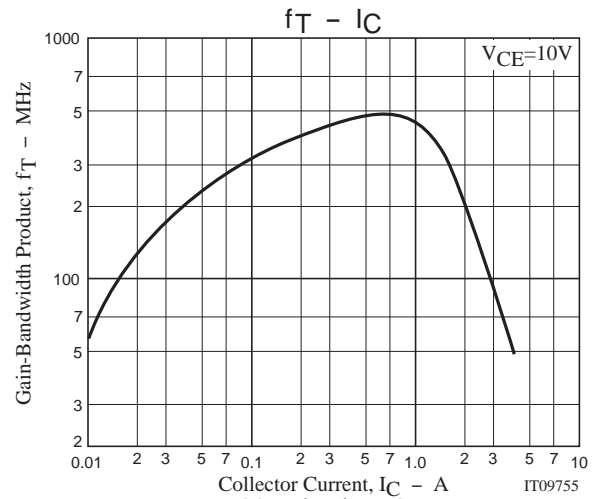
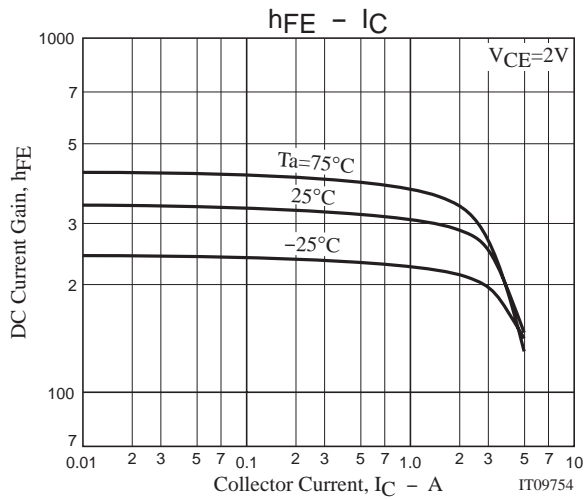


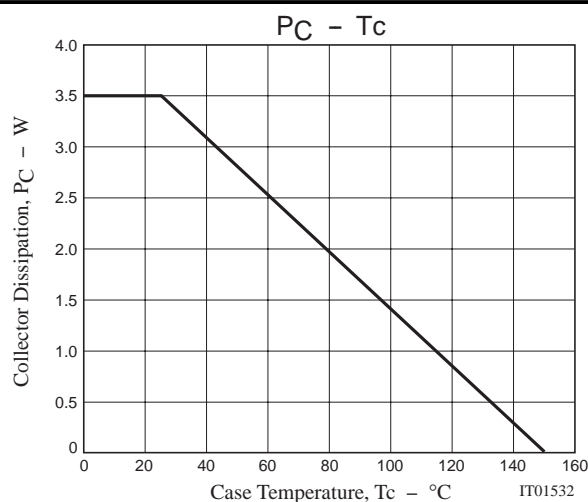
### Switching Time Test Circuit



$$I_C = 20I_{B1} = -20I_{B2} = 500mA$$







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