

PROTECTION PRODUCTS - MicroClamp™

Description

The μ Clamp™ series of Transient Voltage Suppressors (TVS) are designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and PDA's. They offer superior electrical characteristics such as lower clamping voltage and no device degradation when compared to MLVs. They are designed to protect sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD), lightning, electrical fast transients (EFT), and cable discharge events (CDE).

The μ Clamp™3301D is constructed using Semtech's proprietary EPD process technology. The EPD process provides low standoff voltages with significant reductions in leakage currents and capacitance over silicon-avalanche diode processes. They feature a true operating voltage of 3.3 volts for superior protection when compared to traditional pn junction devices.

The μ Clamp3301D is in a SOD-323 package and will protect one unidirectional line. They give the designer the flexibility to protect one line in applications where arrays are not practical.

They may be used to meet the ESD immunity requirements of IEC 61000-4-2, Level 4 (15kV air, 8kV contact discharge).

Features

- ◆ 200 Watts peak pulse power ($t_p = 8/20\mu s$)
- ◆ Transient protection for data lines to
IEC 61000-4-2 (ESD) 15kV (air), 8kV (contact)
IEC 61000-4-4 (EFT) 40A ($t_p = 5/50ns$)
IEC 61000-4-5 (Lightning) 12A ($t_p = 8/20\mu s$)
- ◆ Small package for use in portable electronics
- ◆ Suitable replacement for MLV's in ESD protection applications
- ◆ Protects one line
- ◆ Low clamping voltage
- ◆ Working voltages: **3.3V**
- ◆ Low leakage current
- ◆ Solid-state silicon-avalanche technology

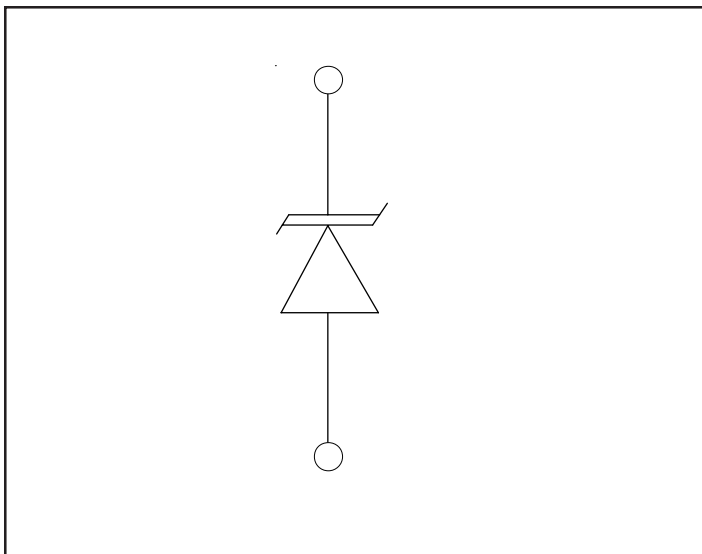
Mechanical Characteristics

- ◆ EIAJ SOD-323 package
- ◆ Molding compound flammability rating: UL 94V-0
- ◆ Marking: Marking code, cathode band
- ◆ Packaging: Tape and Reel per EIA 481
- ◆ Lead Finish: Matte tin

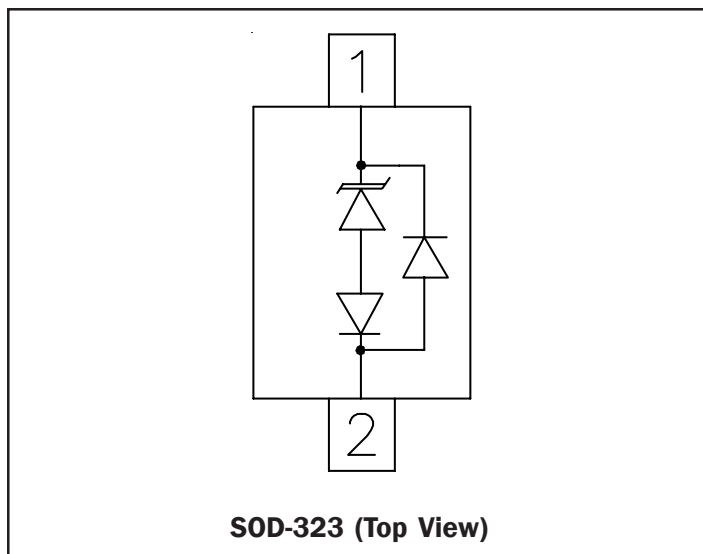
Applications

- ◆ Cell Phone Handsets and Accessories
- ◆ Laser Diode Protection
- ◆ Personal Digital Assistants (PDA's)
- ◆ Notebooks, Desktops, & Servers
- ◆ Portable Instrumentation
- ◆ Analog Inputs

Equivalent Circuit Diagram



Schematic & PIN Configuration



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Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power ($t_p = 8/20\mu s$)	P_{pk}	200	Watts
Peak Pulse Current ($t_p = 8/20\mu s$)	I_{pp}	12	A
ESD Voltage (HBM Waveform per IEC 61000-4-2)	V_{pp}	30	kV
Operating Temperature	T_J	-55 to +125	°C
Storage Temperature	T_{STG}	-55 to +150	°C

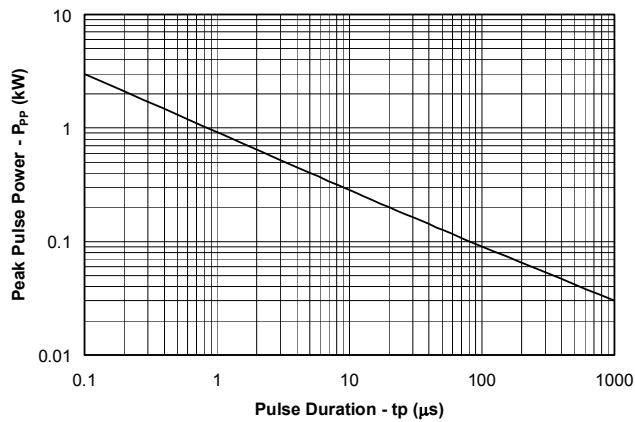
Electrical Characteristics

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V_{RWM}				3.3	V
Punch-Through Voltage	V_{PT}	$I_{PT} = 2\mu A$	3.5			V
Snap-Back Voltage	V_{SB}	$I_{SB} = 50mA$	2.8			V
Reverse Leakage Current	I_R	$V_{RWM} = 3.3V, T=25^\circ C$			0.5	μA
Clamping Voltage	V_C	$I_{pp} = 1A, t_p = 8/20\mu s$ Pin 1 to 2			4.5	V
Clamping Voltage	V_C	$I_{pp} = 5A, t_p = 8/20\mu s$ Pin 1 to 2			6.8	V
Clamping Voltage	V_C	$I_{pp} = 12A, t_p = 8/20\mu s$ Pin 1 to 2			9.5	V
Steering Diode Forward Voltage (Reverse Clamping Voltage)	V_F	$I_{pp} = 1A, t_p = 8/20\mu s$ Pin 2 to 1			1.7	V
Junction Capacitance	C_J	$V_R = 0V, f = 1MHz$		30	40	pF

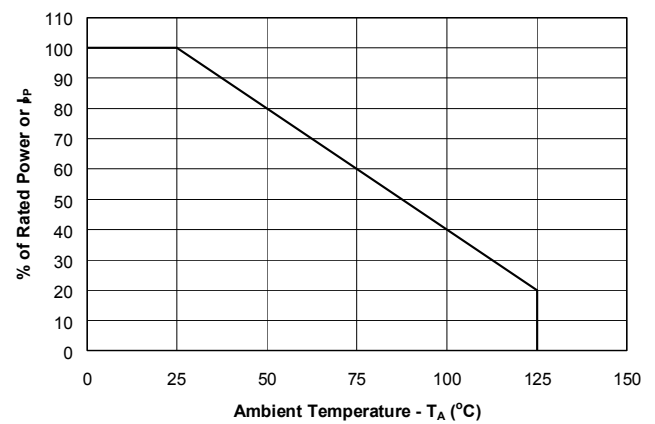
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Typical Characteristics

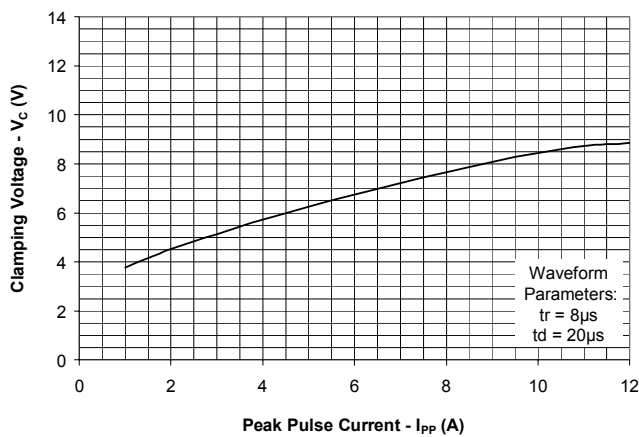
Non-Repetitive Peak Pulse Power vs. Pulse Time



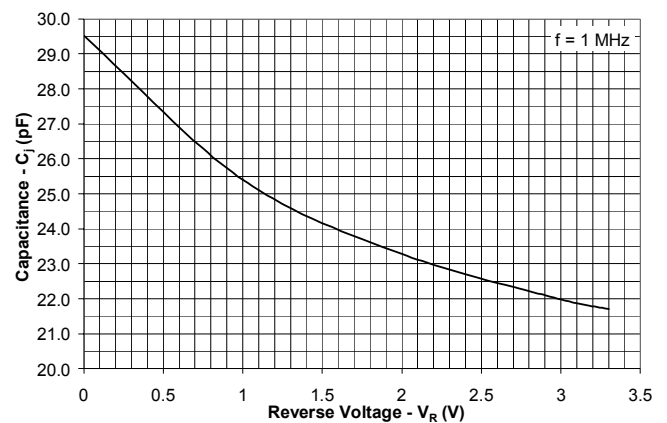
Power Derating Curve



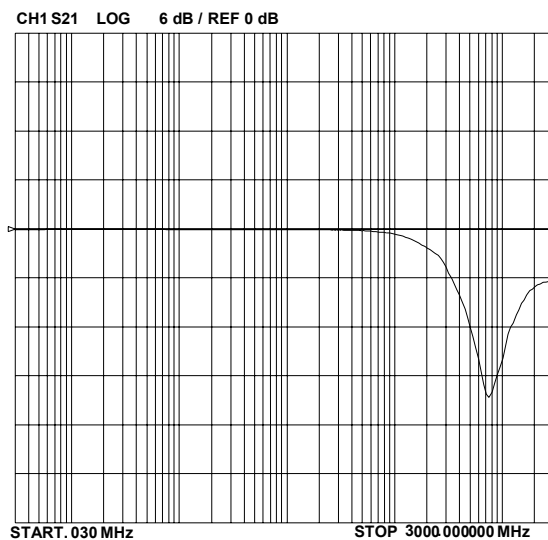
Clamping Voltage vs. Peak Pulse Current



Capacitance vs. Reverse Voltage



Insertion Loss S21



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Applications Information

Device Connection Options

The μ Clamp3301D is designed to protect one I/O, or power supply line. It will present a high impedance to the protected line up to 3.3 volts. It will “turn on” when the line voltage exceeds 3.5 volts. The device is unidirectional and may be used on lines where the signal polarity is above ground. The cathode band should be placed towards the line that is to be protected.

EPD TVS Characteristics

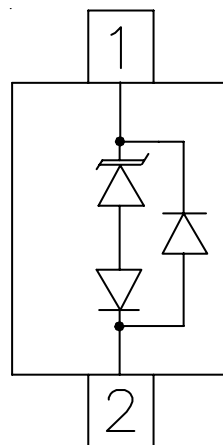
These devices are constructed using Semtech’s proprietary EPD technology. The structure of the EPD TVS is vastly different from the traditional pn-junction devices. At voltages below 5V, high leakage current and junction capacitance render conventional avalanche technology impractical for most applications. However, by utilizing the EPD technology, these devices can effectively operate at 3.3V while maintaining excellent electrical characteristics.

The EPD TVS employs a complex npnp structure in contrast to the pn structure normally found in traditional silicon-avalanche TVS diodes. The EPD mechanism is achieved by engineering the center region of the device such that the reverse biased junction does not avalanche, but will “punch-through” to a conducting state. This structure results in a device with superior dc electrical parameters at low voltages while maintaining the capability to absorb high transient currents.

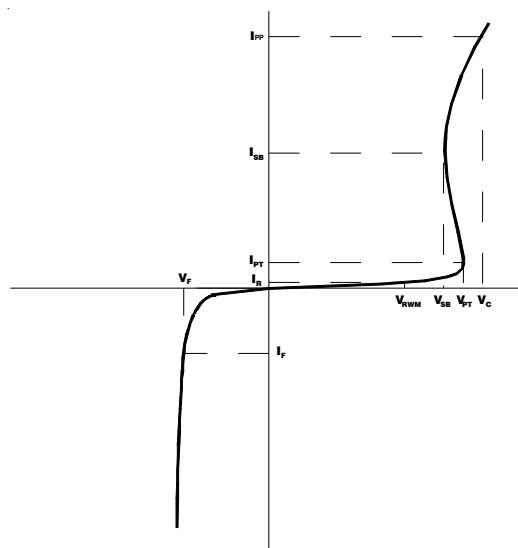
Matte Tin Lead Finish

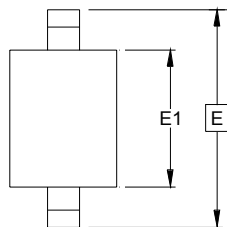
Matte tin has become the industry standard lead-free replacement for SnPb lead finishes. A matte tin finish is composed of 100% tin solder with large grains. Since the solder volume on the leads is small compared to the solder paste volume that is placed on the land pattern of the PCB, the reflow profile will be determined by the requirements of the solder paste. Therefore, these devices are compatible with both lead-free and SnPb assembly techniques. In addition, unlike other lead-free compositions, matte tin does not have any added alloys that can cause degradation of the solder joint.

Device Schematic & Pin Configuration

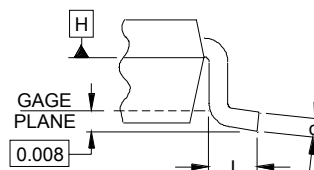
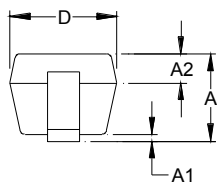
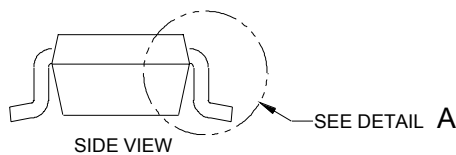


EPD TVS IV Characteristic Curve

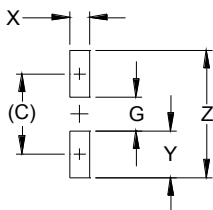


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Outline Drawing - SOD-323


DIM	INCHES			MILLIMETERS		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.36	-	.046	0.91	-	1.17
A1	.000	-	.004	0.00	-	0.10
A2	.012	-	.016	0.30	-	0.40
b	.013	-	.017	0.33	-	0.43
c	.005	-	.008	0.13	-	0.20
D	.044	.050	.054	1.18	1.28	1.37
E1	.060	.065	.070	1.50	1.64	1.78
E	.097	.102	.107	2.46	2.59	2.72
L	.010	.014	.018	0.25	0.35	0.45
N	2			2		


DETAIL A

NOTES:

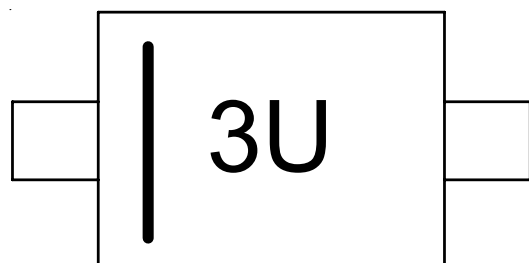
1. CONTROLLING DIMENSIONS ARE IN INCHES (ANGLES IN DEGREES).
2. DIMENSIONS "E1" AND "D" DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

Land Pattern - SOD-323


DIM	DIMENSIONS	
	INCHES	MILLIMETERS
C	(.085)	(2.15)
G	.035	0.90
X	.021	0.53
Y	.049	1.25
Z	.134	3.40

NOTES:

1. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

PROTECTION PRODUCTS**Marking Code****Ordering Information**

Part Number	Working Voltage	Qty per Reel	Reel Size
uClamp3301D.TCT	3.3V	3,000	7"

MicroClamp, uClamp, and μ Clamp are marks of Semtech Corporation

Contact Information

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