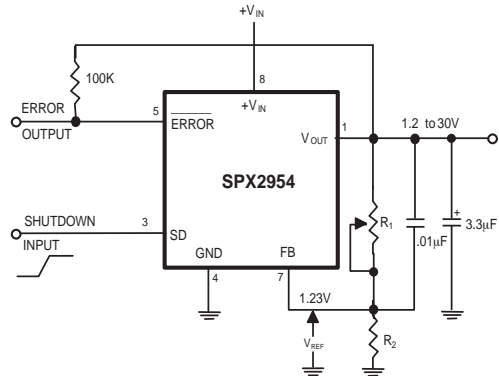


250mA Low Dropout Voltage Regulator

FEATURES

- 5.0V and 3.3V Versions at 250mA Output
- Accurate 0.5% for SPX2954A
- Very Low Quiescent Current
- Low Dropout: 310mV at 250mA
- Extremely Tight Load and Line Regulation
- Very Low Temperature Coefficient
- Current and Thermal Limiting
- Need only 1 μ F for Stability
- Direct Replacement for LP2954
- Offered in TO-92, TO-263, SOT-223, TO-220 and SOIC
- 8 Pin Versions Only**
 - Error Flag Warns of Output Dropout
 - Logic-Controlled Electronic Shutdown
 - Programmable Output from 1.24V to 30V

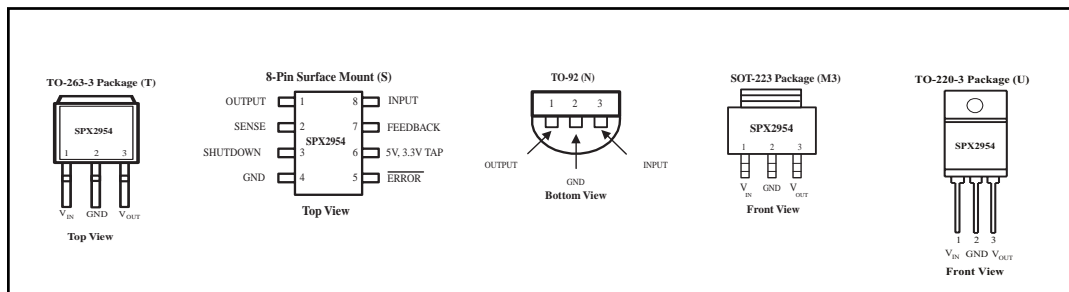


Adjustable Regulator

DESCRIPTION

The SPX2954 and SPX2954A are low power voltage regulators. These devices are an excellent choice for use in battery-powered applications such as cordless telephones, radio control systems, and portable computers. The SPX2954 and SPX2954A feature low quiescent current and low dropout voltage (typ. 20mV at 100 μ A and 310mV at 250mA). This includes a tight initial tolerance (0.5% for SPX2954A), extremely good load and line regulation (0.05% typ.), and very low output temperature coefficient (20 ppm/ $^{\circ}$ C typ.), making the SPX2954/SPX2954A useful as a low-power voltage reference.

The error flag output feature is used as a power-on reset for warning of a low output voltage, due to a falling input voltage. The logic-compatible shutdown feature enables the regulator to be switched ON and OFF. The SPX2954/SPX2954A is offered in 3-pin TO-92, TO-263, SOT-223 and TO-220 packages, and 8 pin SOIC package.



Pin Connections

ABSOLUTE MAXIMUM RATINGS

Power Dissipation.....Internally Limited
 Lead Temperature (soldering, 5 seconds)260°C
 Storage Temperature Range.....-65°C to +150°C
 Operating Junction Temperature Range.....-40°C to +125°C
 Input Supply Voltage.....-0.3V to +30V

Feedback Input Voltage.....-1.5V to +30V
 Shutdown Input Voltage-0.3V to +30V
 Error Comparator Output.....-0.3V to +30V
 ESD Rating.....2kV Min

ELECTRICAL CHARACTERISTICS $V_{IN}=V_O + 1V$, $I_L=100\mu A$, $C_L=1\mu F$ (NOTE 2), $T_A = 25^\circ C$, unless otherwise specified.

		SPX2954A			SPX2954			
PARAMETER	CONDITIONS	MIN.	TYP.	MAX	MIN.	TYP.	MAX.	UNIT
3.3 V Versions								
Output Voltage	$T_J=25^\circ C$	3.284	3.3	3.317	3.267	3.3	3.333	V
	$-25^\circ C \leq T_J \leq 85^\circ C$	3.267	3.3	3.333	3.251	3.3	3.350	
	Full Operating Temp.	3.260	3.3	3.340	3.234	3.3	3.366	
Output Voltage	$100\mu A \leq I_L \leq 250\text{ mA}$ $T_J \leq T_{JMAX}$	3.251	3.3	3.350	3.201	3.3	3.399	V
5 V Versions								
Output Voltage	$T_J = 25^\circ C$	4.975	5.0	5.025	4.950	5.0	5.050	V
	$-25^\circ C \leq T_J \leq 85^\circ C$	4.950	5.0	5.050	4.925	5.0	5.075	
	Full Operating Temp.	4.940	5.0	5.060	4.900	5.0	5.100	
Output Voltage	$100\mu A \leq I_L \leq 250\text{ mA}$ $T_J \leq T_{JMAX}$	4.925	5.0	5.075	4.850	5.0	5.150	V
All Voltage Options								
Output Voltage Temperature Coefficient	(NOTE 1)		20	100		50	150	ppm/°C
Line Regulation (NOTE 3)	$V_O + 1V \leq V_{IN} \leq 30V$ (NOTE 4)		0.03	0.10		0.04	0.20	%
Load Regulation (NOTE 3)	$100\mu A \leq I_L \leq 250\text{ mA}$		0.04	0.20		0.10	0.30	%
Dropout Voltage (NOTE 5)	$I_L = 1\text{ mA}$		60	100		60	100	mV
	$I_L = 100\text{ mA}$		290	450		290	450	
	$I_L = 250\text{ mA}$		310	500		310	500	
Ground Current	$I_L = 1\text{ mA}$		150	170		150	170	μA
	$I_L = 100\text{ mA}$		3	6		3	6	mA
	$I_L = 250\text{ mA}$		10	14		10	14	mA
Current Limit	$V_{OUT} = 0$		270	550		270	550	mA
Thermal Regulation			0.05	0.2		0.05	0.2	%/W
Output Noise, 10Hz to 100kHz	$C_L = 1\mu F$		430			430		μV rms
	$C_L = 200\mu F$		160			160		μV rms
	$C_L = 3.3\mu F$, Bypass= 0.01 μF from Pin 7 to Pin 1 (8 pin Versions)		100			100		μV rms
8-Pin Versions only								
Reference Voltage		1.220	1.235	1.250	1.210	1.235	1.260	V
Reference Voltage	Over Temp (NOTE 6)	1.190		1.270	1.185		1.285	V
Feedback Pin Bias Current			40	60		40	60	nA
Vref Temperature Coefficient			20			50		ppm/°C
Feedback I_{BIAS} Temperature Coefficient			0.1			0.1		nA/°C

ELECTRICAL CHARACTERISTICS (Cont.) $V_{IN}=V_O+1V$, $I_L=100\mu A$, $C_L=1\mu F$ (NOTE 2), $T_A = 25^\circ C$, unless otherwise specified.

		SPX2954A			SPX2954			
PARAMETER	CONDITIONS	MIN.	TYP.	MAX	MIN.	TYP.	MAX.	UNIT
Error Comparator								
Output Leakage Current	V _{OH} = 30V		0.01	1		0.01	1	μA
Output Low Voltage	V _{IN} = V _O -0.5V, I _{OL} = 400μA		150	250		150	250	mV
Upper Threshold Voltage	(NOTE 7)	40	60		40	60		mV
Lower Threshold Voltage	(NOTE 7)		75	95		75	95	mV
Hysteresis	(NOTE 7)		15			15		mV
Shutdown								
Input logic Voltage	Low (Regulator ON) High (Regulator OFF)	2.0	1.3	0.7	2.0	1.3	0.7	V
Shutdown Input Current	V _S = 2.4V V _S = 30V		30 400	50 800		30 400	50 800	μA
Regulator Output Current in Shutdown	(NOTE 8)		3	10		3	10	μA

NOTE 1: Output or reference voltage temperature coefficients defined as the worst case voltage change divided by the total temperature range.

NOTE 2: Unless otherwise specified all limits guaranteed for $T_J = 25^\circ C$, $V_{IN} = 6V$, $I_L = 100\mu A$ and $C_L = 1\mu F$. Additional conditions for the 8-pin versions are feedback tied to 5V tap and output tied to output sense ($V_{OUT} = 5V$) and $V_{SHUTDOWN} \leq 0.8V$.

NOTE 3: Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specification for thermal regulation.

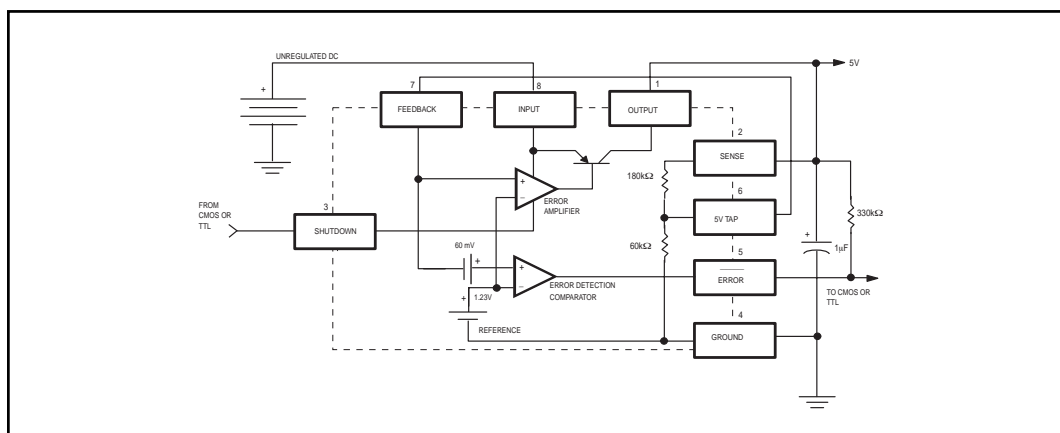
NOTE 4: Line regulation for the SPX2954 is tested at $I_L = 1mA$.

NOTE 5: Dropout voltage is defined as the input to output differential at which the output voltage drops 100mV below its nominal value measured at 1V differential. At very low values of programmed output voltage, the minimum input supply voltage of 2V (2.3V over temperature) must be taken into account.

NOTE 6: $V_{REF} \leq V_{OUT} \leq (V_{IN} - 1V)$, $2.3 \leq V_{IN} \leq 30V$, $100\mu A \leq I_L \leq 250mA$, $T_J \leq T_{JMAX}$.

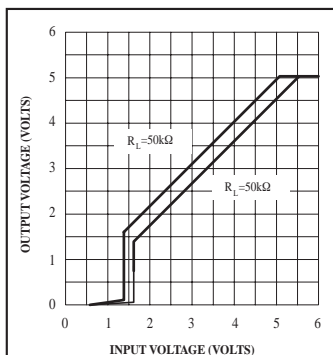
NOTE 7: Comparator thresholds are expressed in terms of a voltage differential at the feedback terminal below the nominal reference voltage measured at 6V input. To express these thresholds in terms of output voltage change, multiply by the error amplifier gain $= V_{OUT}/V_{REF} = (R_1 + R_2)/R_2$. For example, at a programmed output voltage of 5V, the error output is guaranteed to go low when the output drops by 95 mV $\times 5V/1.235 = 384$ mV. Thresholds remain constant as a percent of V_{OUT} as V_{OUT} is varied, with the dropout warning occurring at typically 5% below nominal, 7.5% guaranteed.

NOTE 8: $V_{SHUTDOWN} \geq 2V$, $V_{IN} \leq 30V$, $V_{OUT} = 0$, Feedback pin tied to 5V/3.3V Tap.

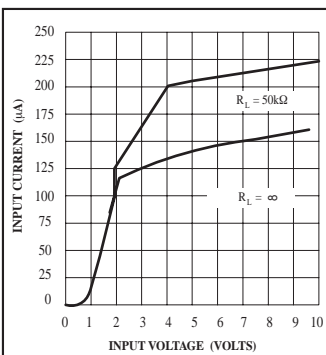


SPX2954 Block Diagram, 8 Lead 5V Version

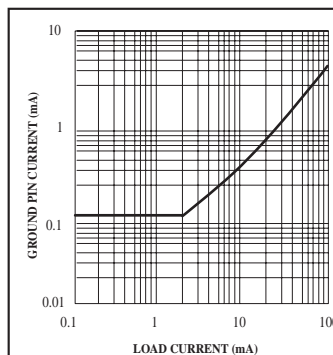
TYPICAL PERFORMANCE CHARACTERISTICS



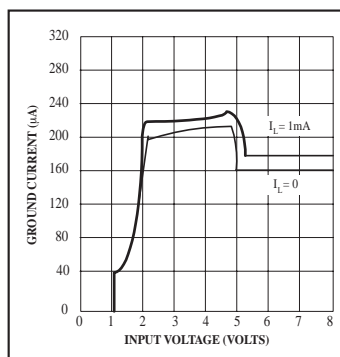
Dropout Characteristics



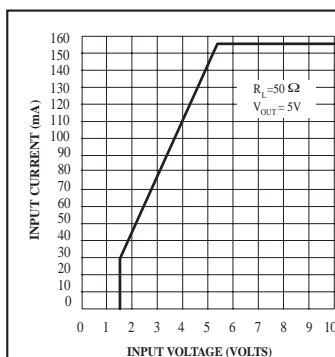
Input Current



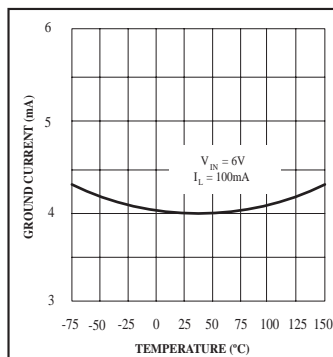
Quiescent Current



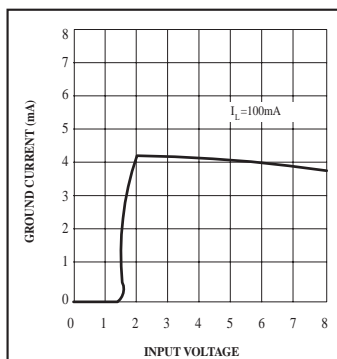
Ground Current



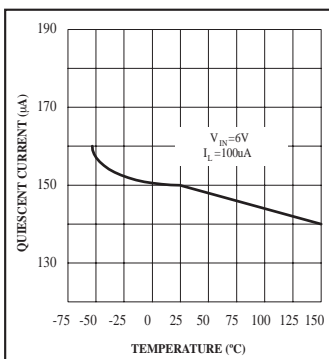
Input Current



Ground Current

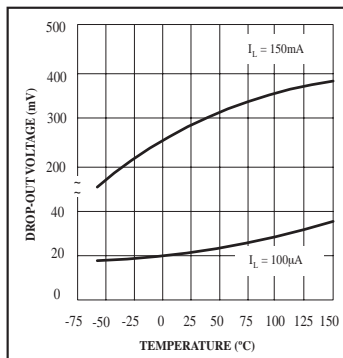


Ground Current

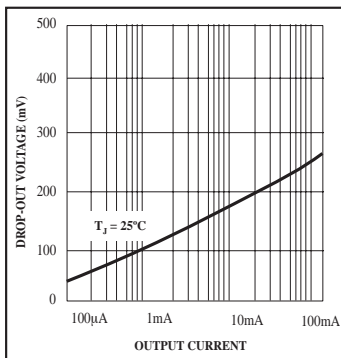


Quiescent Current

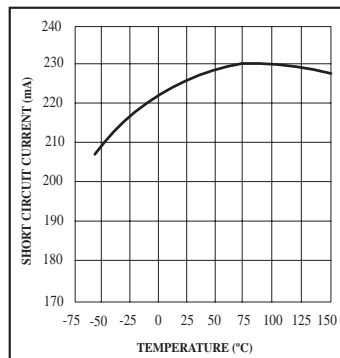
TYPICAL PERFORMANCE CHARACTERISTICS (CONT.)



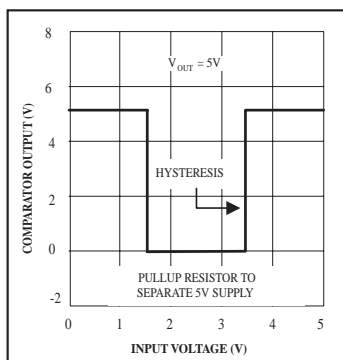
Drop-Out Voltage



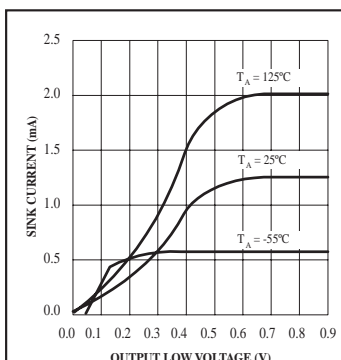
Drop-Out Voltage



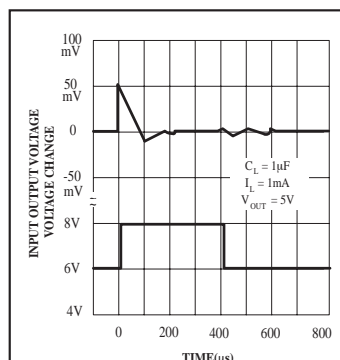
Short Circuit Current



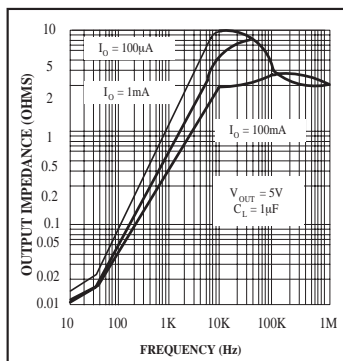
Error Comparator Output



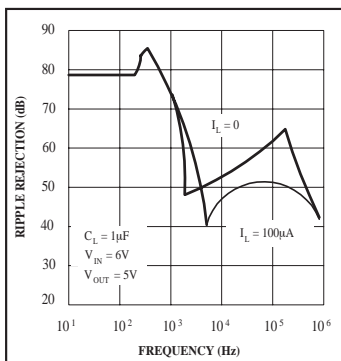
Comparator Sink Current



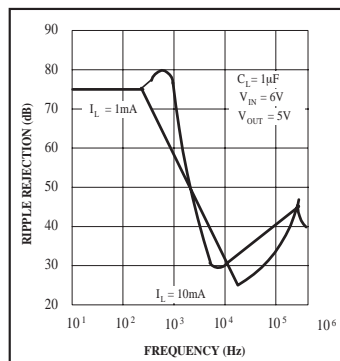
Line Transient Response



Output Impedance

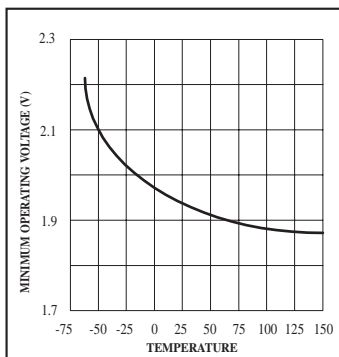


Ripple Rejection

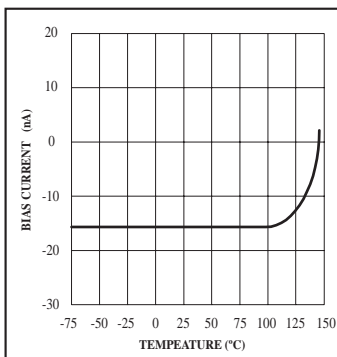


Ripple Rejection

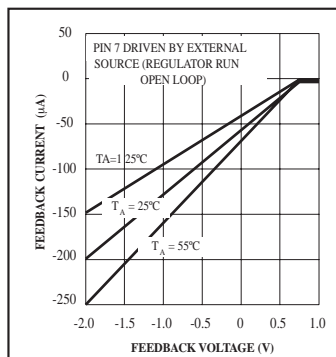
TYPICAL PERFORMANCE CHARACTERISTICS (CONT.)



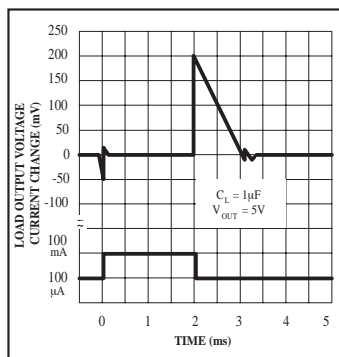
Minimum Operating Voltage



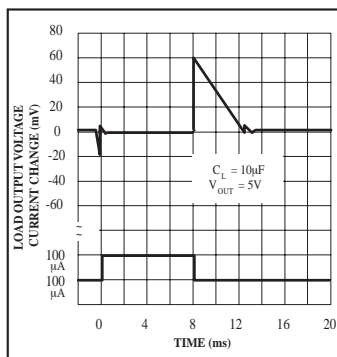
Feedback Bias Current



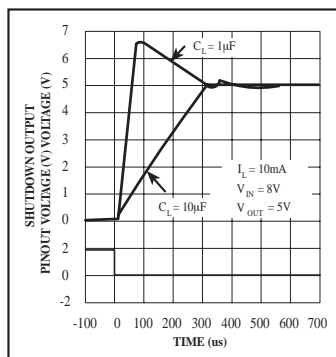
Feedback Pin Current



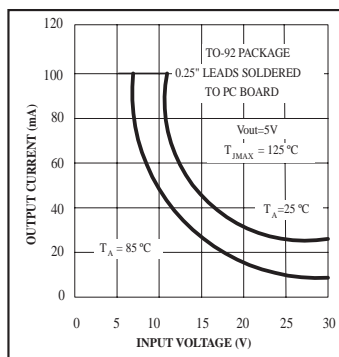
Load Transient Response



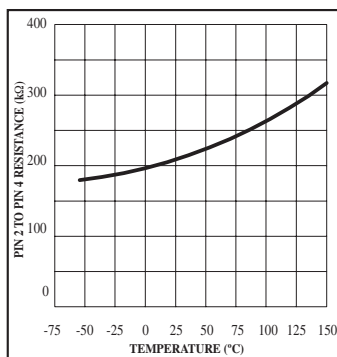
Load Transient Response



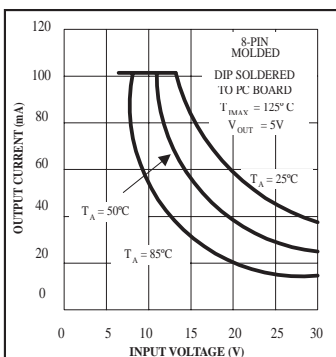
Enable Transient



Maximum Rated Output

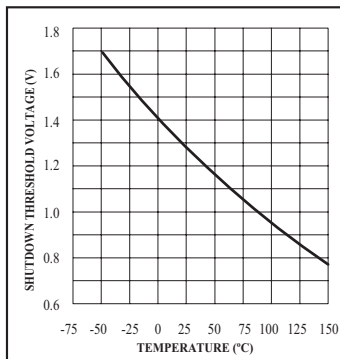


Divider Resistance

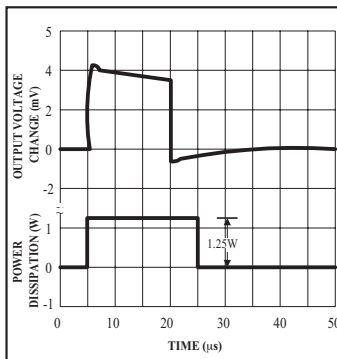


Maximum Rated Output Current

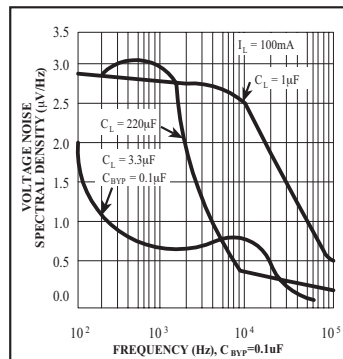
TYPICAL PERFORMANCE CHARACTERISTICS (CONT.)



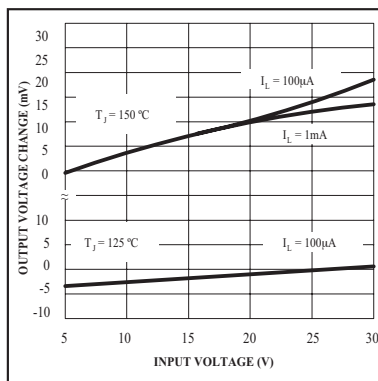
Shutdown Threshold Voltage



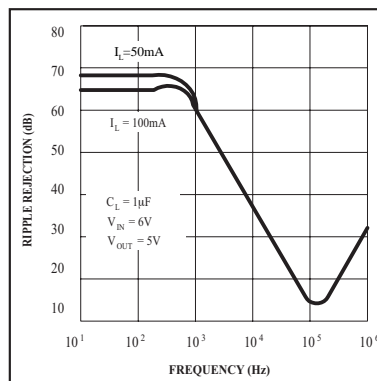
Thermal Response



Output Noise



Line Regulation



Ripple Rejection

APPLICATIONS HINTS

EXTERNAL CAPACITORS

The stability of the SPX2954 requires a 1 μ F or greater capacitor between output and ground. Oscillation could occur without this capacitor. Most types of tantalum or aluminum electrolytic works fine here. For operations below -25°C solid tantalum is recommended since the many aluminum types have electrolytes that freeze at about -30°C. The ESR of about 5 Ω or less and resonant frequency above 500kHz are the most important parameters in the value of the capacitor. The capacitors value may be increased without limit.

At lower values of output current, less output capacitance is required for stability. For the currents below 10mA the value of the capacitor can be reduced to 0.33 μ F and 0.1 μ F for 1mA. More output capacitance is needed for the 8-pin version at voltages below 5V since it runs the error amplifier at lower gain. At worst case 3.3 μ F or greater must be used for the condition of 150mA load at 1.23V output.

The SPX2954 unlike other low dropout regulators will remain stable and in regulation with no load in addition to the internal voltage divider. This feature is especially important in applications like CMOS RAM keep-alive. When setting the output voltage of the SPX2954 version with external resistors, a minimum load of 1 μ A is recommended

If there is more than 10 inches of wire between the input and the AC filter capacitor or if a battery is used as the input then a 1 μ F tantalum or aluminum electrolytic capacitor should be placed from the input to the ground.

Instability can occur if there is stray capacitance to the SPX2954 feedback terminal (pin 7). This could cause more problems when using a higher value of external resistors to set the output voltage. This problem can be fixed by adding a 100pF capacitor between output and feedback and increasing the output capacitor to at least 3.3 μ F.

ERROR DETECTION COMPARATOR OUTPUT

The comparator produces a logic low output whenever the SPX2954 output falls out of regulation by more than around 5%. This is around 60 mV offset divided by the 1.235 reference voltage. This trip level remains 5% below normal regardless of the programmed output voltage of the regulator.

Figure 1 shows the timing diagram depicting the ERROR signal and the regulator output voltage as the SPX2954 input is ramped up and down. The ERROR signal becomes low at around 1.3V input, and goes high around 5V input (input voltage at which $V_{OUT} = 4.75$). Since the SPX2954's dropout voltage is load dependent, the input voltage trip point (around 5V) will vary with the load current. The output voltage trip point (approx. 4.75V) does not vary with load.

The error comparator has an open-collector output, which requires an external pull-up resistor. Depending on the system requirements the resistor may be returned to 5V output or other supply voltage. In determining the value of this resistor, note that the output is rated to sink 400mA, this value adds to battery drain in a low battery condition. Suggested values range from 100k to 1M Ω . If the output is unused this resistor is not required.

PROGRAMMING THE OUTPUT VOLTAGE OF SPX2954

The SPX2954 may be pin-strapped for 5V using its internal voltage divider by tying Pin 1 (output) to Pin 2 (sense) and Pin 7 (feedback) to Pin 6 (5V Tap). Also, it may be programmed for any output voltage between its 1.235V reference and its 30V maximum rating using an external pair of resistors. Refer to the below equation for the programming of the output voltage:

$$V_{OUT} = V_{REF} \times (1 + R_1/R_2) + I_{FB} R_1$$

The V_{REF} is 1.235 and I_{FB} is the feedback bias current, nominally -20nA. The minimum recommended load current of 1 μ A forces an

APPLICATIONS HINTS (CONT)

upper limit of $1.2\text{M}\Omega$ on value of R_2 . If no load is presented the I_{FB} produces an error of typically 2% in V_{OUT} which may be eliminated at room temperature by trimming R_1 . To improve the accuracy choose the value of $R_2 = 100\text{k}$ this reduces the error by 0.17% and increases the resistor program current by $12\mu\text{A}$. Since the SPX2954 typically draws $60\mu\text{A}$ at no load with Pin 2 open-circuited, this is a small price to pay.

REDUCING OUTPUT NOISE

It may be an advantage to reduce the AC noise present at the output. One way is to reduce the regulator bandwidth by increasing the size of the output capacitor. This is the only way that noise can be reduced on the 3 lead SPX2954 but is relatively inefficient, as increasing the capacitor from $1\mu\text{F}$ to $220\mu\text{F}$ only decreases the noise from $430\mu\text{V}$ to $160\mu\text{VRMS}$ for a 100kHz bandwidth at 5V output. Noise can be reduced four-fold by using a bypass capacitor across R_1 , since it reduces the high frequency gain from 4 to unity. Pick

$$C_{\text{BYPASS}} \cong (1 / 2\pi R_1 \times 200 \text{ Hz})$$

or choose $0.01\mu\text{F}$. When doing this, the output capacitor must be increased to $3.3\mu\text{F}$ to maintain stability. These changes reduce the output noise from $430\mu\text{V}$ to $100\mu\text{VRMS}$ for a 100kHz bandwidth at 5V output. With the bypass capacitor added, noise no longer scales with output voltage so that improvements are more dramatic at higher output voltages.

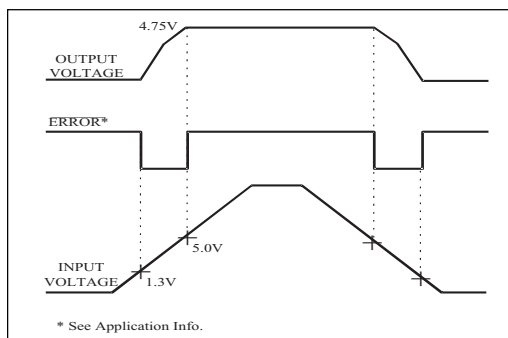


Figure 1. Error Output Timing

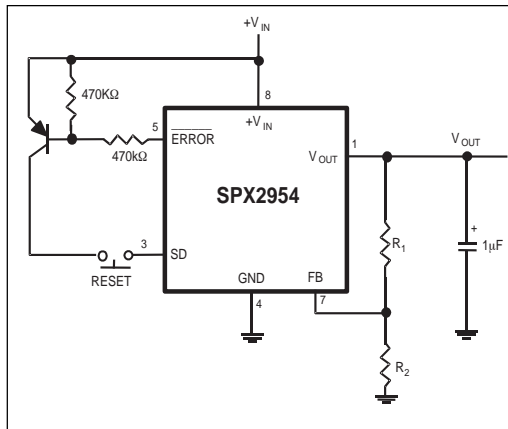


Figure 2. Latch Off when Error Flag Occurs

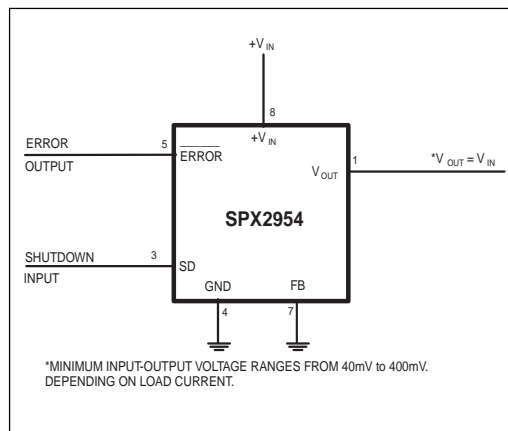


Figure 3. Wide Input Voltage Range, Current Limiter

The diagram shows a power management circuit. A 6V SEALED LEAD-ACID BATTERY SOURCE is connected to the +V_{IN} pin (pin 8) of the SPX2954. The SPX2954's GND pin (pin 4) is connected to ground. The SPX385's FB pin is connected to a voltage divider consisting of a 120kΩ resistor to +V_{IN} and a 100kΩ resistor to ground. The SPX385's output is connected to the base of a 1N457 diode. The diode's cathode is connected to the SPX2954's SENSE pin (pin 2). The SPX2954's V_{OUT} pin (pin 1) is connected to the anode of the 1N457 diode. The SPX2954's SD pin (pin 3) is connected to the emitter of a transistor. The transistor's base is connected to the junction of a 400kΩ resistor to +V_{IN} and a 100kΩ resistor to ground. The transistor's collector is connected to the anode of a diode that leads to the MAIN V+ output. The SPX2954's V_{OUT} pin (pin 1) is also connected to the anode of a diode that leads to the MEMORY V+ output. A 1μF capacitor is connected between the SENSE pin (pin 2) and ground. A 20kΩ resistor is connected between the MEMORY V+ output and ground. The MEMORY V+ output is also connected to an NI-CAD BACKUP BATTERY.

* OPTIONAL LATCH OFF WHEN DROPOUT OCCURS. ADJUST R3 FOR C2 SWITCHING WHEN V_{IN} IS 6.0V

* OUTPUTS GO LOW WHEN V_{IN} DROPS BELOW DESIGNATED THRESHHOLDS

10

APPLICATIONS HINTS

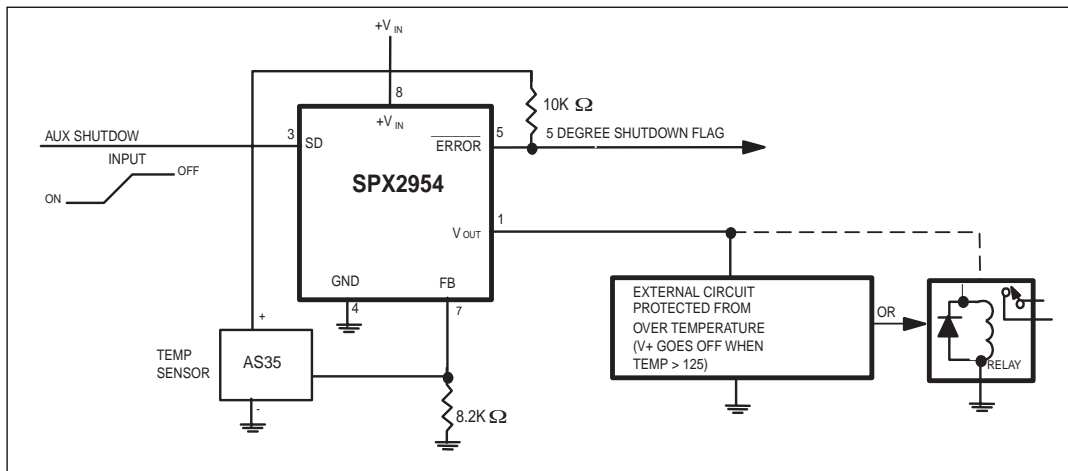


Figure 6. System Over Temperature Protection

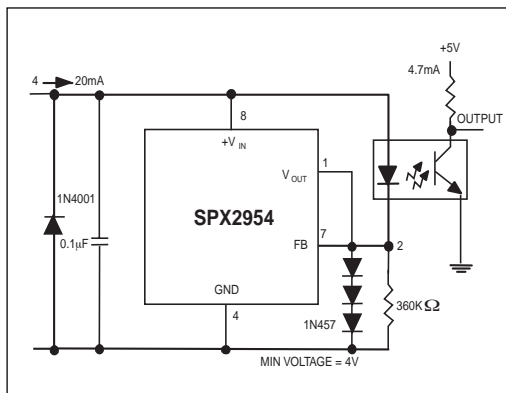


Figure 7. Open Circuit Detector for 4mA to 20mA Current Loop

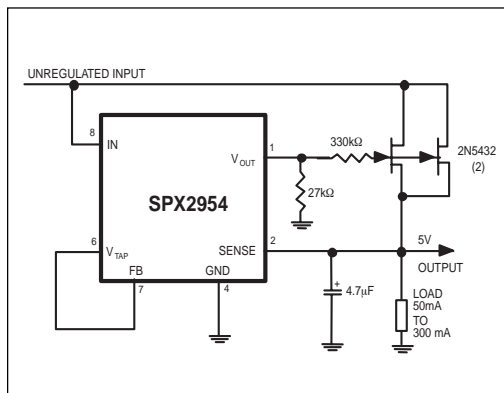
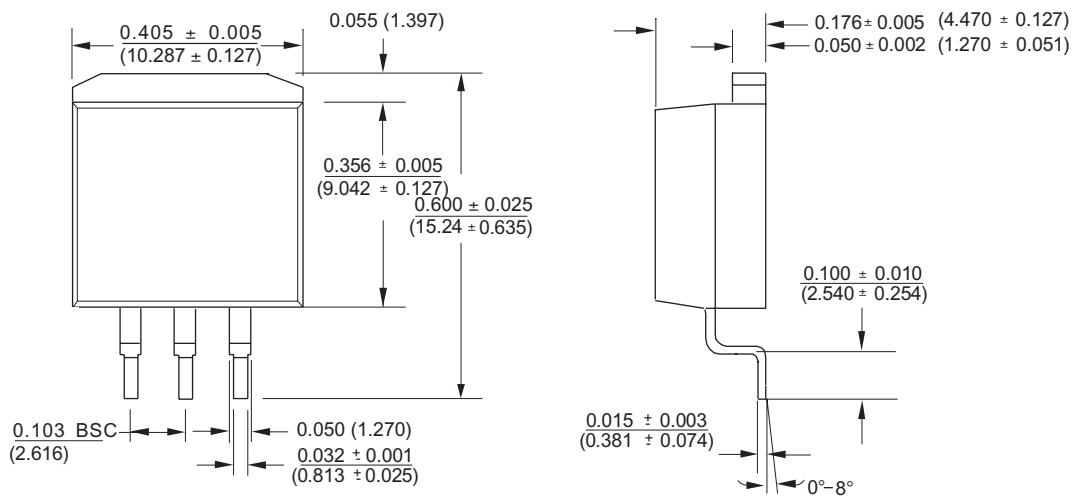


Figure 8. 300mA Regulator with 0.75V

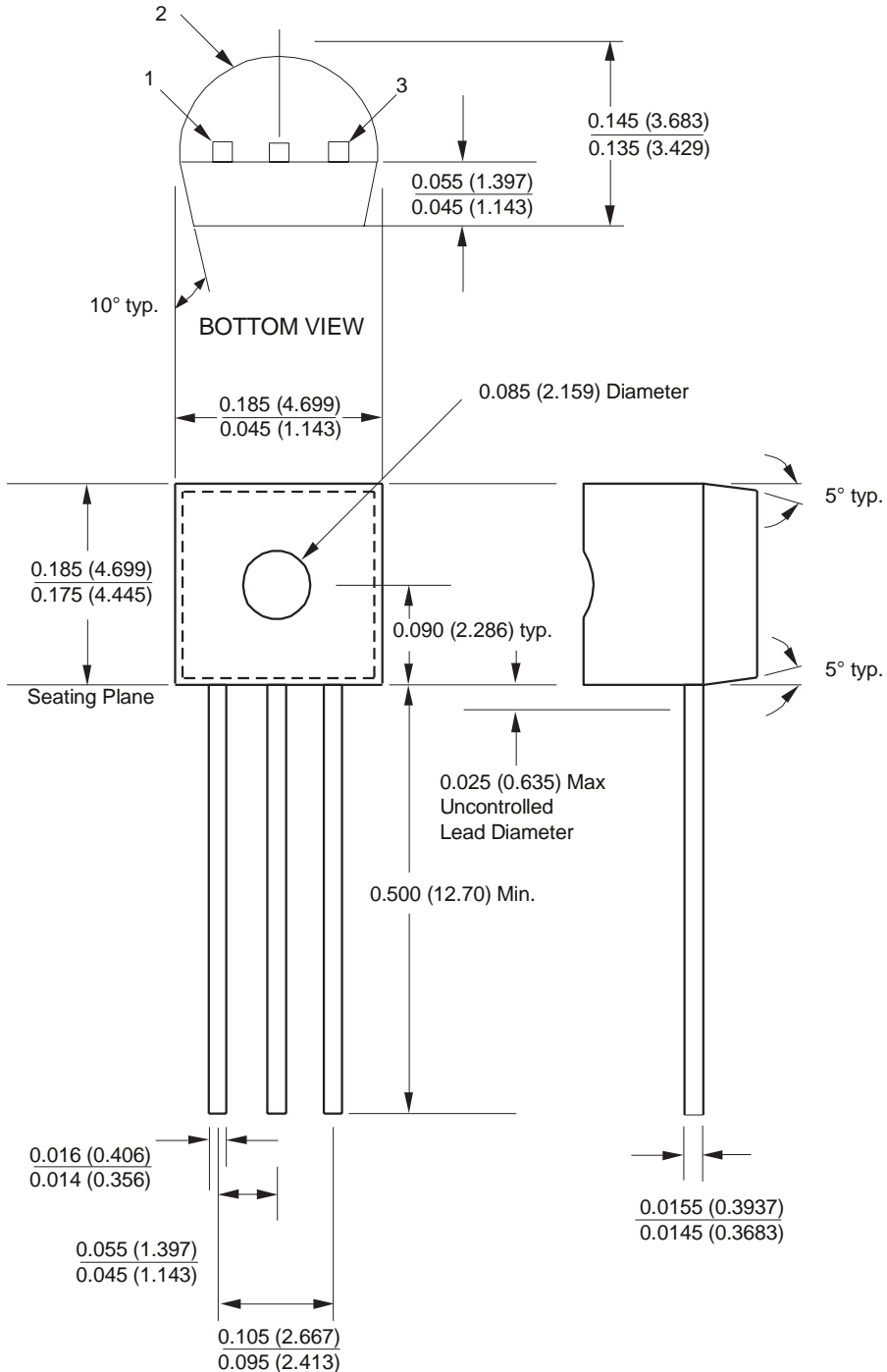
PACKAGES

PACKAGE: 3 Lead TO-263

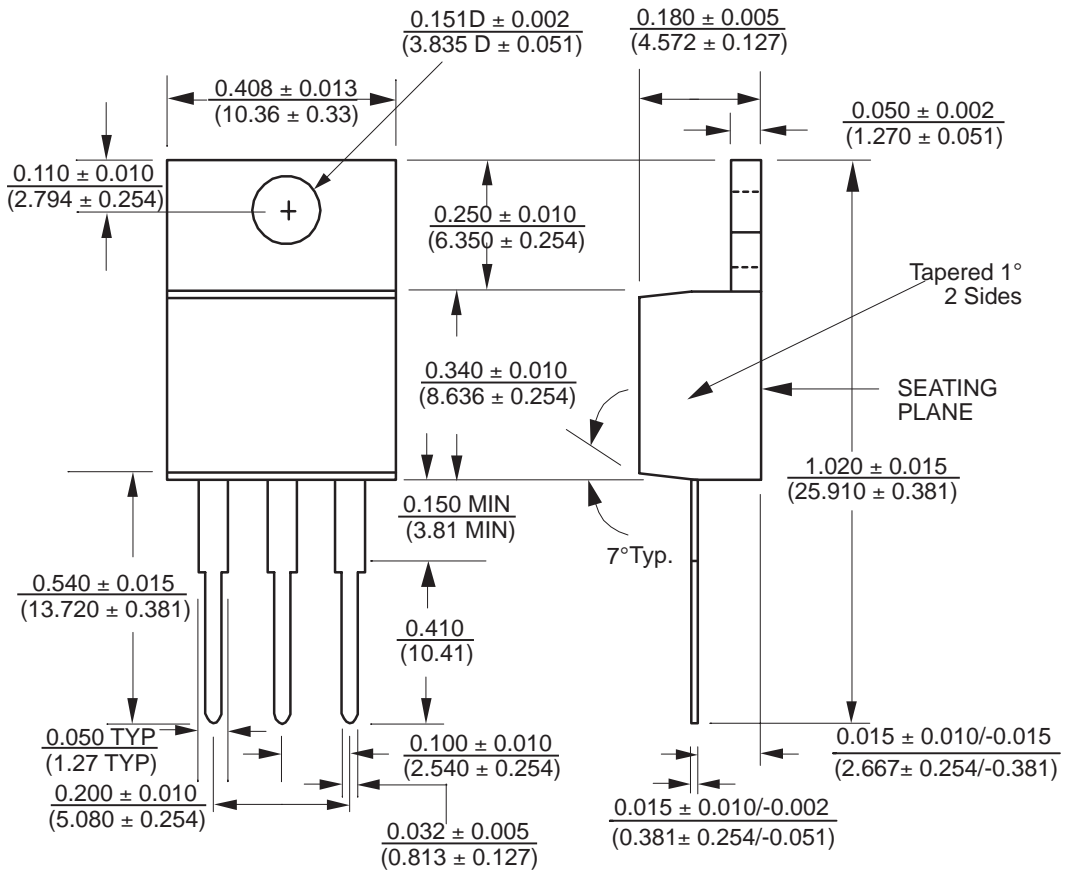


PACKAGE: TO-92

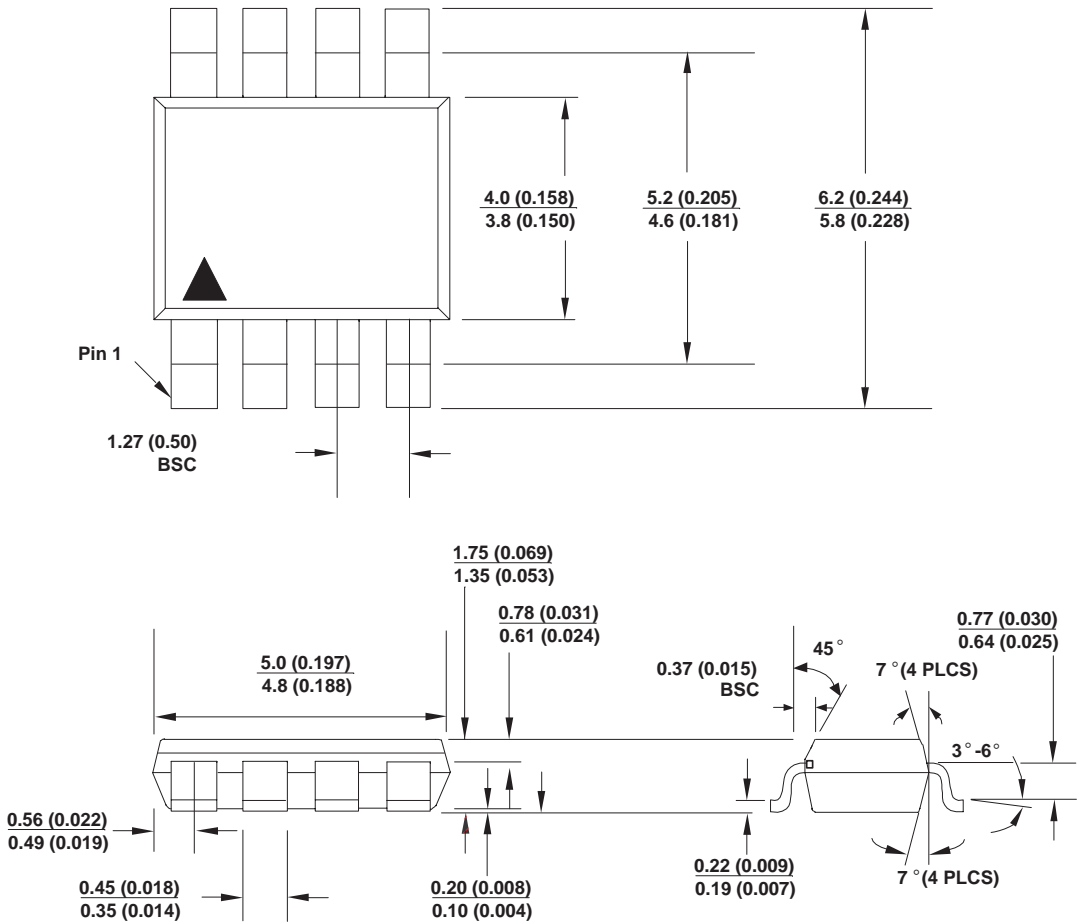
0.090 (2.286) Radius typ.



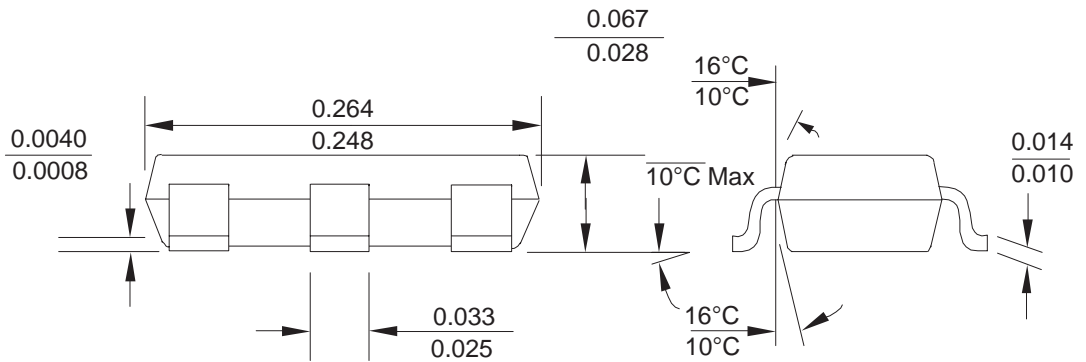
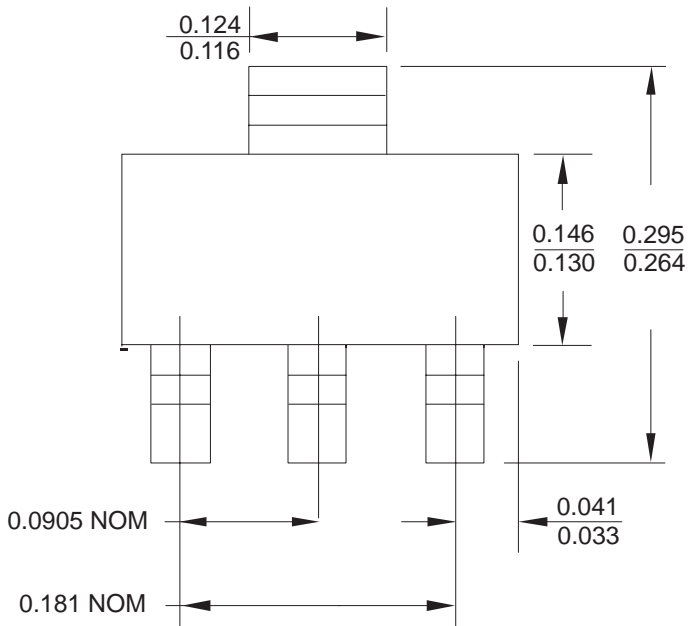
PACKAGE: 3 Lead TO-220



PACKAGE: 8 PIN PLASTIC SMALL OUTLINE (SOIC) (NARROW)



PACKAGE: 3 Lead SOT-223



ORDERING INFORMATION

PART NUMBER	ACCURACY	OUTPUT VOLTAGE	PACKAGE
SPX2954AT-3.3	0.5%	3.3V	3 lead TO-263
SPX2954AT-5.0	0.5%	5.0V	3 lead TO-263
SPX2954T-3.3	1%	3.3V	3 lead TO-263
SPX2954T-5.0	1%	5.0V	3 lead TO-263
SPX2954AN-3.3	0.5%	3.3V	3 lead TO-92
SPX2954AN-5.0	0.5%	5.0V	3 lead TO-92
SPX2954N-3.3	1%	3.3V	3 lead TO-92
SPX2954N-5.0	1%	5.0V	3 lead TO-92
SPX2954AU-3.3	0.5%	3.3V	3 lead TO-220
SPX2954AU-5.0	0.5%	5.0V	3 lead TO-220
SPX2954U-3.3	1%	3.3V	3 lead TO-220
SPX2954U-5.0	1%	5.0V	3 lead TO-220
SPX2954AS-3.3	0.5%	3.3V	8 lead SOIC
SPX2954AS-5.0	0.5%	5.0V	8 lead SOIC
SPX2954S-3.3	1%	3.3V	8 lead SOIC
SPX2954S-5.0	1%	5.0V	8 lead SOIC
SPX2954AM3-3.3	0.5%	3.3V	3 lead SOT-223
SPX2954AM3-5.0	0.5%	5.0V	3 lead SOT-223
SPX2954M3-3.3	1%	3.3V	3 lead SOT-223
SPX2954M3-5.0	1%	5.0V	3 lead SOT-223

Please consult the factory for pricing and availability on a Tape-On-Reel option.



SIGNAL PROCESSING EXCELLENCE

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