



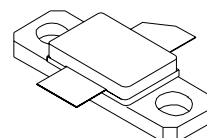
# LET20030C

## RF POWER TRANSISTORS *Ldmos Enhanced Technology*

### TARGET DATA

Designed for GSM / EDGE / IS-97 applications

- IS-97 CDMA PERFORMANCES  
P<sub>OUT</sub> = 4.5 W  
EFF. = 17 %
- EXCELLENT THERMAL STABILITY
- COMMON SOURCE CONFIGURATION
- P<sub>OUT</sub> = 30 W with 11 dB gain @ 2000 MHz
- ESD PROTECTION



**M243**  
epoxy sealed

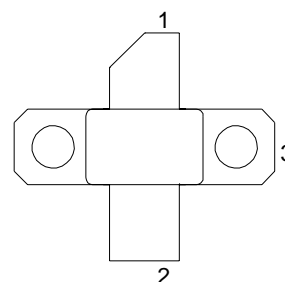
**ORDER CODE**  
LET20030C

**BRANDING**  
LET20030C

### DESCRIPTION

The LET20030C is a common source N-Channel enhancement-mode lateral Field-Effect RF power transistor designed for broadband commercial and industrial applications at frequencies up to 2.0 GHz. The LET20030C is designed for high gain and broadband performance operating in common source mode at 26 V. It is ideal for base station applications requiring high linearity.

### PIN CONNECTION



1. Drain  
2. Gate

3. Source

### ABSOLUTE MAXIMUM RATINGS (T<sub>case</sub> = 25 °C)

Symbol	Parameter	Value	Unit
V <sub>(BR)DSS</sub>	Drain-Source Voltage	65	V
V <sub>DGR</sub>	Drain-Gate Voltage (R <sub>GS</sub> = 1 MΩ)	65	V
V <sub>GS</sub>	Gate-Source Voltage	-0.5 to +15	V
I <sub>D</sub>	Drain Current	4	A
P <sub>DISS</sub>	Power Dissipation (@ T <sub>c</sub> = 70 °C)	65	W
T <sub>j</sub>	Max. Operating Junction Temperature	200	°C
T <sub>STG</sub>	Storage Temperature	-65 to +200	°C

### THERMAL DATA (T<sub>case</sub> = 70 °C)

R <sub>th(j-c)</sub>	Junction -Case Thermal Resistance	2.0	°C/W
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**ELECTRICAL SPECIFICATION** ( $T_{CASE} = 25^{\circ}C$ )

**STATIC**

Symbol	Test Conditions		Min.	Typ.	Max.	Unit
$V_{(BR)DSS}$	$V_{GS} = 0\text{ V}$	$I_{DS} = 1\text{ mA}$	65			V
$I_{DSS}$	$V_{GS} = 0\text{ V}$	$V_{DS} = 26\text{ V}$			1	$\mu\text{A}$
$I_{GSS}$	$V_{GS} = 5\text{ V}$	$V_{DS} = 0\text{ V}$			1	$\mu\text{A}$
$V_{GS(Q)}$	$V_{DS} = 26\text{ V}$	$I_D = \text{TBD}$	2.5		5.0	V
$V_{DS(ON)}$	$V_{GS} = 10\text{ V}$	$I_D = 1\text{ A}$		TBD		V
$G_{FS}$	$V_{DS} = 10\text{ V}$	$I_D = 1\text{ A}$		TBD		mho
$C_{ISS}$	$V_{GS} = 0\text{ V}$	$V_{DS} = 26\text{ V}$		TBD		pF
$C_{OSS}$	$V_{GS} = 0\text{ V}$	$V_{DS} = 26\text{ V}$		TBD		pF
$C_{RSS}$	$V_{GS} = 0\text{ V}$	$V_{DS} = 26\text{ V}$		TBD		pF

Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>DYNAMIC</b> ( <i>f</i> = 2000 MHz)					
P <sub>1dB</sub>	V <sub>DD</sub> = 26 V    I <sub>DQ</sub> = 200 mA	30			W
G <sub>P</sub>	V <sub>DD</sub> = 26 V    I <sub>DQ</sub> = 200 mA    P <sub>OUT</sub> = 30 W		11		dB
η <sub>D</sub>	V <sub>DD</sub> = 26 V    I <sub>DQ</sub> = 200 mA    P <sub>OUT</sub> = 30 W		52		%
IMD3 <sup>(1)</sup>	V <sub>DD</sub> = 26 V    I <sub>DQ</sub> = 200 mA    P <sub>OUT</sub> = 30 W PEP		-31	-28	dBc
Load mismatch	V <sub>DD</sub> = 26 V    I <sub>DQ</sub> = 200 mA    P <sub>OUT</sub> = 30 W ALL PHASE ANGLES	10:1			VSWR
<b>DYNAMIC</b> ( <i>f</i> = 1930 - 1990 MHz)					
P <sub>OUT</sub> <sup>(2)</sup>	V <sub>DD</sub> = 26 V    I <sub>DQ</sub> = TBD	25	30		W
G <sub>P</sub>	V <sub>DD</sub> = 26 V    I <sub>DQ</sub> = TBD    P <sub>OUT</sub> = 30 W	11			dB
η <sub>D</sub> <sup>(2)</sup>	V <sub>DD</sub> = 26 V    I <sub>DQ</sub> = TBD	40	45		%
P <sub>out(CDMA)</sub> <sup>(3)</sup>	885 KHz < -47 dBc 1.25 MHz < -55 dBc 2.25 MHz < -55 dBc		4.5		W
η <sub>D(CDMA)</sub> <sup>(3)</sup>	885 KHz < -47 dBc 1.25 MHz < -55 dBc 2.25 MHz < -55 dBc		17		%

(1)  $f_1 = 2000\text{ MHz}$ ,  $f_2 = 2000.1\text{ MHz}$

(2) 1 dB Compression point

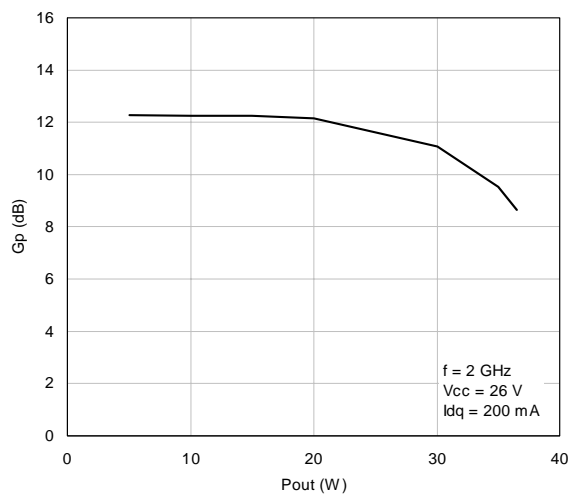
(3) IS-97 CDMA Pilot, Sync, Paging, Traffic, Codes 8 Thru 13

**ESD PROTECTION CHARACTERISTICS**

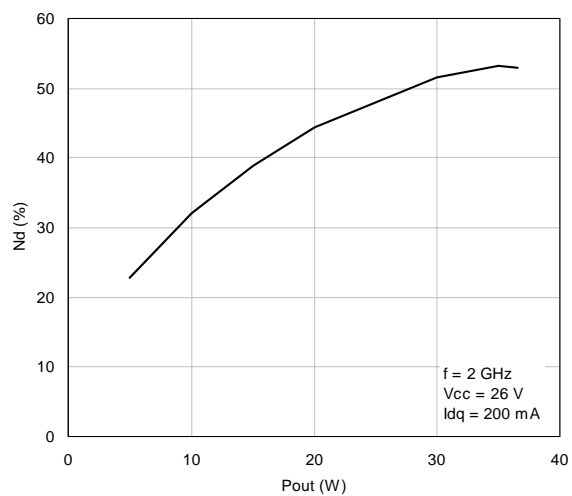
Test Conditions	Class
Human Body Model	2
Machine Model	M3

## TYPICAL PERFORMANCE

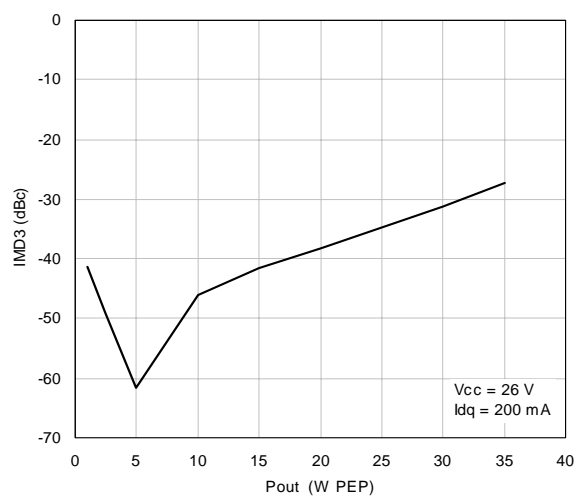
Power Gain vs. Output Power



Efficiency vs. Output Power

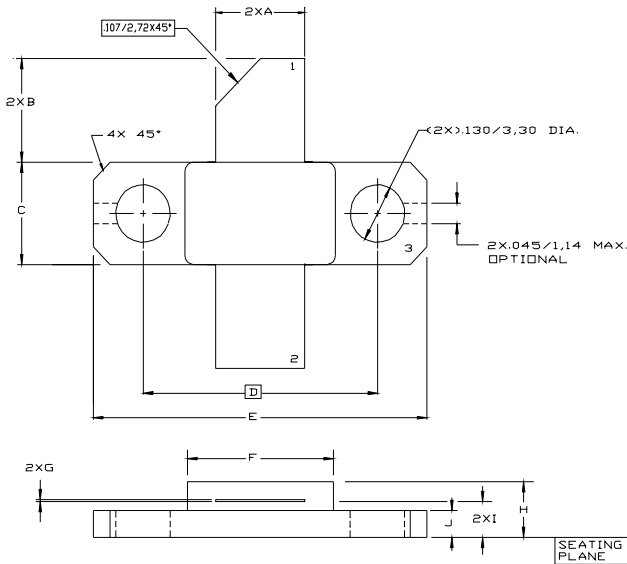


IMD3 vs. Output Power



M243 (.230 x .360 2L N/HERM W/FLG) MECHANICAL DATA

DIM.	mm			Inch		
	MIN.	TYP.	MAX	MIN.	TYP.	MAX
A	5.21		5.72	0.205		0.225
B	5.46		6.48	0.215		0.255
C	5.59		6.10	0.220		0.240
D		14.27			0.562	
E	20.07		20.57	0.790		0.810
F	8.89		9.40	0.350		0.370
G	0.10		0.15	0.004		0.006
H	3.18		4.45	0.125		0.175
I	1.83		2.24	0.072		0.088
J	1.27		1.78	0.050		0.070



Controlling dimension: Inches

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