



# ST19GF34

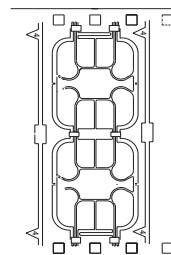
## Smartcard MCU

### With 34 KBytes EEPROM

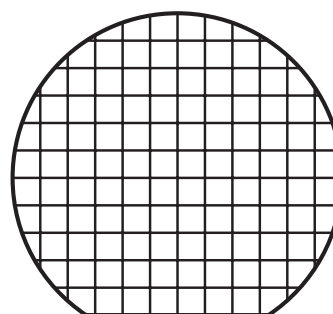
DATA BRIEFING

#### ST19GF34 FEATURES:

- ENHANCED 8 BIT CPU WITH EXTENDED ADDRESSING MODES
- USER ROM WITH PARTITIONING
- 56 K BYTES OF SYSTEM ROM
- 1984BYTES OF USER RAM WITH PARTITIONING
- 34 K BYTES OF USER EEPROM WITH PARTITIONING
  - Highly reliable CMOS EEPROM submicron technology
  - 10 years data retention
  - 100,000 Erase/Write cycles endurance
  - Separate Write and Erase cycles for fast "1" programming
  - 1 to 64 bytes Erase or Program
  - Full support of GSM Toolkit and Javacard implementation
- SECURITY FIREWALLS FOR MEMORIES
- VERY HIGH SECURITY FEATURES INCLUDING EEPROM FLASH PROGRAM
- 8 BIT TIMER WITH INTERRUPT CAPABILITY
- 2 SERIAL ACCESS, ISO 7816-3 COMPATIBLE
- $3V \pm 10\%$  or  $5V \pm 10\%$  SUPPLY VOLTAGE
- POWER SAVING STANDBY MODE
- CONTACT ASSIGNMENT COMPATIBLE ISO 7816-2
- ESD PROTECTION GREATER THAN 5000V



Micromodule (D4)



Wafer

**HARDWARE DESCRIPTION**

The ST19GF34, a member of the ST19 device family, is a serial access microcontroller especially designed for very large volume and cost competitive secure portable objects.

The ST19GF34 is based on a STMicroelectronics 8 bit CPU core including on-chip memories: 1984 Bytes of RAM, 56 K Bytes of USER ROM and 34 K Bytes of EEPROM.

RAM, ROM and EEPROM memories can be configured into partitions. Access rules from any memory partition to another partition are setup by the user defined Memory Access Control Logic.

It is manufactured using the highly reliable ST technology.

As all other ST19 family members, it is fully compatible with the ISO standards for Smartcard applications.

**SOFTWARE DESCRIPTION**

Software development and firmware (ROM code/options) generation are completed by the ST16-19 HDSE development system.

**Figure 1. Block Diagram**

