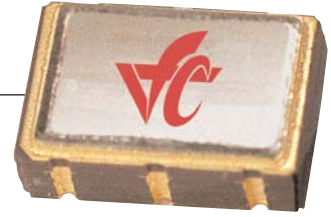


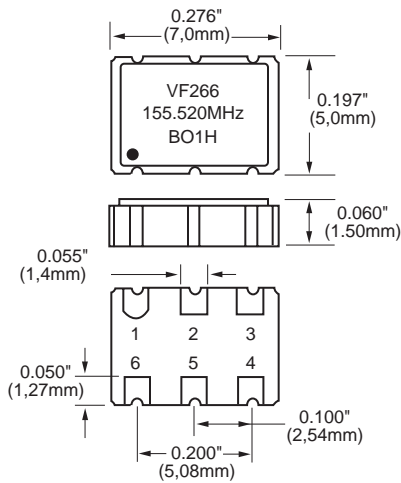
VF266

LVPECL Compatible With Standby Function Crystal Clock Oscillator



FEATURES

- Wide Frequency Range
- EMI Shielded
- Tight Duty Cycle Available
- Industrial Temperature Range (-40°C to +85°C) Available
- Miniature Ceramic Package
- Industry Standard Footprint
- Complementary Output



All dimensions are typical unless otherwise specified.

Creating a Part Number

VF266 [] - [] - **FREQ.**

FREQUENCY STABILITY	
Code	Specification
B	±50 ppm
	±100 ppm (std.)

OPERATIONAL TEMP. RANGE	
Code	Specification
	0 C to +70 C (std.)
1	-40 C to +85 C *

Symmetry	
Code	Specification
H	±5.0%
	±10% (std.)

Example: VF266BH-1-155.52MHz: Frequency Stability ±50ppm, Symmetry ±5.0%, Operating Temperature -40 C to +85 C, Frequency 155.52 MHz.

	Parameter	Symb	Condition	Min	Typ	Max	Unit	Note
Absolute Max. Ratings	Input Break Down Voltage	Vcc		-0.5		7.0	V	
	Storage Temp.	Ts		-55		+125	°C	
Electrical	Frequency Range	F		75		250*	MHz	
	Frequency Stability	ΔF/F	Overall	-100		±100	ppm	1
	Input Voltage	Vcc		3.15	3.30	3.45	V	
	Input Current	Icc	Loaded		55	88	mA	
	Output Load	RL	Terminated to Vcc-2V	50 Ohm				
	Duty Cycle		@ 50% Vcc	40	50	60	%	std.
	Rise/Fall Time	Tr/Tf	20% to 80%		0.5	1.0	ns	
	Logic "1" Level	Voh	@Vcc = 3.3V	2.275	2.350	2.420	V	
	Logic "0" Level	Vol	@Vcc = 3.3V	1.490	1.600	1.680	V	
	Output enable time	OE	Ta=25°C Vcc-2V to 50Ω			200	ns	
	Standby function		Outputs High impedance			0.3Vcc	V	Pin #1
	Phase Jitter		1σ			1	ps	fj>1KHz
Environmental and Mechanical	Operating Temperature Range		0°C to +70°C (-40°C to +85°C *consult VF for availability)					
	Mechanical Shock		Per MIL-STD-202, Method 213, Cond. E					
	Thermal Shock		Per MIL-STD-883, Method 1011, Cond. A					
	Vibration		Per MIL-STD-883, Method 2007, Cond. A					
	Soldering Conditions		260°C, for 10s, Max or 230°C, for 90s, Max.					
	Hermetic Seal		Leak rate less than 5 x 10 ⁻⁸ atm.cc/s of helium					
Electrical Connections	Pin Out	Pin #1-OE Pin #2-NC Pin #3-gnd			Pin #4-Output true Pin#5-Output Complementary Pin#6-Vcc			

Notes: 1. Tighter frequency stability available.

When OE goes LOW, the oscillator stops and the output pins become high impedance.

All specifications are subject to change without notice.

Oct. 3, 2003